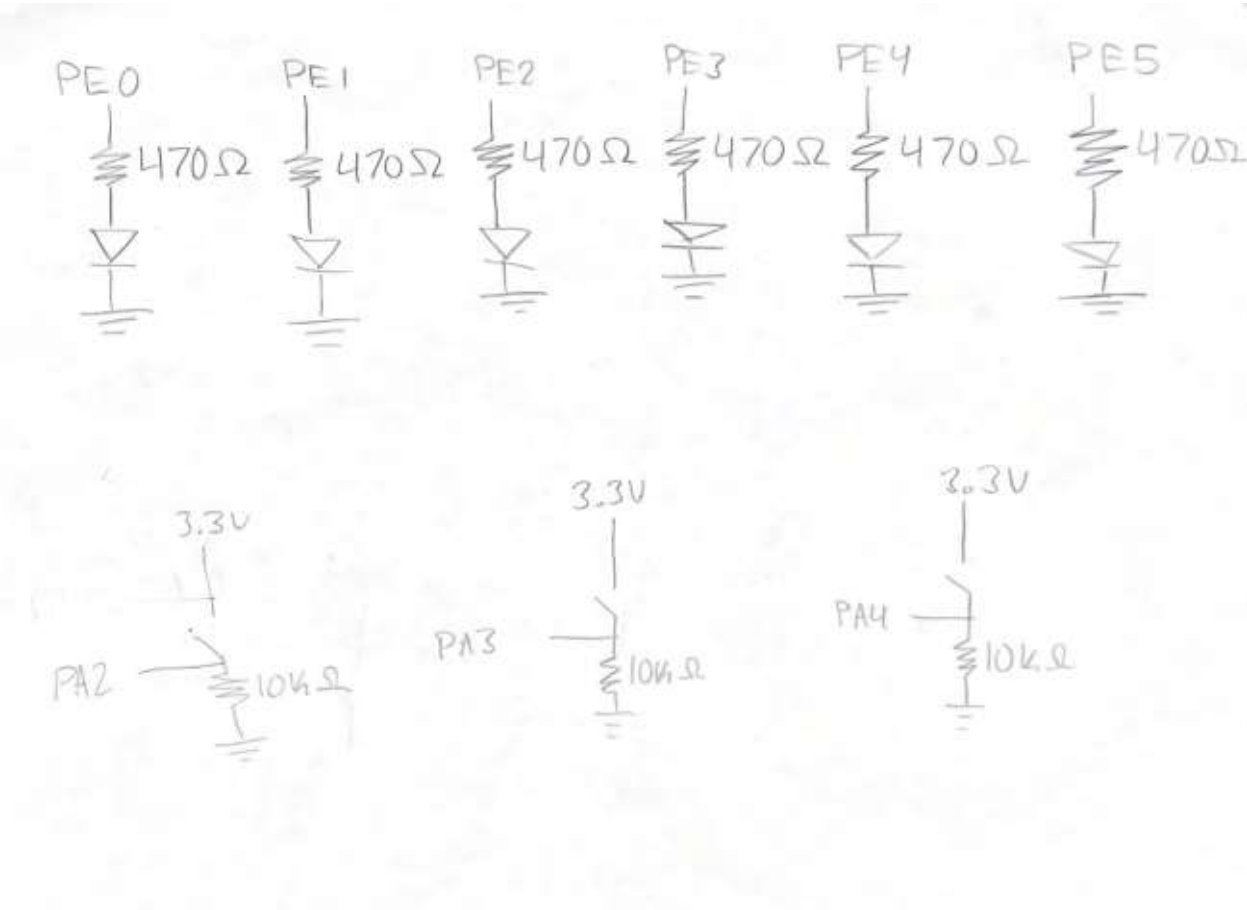
Hyokwon Chung (HC27426)

Daniel Davis (DND663)

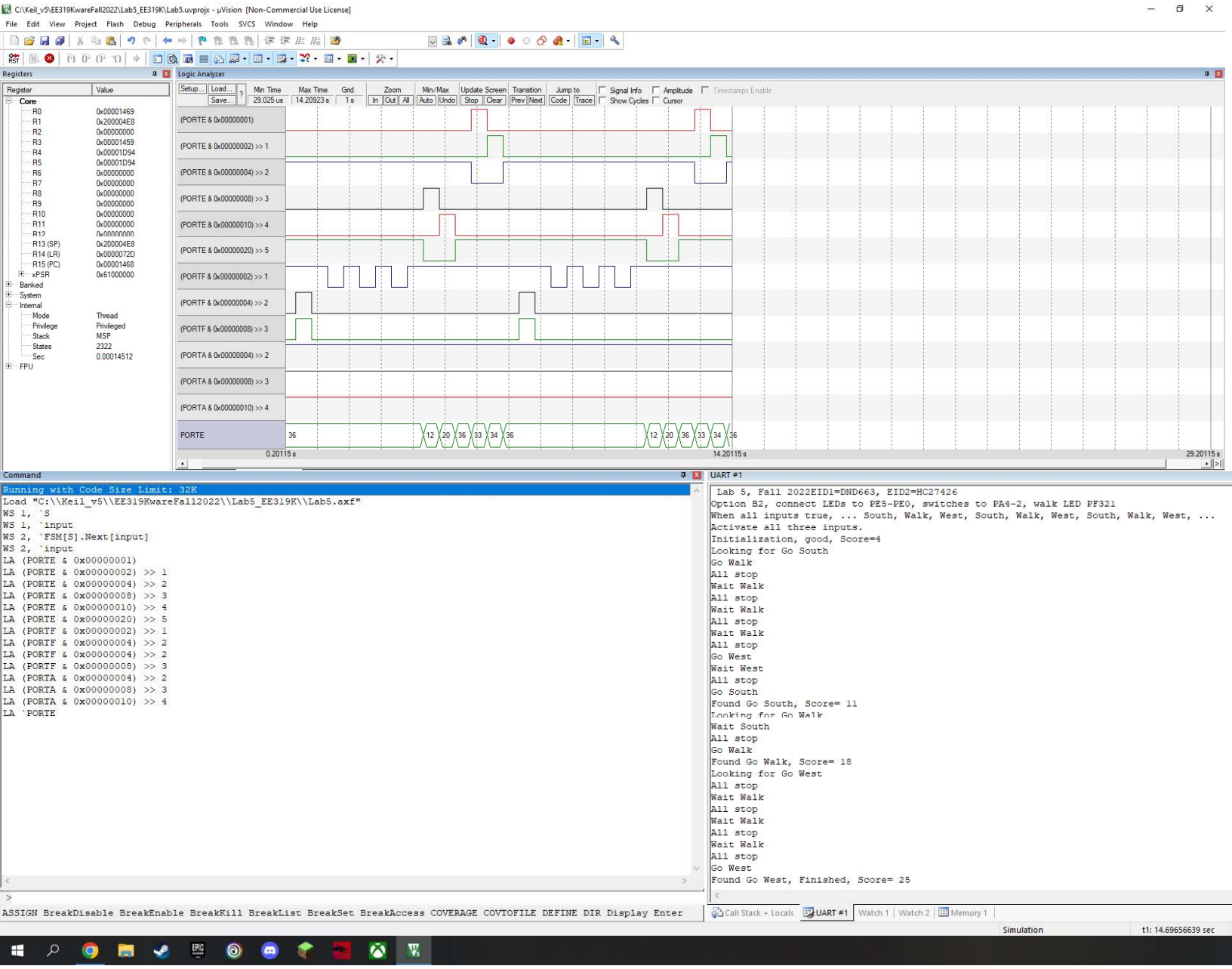
Valvano

Lab 5 Deliverables

2. Circuit Diagram



3. Logic analyzer and Graded code with EID



4. FSM Graph