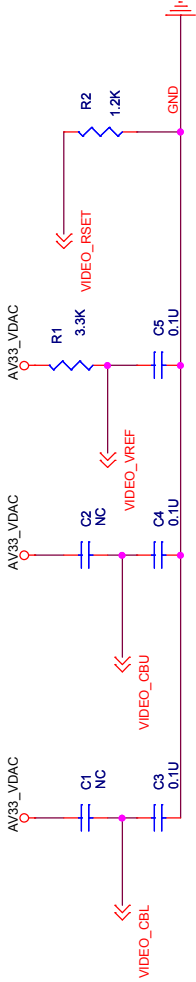
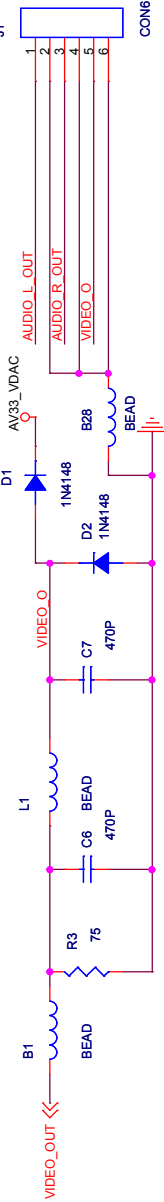


TV OUT

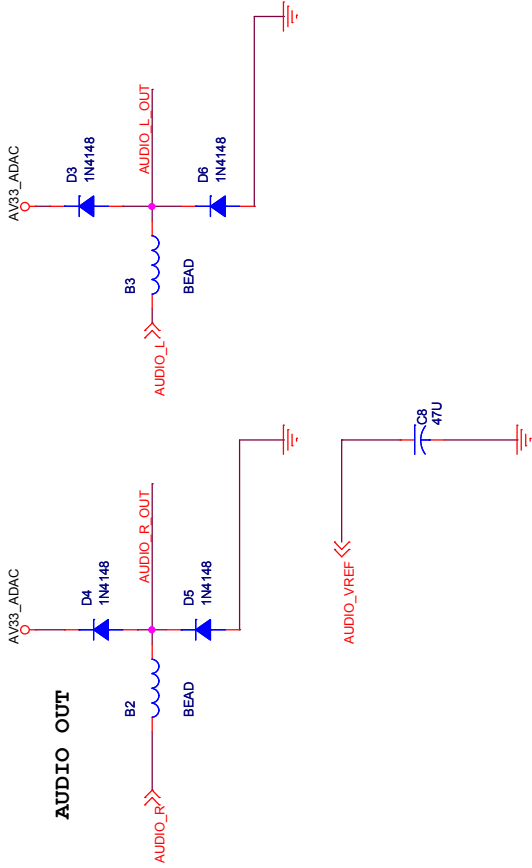
Please place this circuit close to the SPG290 chip.



Please place this circuit close to the VIDEO out connector.



AUDIO OUT



Matei Inc Design by Daniel Stelung

Title TV OUT

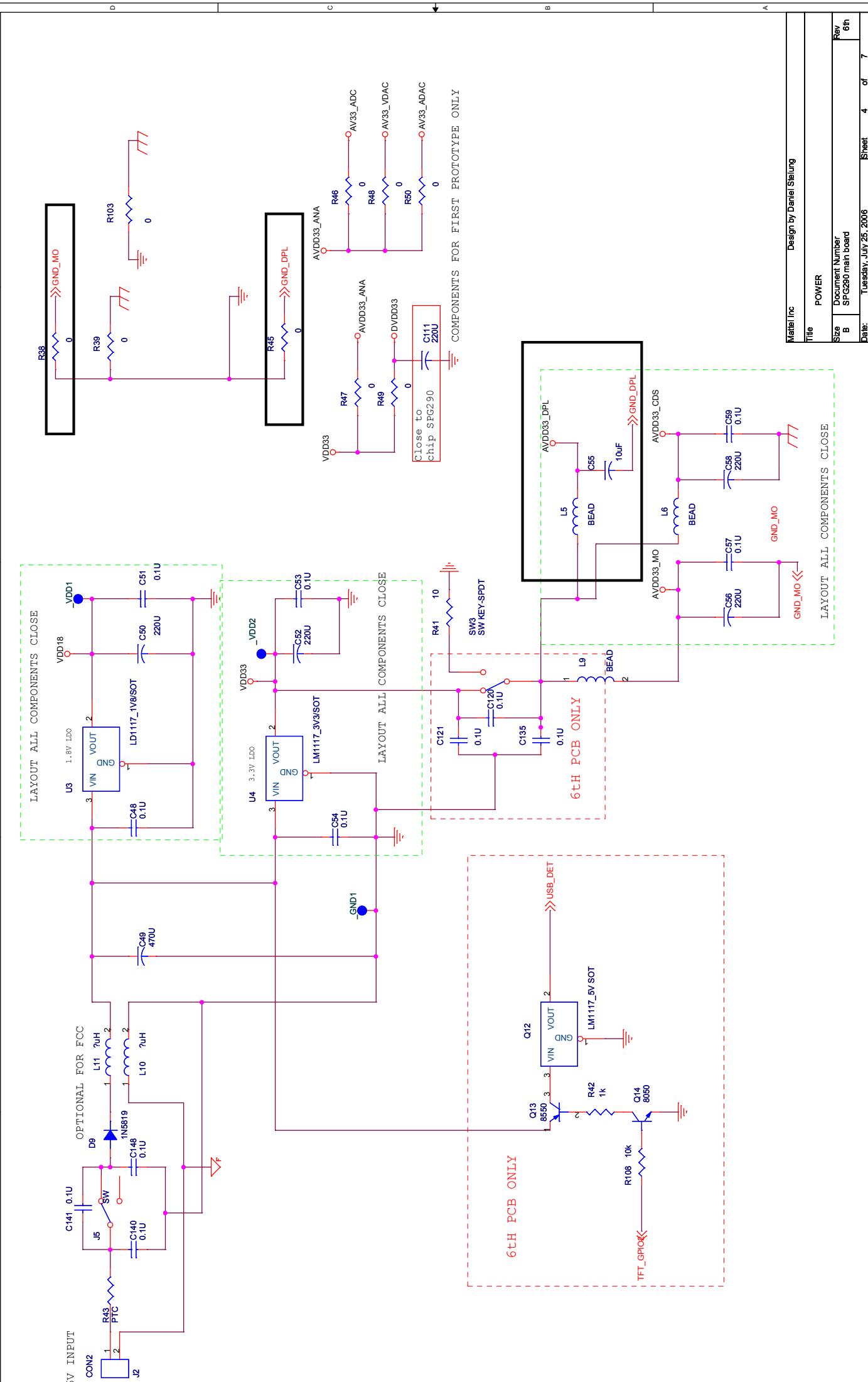
Size B Document Number SPG290 main board Rev 0H

Date: Tuesday, July 25, 2006

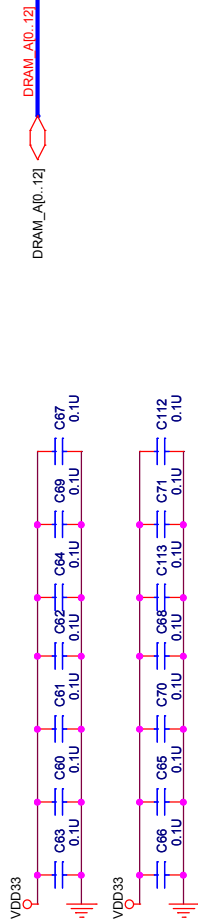
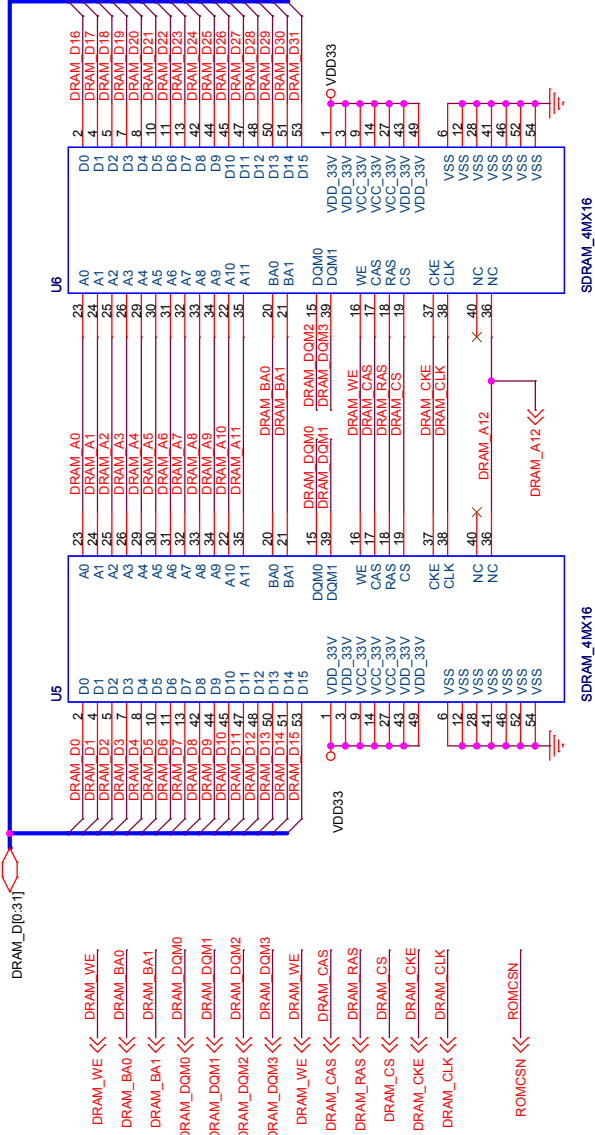
Sheet 1 of 7







Mater Inc		Design by Daniel Stieling	
Title		POWER	
Size	Document Number	Rev	6th
B	SPG290 main board		
Date:	Tuesday, July 25, 2006	Sheet	4 of 7



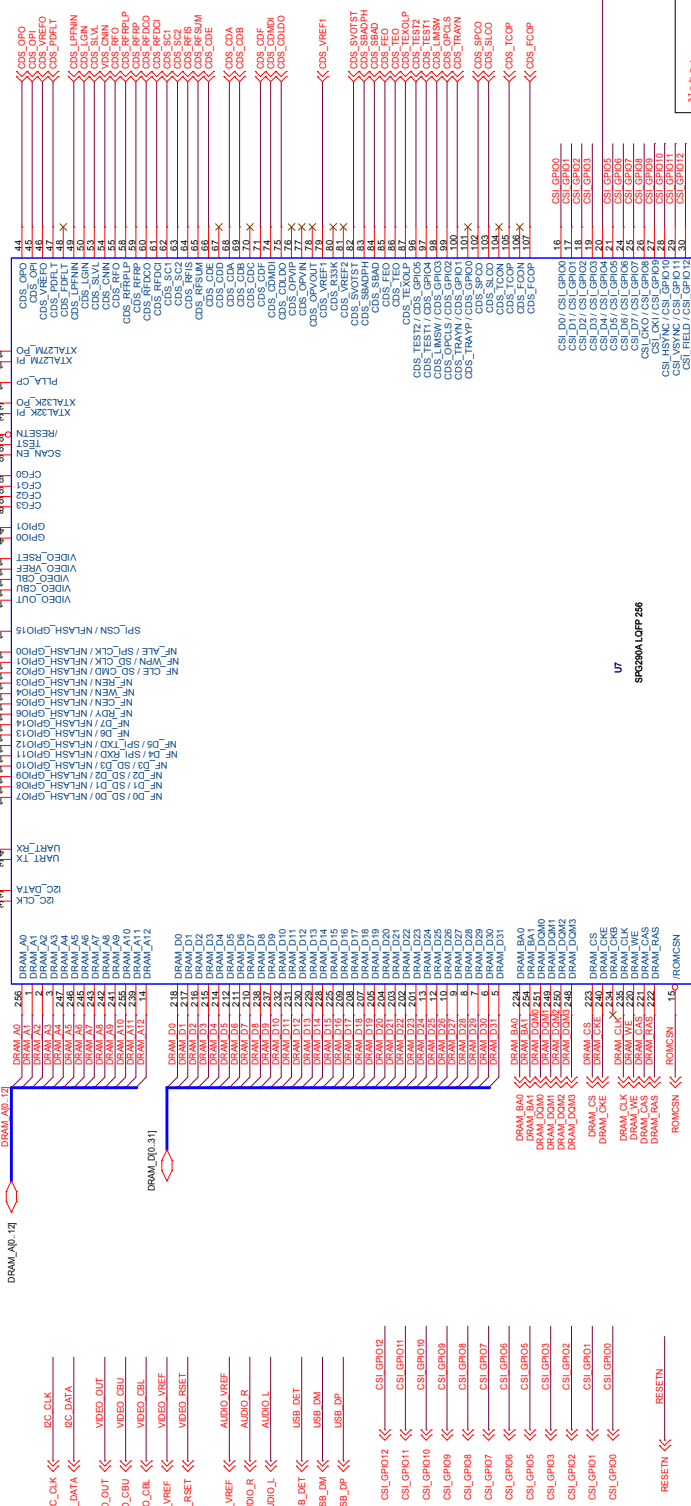
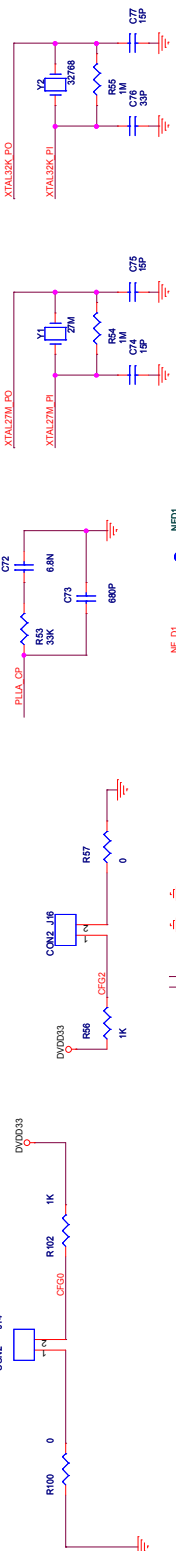
Mattel Inc Design by Daniel Stelung

Title DRAM

Size B Document Number SP6230 main board Rev 6th

Date: Tuesday, July 25, 2006 Sheet 5 of 7

About CFG1:  
In this part, add three Jumper J14, J16  
to select different mode.  
CFG3 always low.  
Default all CFG pins were pulled high.



Note:  
LCD\_D0= pull high,  
LCD\_D1= pull low,  
the bank of  
SDRAW is 2048.

