Problem Set 1

Part 1 Overview

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여기에서 제시되는 문제들은 교재의 연습문제(각 장의 마지막에 수록됨)에서 발췌했음. 이 문제들은 각자의 자습용으로 활용하기 바람. 2015년 1학기 문제와 같음

1. Introduction

- 1.2 The issue of resource utilization shows up in different forms in different types of operating systems. List what resources must be managed carefully in the following settings:
 - a. Mainframe or minicomputer systems
 - b. Workstations connected to servers
 - c. Mobile computers
- 1.8 What is the purpose of interrupts? How does an interrupt differ from a trap? Can traps be generated intentionally by a user program? If so, for what purpose?
- 1.9 Direct memory access is used for high-speed I/O devices in order to avoid increasing the CPU's execution load.
 - a. How does the CPU interface with the device to coordinate the transfer?
 - b. How does the CPU know when the memory operations are complete?
 - c. The CPU is allowed to execute other programs while the DMA controller is transferring data. Does this process interfere with the execution of the user programs? If so, describe what forms of interference are caused.
- 1.12 Consider an SMP system similar to the one shown in Figure 1.6. Illustrate with an example how data residing in memory could in fact have a different value in each of the local caches.

2. System Structures

- 2.2 Describe three general methods for passing parameters to the operating system.
- 2.8 Why is the separation of mechanism and policy desirable?
- 2.10 What is the main advantage of the microkernel approach to system design? How do user programs and system services interact in a microkernel architecture? What are the disadvantages of using the microkernel approach?
- 2.13 Explain why Java programs running on Android systems do not use the standard Java API and virtual machine.

3. Process Concept

- 3.2 Describe the actions taken by a kernel to context-switch between processes.
- 3.5 Including the initial parent process, how many processes are created by the program shown in Figure 3.31?
- 3.6 Explain the circumstances under which the line of code marked printf("LINE J") in Figure 3.32 will be reached.
- 3.10 Using the program shown in Figure 3.34, explain what the output will be at lines X and Y.
- 3.11 What are the benefits and the disadvantages of each of the following? Consider both the system level and the programmer level.
 - a. Synchronous and asynchronous communication
 - b. Automatic and explicit buffering
 - c. Send by copy and send by reference
 - d. Fixed-sized and variable-sized messages

```
int main()
          int i;
         for (i = 0; i < 4; i++)
            fork();
                                     Figure 3.31
          return 0;
#include <sys/types.h>
#include <stdio.h>
#include <unistd.h>
int main()
pid_t pid;
   /* fork a child process */
   pid = fork();
   if (pid < 0) { /* error occurred */
      fprintf(stderr, "Fork Failed");
      return 1;
   else if (pid == 0) { /* child process */
      execlp("/bin/ls","ls",NULL);
     printf("LINE J");
   else { /* parent process */
      /* parent will wait for the child to complete */
      wait(NULL);
     printf("Child Complete");
                                      Figure 3.32
   return 0;
```

#include <stdio.h>
#include <unistd.h>

```
#include <sys/types.h>
#include <stdio.h>
#include <unistd.h>
#define SIZE 5
int nums[SIZE] = \{0,1,2,3,4\};
int main()
int i;
pid_t pid;
  pid = fork();
  if (pid == 0) {
     for (i = 0; i < SIZE; i++) {
       nums[i] *= -i;
       printf("CHILD: %d ",nums[i]); /* LINE X */
  else if (pid > 0) {
     wait(NULL);
     for (i = 0; i < SIZE; i++)
       printf("PARENT: %d ",nums[i]); /* LINE Y */
  return 0;
```

Figure 3.34

4. Multithreaded Programming

- 4.2 Under what circumstances does a multithreaded solution using multiple kernel threads provide better performance than a single-threaded solution on a single-processor system?
- 4.9 A system with two dual-core processors has four processors available for scheduling. A CPU-intensive application is running on this system. All input is performed at program start-up, when a single file must be opened. Similarly, all output is performed just before the program terminates, when the program results must be written to a single file. Between startup and termination, the program is entirely CPU bound. Your task is to improve the performance of this application by multithreading it. The application runs on a system that uses the one-to-one threading model (each user thread maps to a kernel thread).
 - How many threads will you create to perform the input and output? Explain.
 - How many threads will you create for the CPU-intensive portion of the application? Explain.
- 4.11 As described in Section 4.7.2, Linux does not distinguish between processes and threads. Instead, Linux treats both in the same way, allowing a task to be more akin to a process or a thread depending on the set of flags passed to the clone() system call. However, other operating systems, such as Windows, treat processes and threads differently. Typically, such systems use a notation in which the data structure for a process contains pointers to the separate threads belonging to the process. Contrast these two approaches for modeling processes and threads within the kernel.

4.12 The program shown in Figure 4.16 uses the Pthreads API. What would be the output from the program at LINE C and LINE P?

```
#include <pthread.h>
#include <stdio.h>
#include <types.h>
int value = 0;
void *runner(void *param); /* the thread */
int main(int argc, char *argv[])
pid_t pid;
pthread_t tid;
pthread_attr_t attr;
  pid = fork();
  if (pid == 0) { /* child process */
     pthread_attr_init(&attr);
     pthread_create(&tid,&attr,runner,NULL);
     pthread_join(tid,NULL);
     printf("CHILD: value = %d", value); /* LINE C */
  else if (pid > 0) { /* parent process */
     wait(NULL);
     printf("PARENT: value = %d",value); /* LINE P */
void *runner(void *param) {
  value = 5;
  pthread_exit(0);
```

Figure 4.16

5. Process Scheduling

- 5.1 Why is it important for the scheduler to distinguish I/O-bound programs from CPU-bound programs?
- 5.4 We have discussed possible race conditions on various kernel data structures. Most scheduling algorithms maintain a *run queue*, which lists processes eligible to run on a processor. On multicore systems, there are two general options: (1) each processing core has its own run queue, or (2) a single run queue is shared by all processing cores. What are the advantages and disadvantages of each of these approaches?
- 5.8 The following processes are being scheduled using a preemptive, round-robin scheduling algorithm. Each process is assigned a numerical priority, with a higher number indicating a higher relative priority. In addition to the processes listed below, the system also has an *idle task* (which consumes no CPU resources and is identified as P_{idle}). This task has priority 0 and is scheduled whenever the system has no other available processes to run. The length of a time quantum is 10 units. If a process is preempted by a higher-priority process, the preempted process is placed at the end of the queue.
 - a. Show the scheduling order of the processes using a Gantt chart.
 - b. What is the turnaround time for each process?
 - c. What is the waiting time for each process?
 - d. What is the CPU utilization rate?

Thread	Priority	Burst	Arrival
P_1	40	20	0
P_2	30	25	25
P_3	30	25	30
P_4	35	15	60
P_5	5	10	100
P_6	10	10	105

- 5.11 Consider a variant of the RR scheduling algorithm in which the entries in the ready queue are pointers to the PCBs.
 - a. What would be the effect of putting two pointers to the same process in the ready queue?
 - b. What would be two major advantages and two disadvantages of this scheme?
 - c. How would you modify the basic RR algorithm to achieve the same effect without the duplicate pointers?
- 5.14 Consider a preemptive priority scheduling algorithm based on dynamically changing priorities. Larger priority numbers imply higher priority. When a process is waiting for the CPU (in the ready queue, but not running), its priority changes at a rate α . When it is running, its priority changes at a rate β . All processes are given a priority of 0 when they enter the ready queue. The parameters α and β can be set to give many different scheduling algorithms.
 - a. What is the algorithm that results from $\beta > \alpha > 0$?
 - b. What is the algorithm that results from $\alpha < \beta < 0$?

- 5.19 Assume that two tasks A and B are running on a Linux system. The nice values of A and B are-5 and +5, respectively. Using the CFS scheduler as a guide, describe how the respective values of vruntime vary between the two processes given each of the following scenarios:
 - Both A and B are CPU-bound.
 - A is I/O-bound, and B is CPU-bound.
 - A is CPU-bound, and B is I/O-bound.
- 5.20 Discuss ways in which the priority inversion problem could be addressed in a real-time system. Also discuss whether the solutions could be implemented within the context of a proportional share scheduler.
- 5.22 Consider two processes, P_1 and P_2 , where p_1 = 50, t_1 = 25, p_2 = 75, and t_1 = 30.
 - a. Can these two processes be scheduled using rate-monotonic scheduling? Illustrate your answer using a Gantt chart such as the ones in Figure 5.16 Figure 5.19.
 - b. Illustrate the scheduling of these two processes using earliest-deadline-first (EDF) scheduling.

6. Synchronization

- 6.1 Race conditions are possible in many computer systems. Consider a banking system that maintains an account balance with two functions: deposit(amount) and withdraw(amount). These two functions are passed the amount that is to be deposited or withdrawn from the bank account balance. Assume that a husband and wife share a bank account. Concurrently, the husband calls the withdraw() function and the wife calls deposit(). Describe how a race condition is possible and what might be done to prevent the race condition from occurring.
- 6.2 The first known correct software solution to the critical-section problem for two processes was developed by Dekker. The two processes, P₀ and P₁, share the following variables:

```
boolean flag[2]; /*
  initially false */
int turn;
```

The structure of process P_i (i == 0 or 1) is shown in Figure 6.21. The other process is P_j (j == 1 or 0). Prove that the algorithm satisfies all three requirements for the critical-section problem.

- 6.5 Explain why interrupts are not appropriate for implementing synchronization primitives in multiprocessor systems.
- 6.9 Consider how to implement a mutex lock using an atomic hardware instruction. Assume that the following structure defining the mutex lock is available:

```
typedef struct {
  int available;
  } lock;

(available == 0) indicates that the lock is available, and a value of 1
indicates that the lock is unavailable. Using this struct, illustrate how the
following functions can be implemented using the test_and_set() and
  compare_and_swap() instructions:
```

- void acquire(lock *mutex)
- void release(lock *mutex)

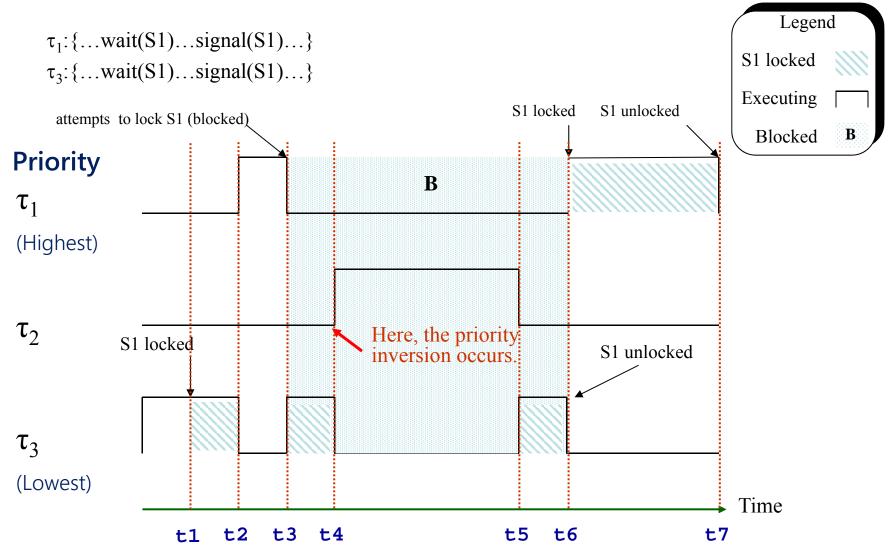
Be sure to include any initialization that may be necessary.

- 6.14 Consider the code example for allocating and releasing processes shown in Figure 6.23.
 - a. Identify the race condition(s).
 - b. Assume you have a mutex lock named mutex with the operations acquire() and release().
 Indicate where the locking needs to be placed to prevent the race condition(s).
 - c. Could we replace the integer variable
 int number of processes = 0
 with the atomic integer
 atomic t number of
 processes = 0
 to prevent the race condition(s)?

```
#define MAX_PROCESSES 255
int number_of_processes = 0;
/* the implementation of fork() calls this function */
int allocate_process() {
int new_pid;
  if (number_of_processes == MAX_PROCESSES)
      return -1;
  else {
      /* allocate necessary process resources */
      ++number_of_processes;
      return new_pid;
/* the implementation of exit() calls this function */
void release_process() {
   /* release process resources */
   --number_of_processes;
```

- 6.17 Show how to implement the wait() and signal() semaphore operations in multiprocessor environments using the test_and_set() instruction. The solution should exhibit minimal busy waiting.
- 6.20 Design an algorithm for a bounded-buffer monitor in which the buffers (portions) are embedded within the monitor itself.
- 6.25 Consider a system consisting of processes P₁, P₂, ..., P_n, each of which has a unique priority number. Write a monitor that allocates three identical printers to these processes, using the priority numbers for deciding the order of allocation.
- 6.28 Suppose we replace the wait() and signal() operations of monitors with a single construct await(B), where B is a general Boolean expression that causes the process executing it to wait until B becomes true.
 - a. Write a monitor using this scheme to implement the readers—writers problem.
 - b. Explain why, in general, this construct cannot be implemented efficiently.

- Priority inversion problem
 - Explain what is happening at t1 through t7 in the diagram shown below.



7. Deadlocks

- 7.1 Consider the traffic deadlock depicted in Figure 7.10.
 - a. Show that the four necessary conditions for deadlock hold in this example.
 - b. State a simple rule for avoiding deadlocks in this system.
- 7.8 Consider a system consisting of m resources of the same type being shared by n processes. A process can request or release only one resource at a time. Show that the system is deadlock free if the following two conditions hold:
 - a. The maximum need of each process is between one resource and m resources.
 - b. The sum of all maximum needs is less than m + n.

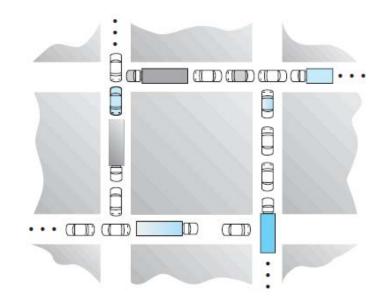


Figure 7.10

7.9 Consider the version of the dining-philosophers problem in which the chopsticks are placed at the center of the table and any two of them can be used by a philosopher. Assume that requests for chopsticks are made one at a time. Describe a simple rule for determining whether a particular request can be satisfied without causing deadlock given the current allocation of chopsticks to philosophers.

	Allocation	Max	Available
	ABCD	ABCD	ABCD
P_0	2001	4212	3321
P_1	3121	5252	
P_2	2103	2316	
P_3	1312	1424	
P_4	1432	3665	

7.13 Consider the following snapshot of a system:

Answer the following questions using the banker's algorithm:

- a. Illustrate that the system is in a safe state by demonstrating an order in which the processes may complete.
- b. If a request from process P₁ arrives for (1, 1, 0, 0), can the request be granted immediately?
- c. If a request from process P₄ arrives for (0, 0, 2, 0), can the request be granted immediately?