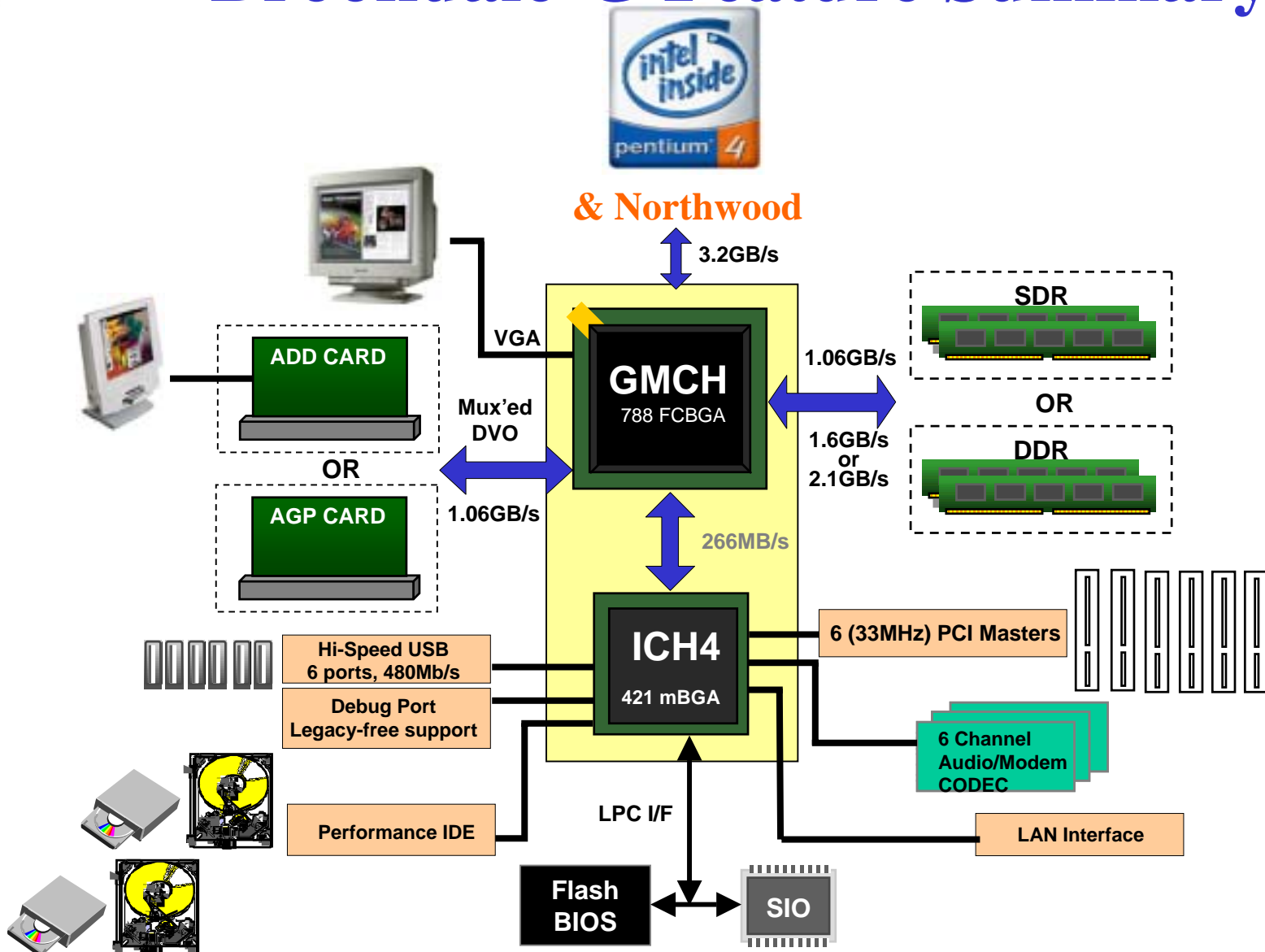


# AOS for Mother Board Solutions

May 2003



# Brookdale-G Feature Summary



# ATX POWER CONNECTOR & Voltage Tolerances

**Table 3. DC Output Voltage Regulation**

Output	Range	Min.	Nom.	Max.	Unit
+12VDC <sup>(1)</sup>	±5%	+11.40	+12.00	+12.60	Volts
+5VDC	±5%	+4.75	+5.00	+5.25	Volts
+3.3VDC	±5%	+3.14	+3.30	+3.47	Volts
-5VDC	±10%	-4.50	-5.00	-5.50	Volts
-12VDC	±10%	-10.80	-12.00	-13.20	Volts
+5VSB	±5%	+4.75	+5.00	+5.25	Volts

<sup>(1)</sup> At +12 VDC peak loading, regulation at the +12 VDC output can go to ± 10%.

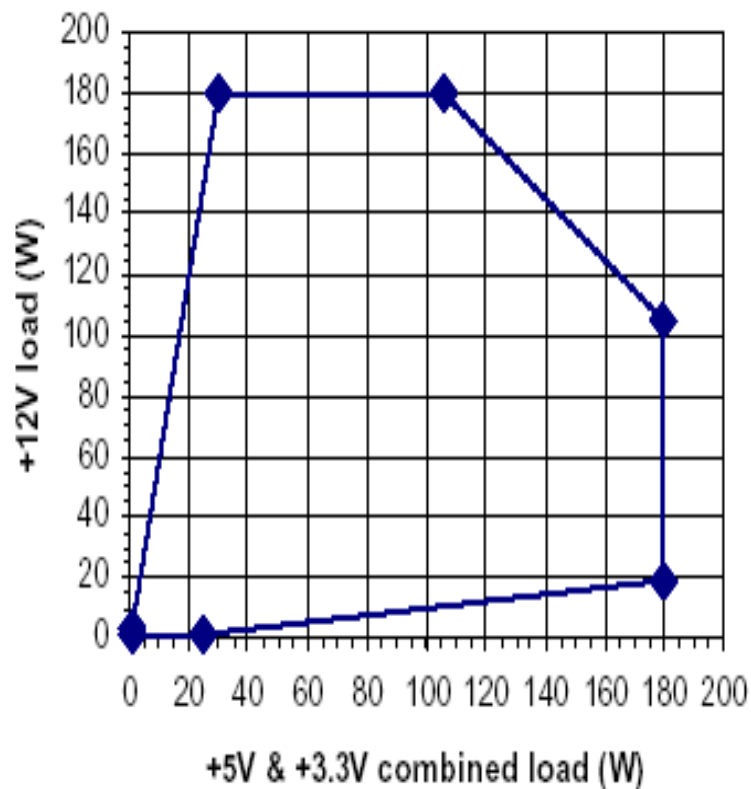
ALSO MAIN 3.3V SENSE)

3.3V	⑪ ①	3.3V
-12V	⑫ ②	3.3V
COM	⑬ ③	COM
PS-ON	⑭ ④	5V
COM	⑮ ⑤	COM
COM	⑯ ⑥	5V
COM	⑰ ⑦	COM
-5V	⑱ ⑧	PW-OK
5V	⑲ ⑨	5VSB
5V	⑳ ⑩	12V

# ATX Power Dissipation

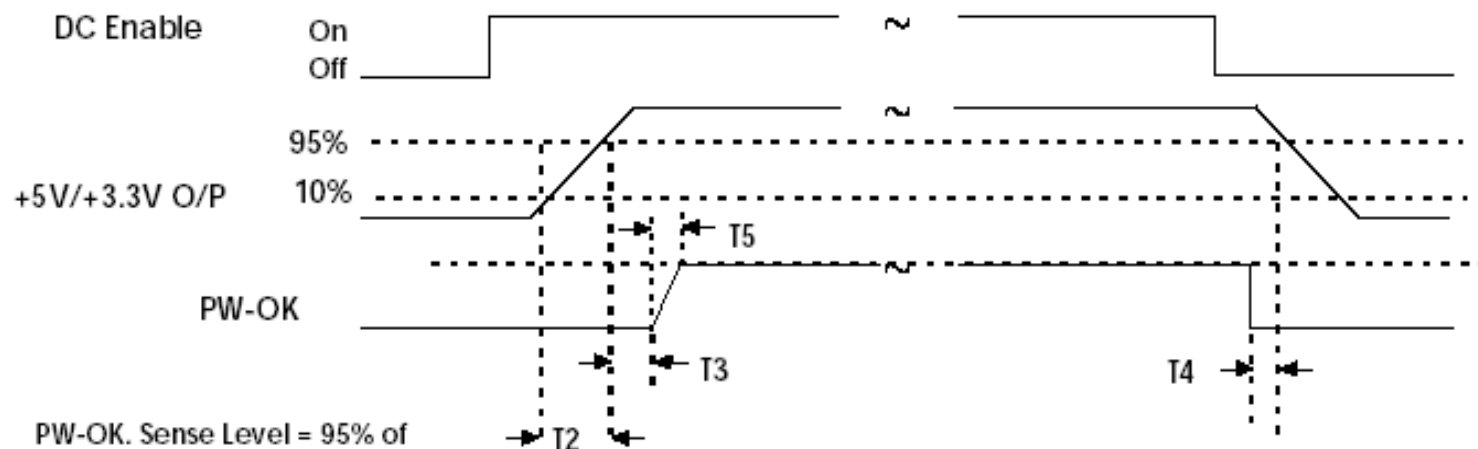
**Table 10. Typical Power Distribution for a 300 W ATX12V Configuration**

Output	Min. Current (amps)	Max. Current (amps)	Peak Current (amps)
+12 VDC	0.0	15.0	18.0
+5 VDC	0.3	30.0	
+3.3 VDC	0.3	28.0	
-12 VDC	0.0	0.8	
+5 VSB	0.0	2.0	2.5



See graph at right for power sharing.

# Timing of Power ON, Power OK



**Figure 12: Timing of PS-ON, PW-OK, and Germane Voltage Rails**

Although there is no requirement to meet specific timing parameters, the following signal timings are recommended:

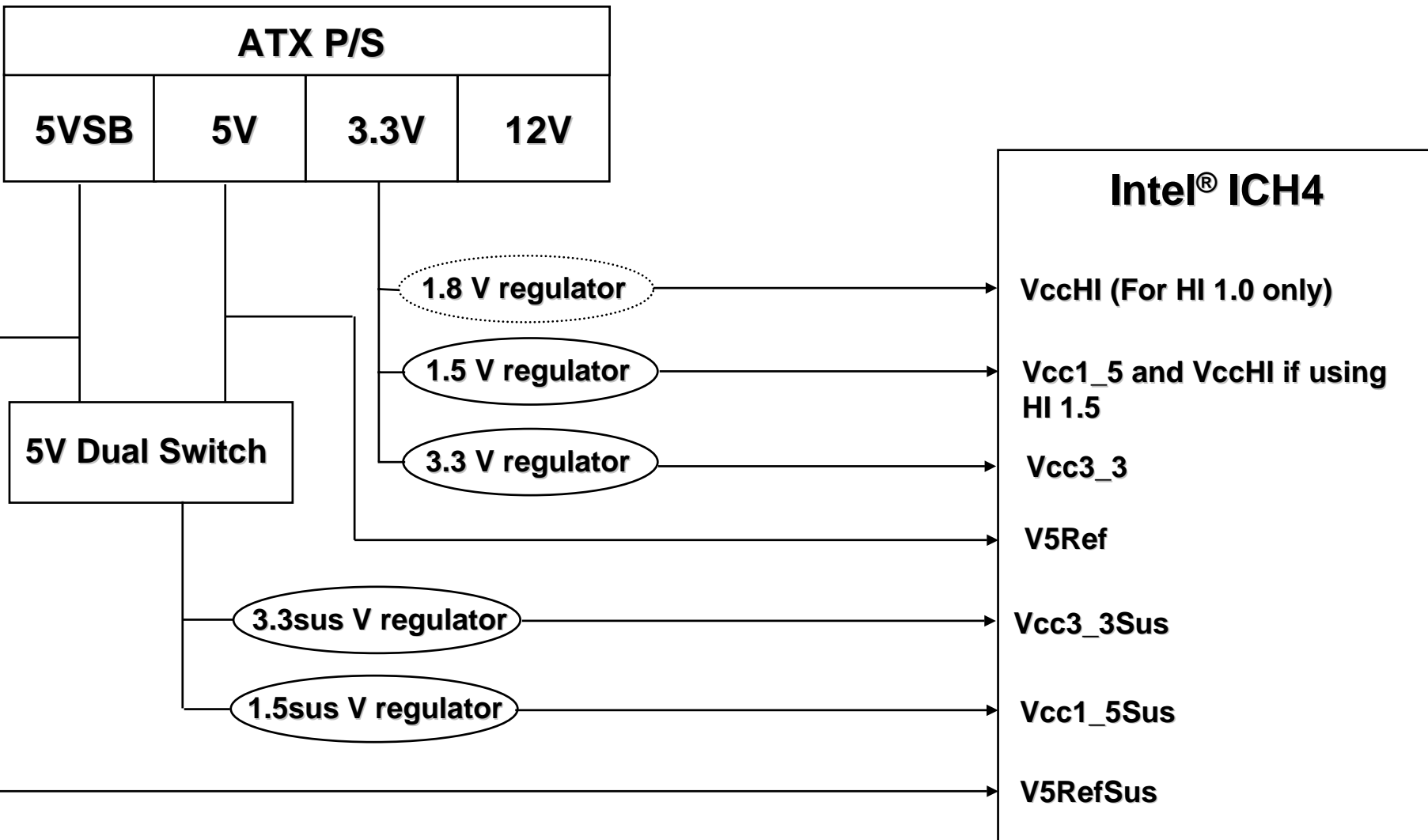
$$2\text{ms} \leq T_2 \leq 20\text{ ms}$$

$$100\text{ ms} < T_3 < 2000\text{ ms}$$

$$T_4 > 1\text{ ms}$$

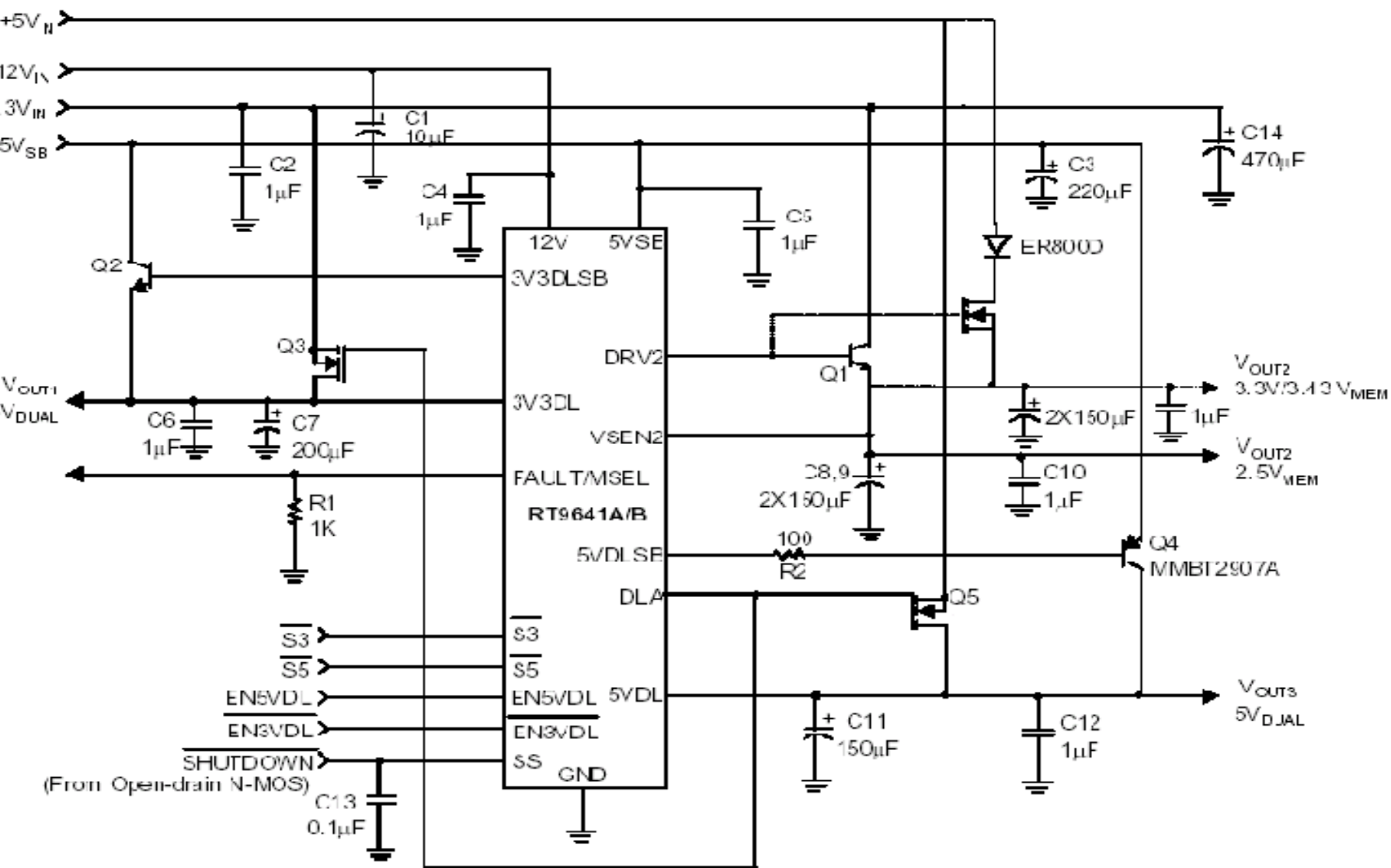
$$T_5 \leq 10\text{ms}$$

# Power Distribution



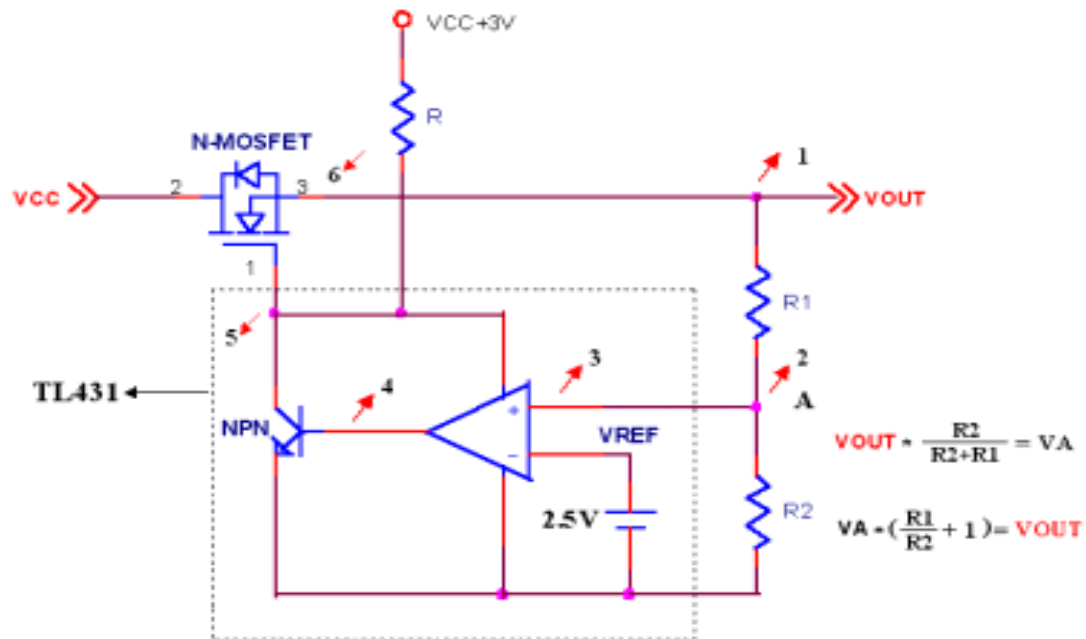


# ACPI Power Circuit

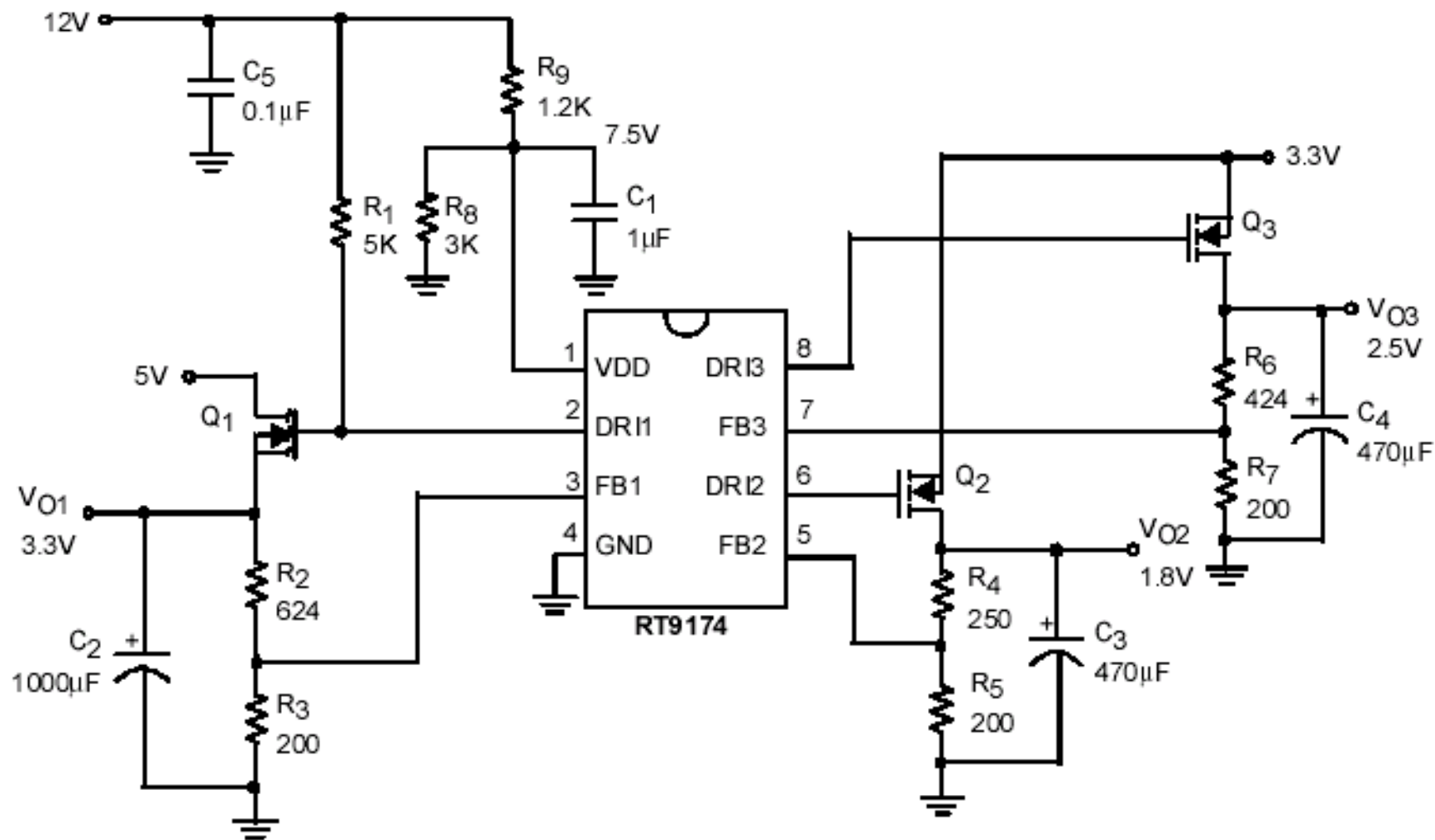




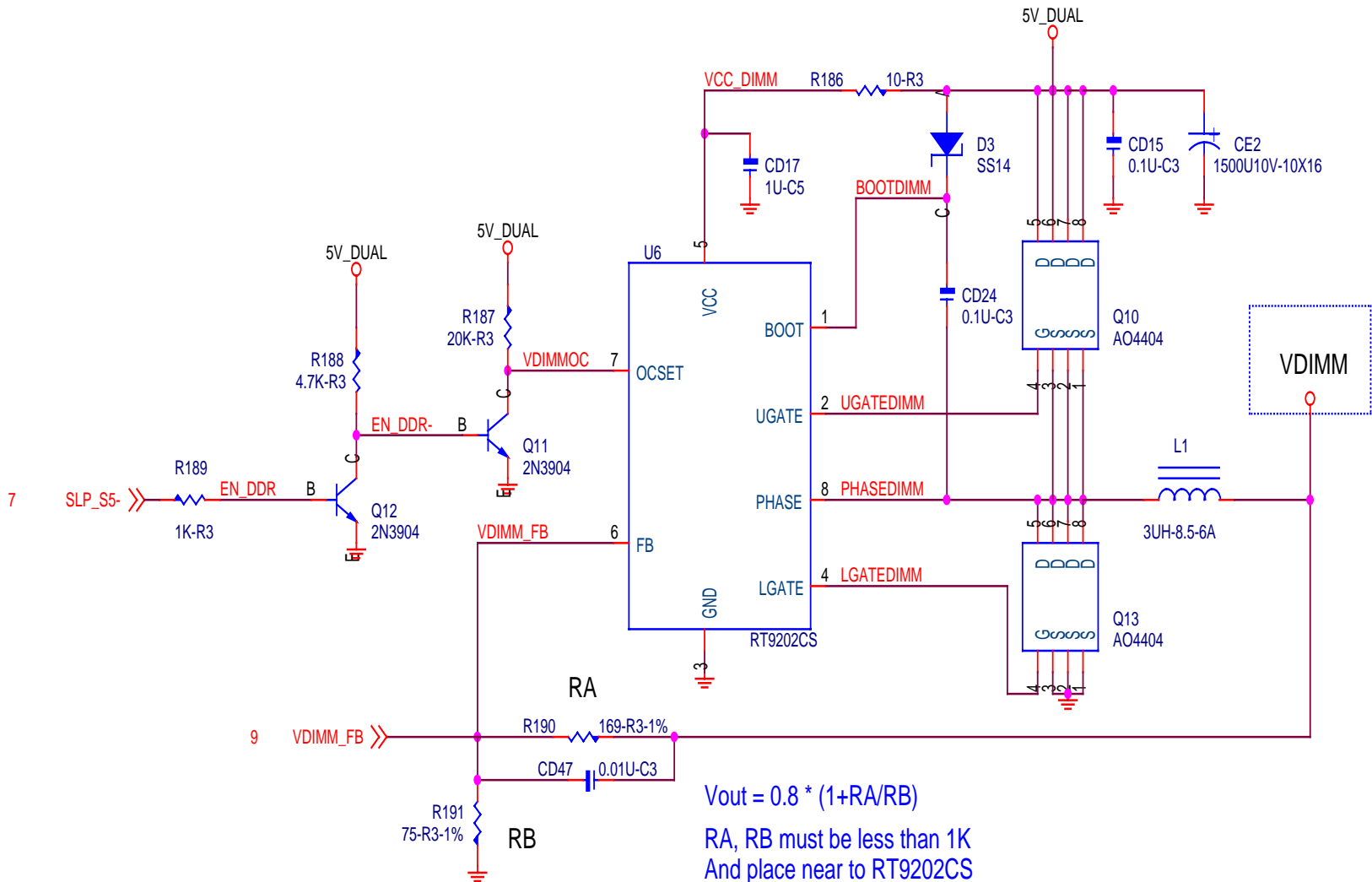
## 普遍的線性電路 (TL- 431 +3055 )



# Triple LDOs Driving MOS Application

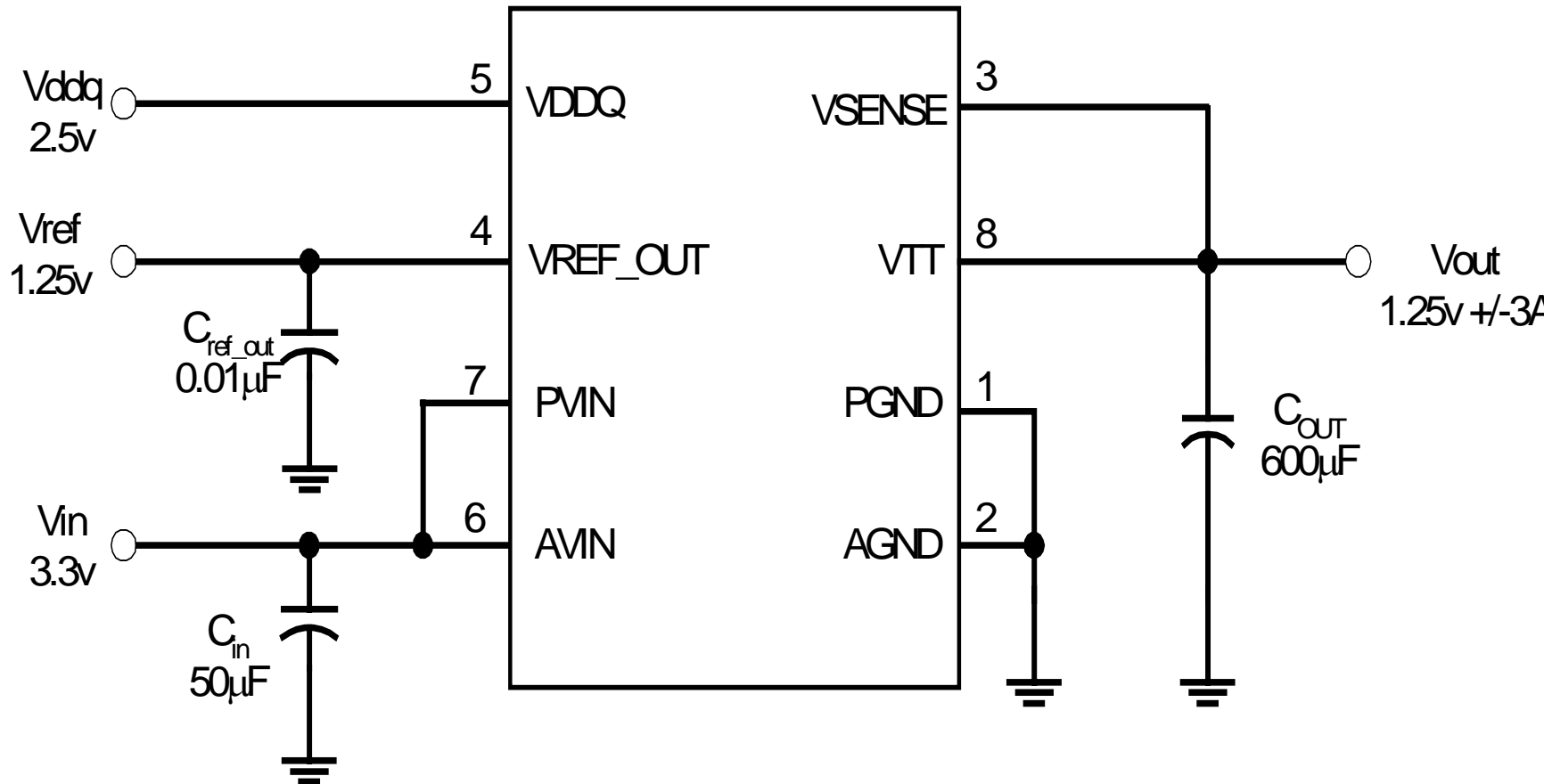


# DDR Voltage or VGA VDDQ For PWM Solution



# DDR Termination Voltage

## LP2995



# AGP Power supply limit

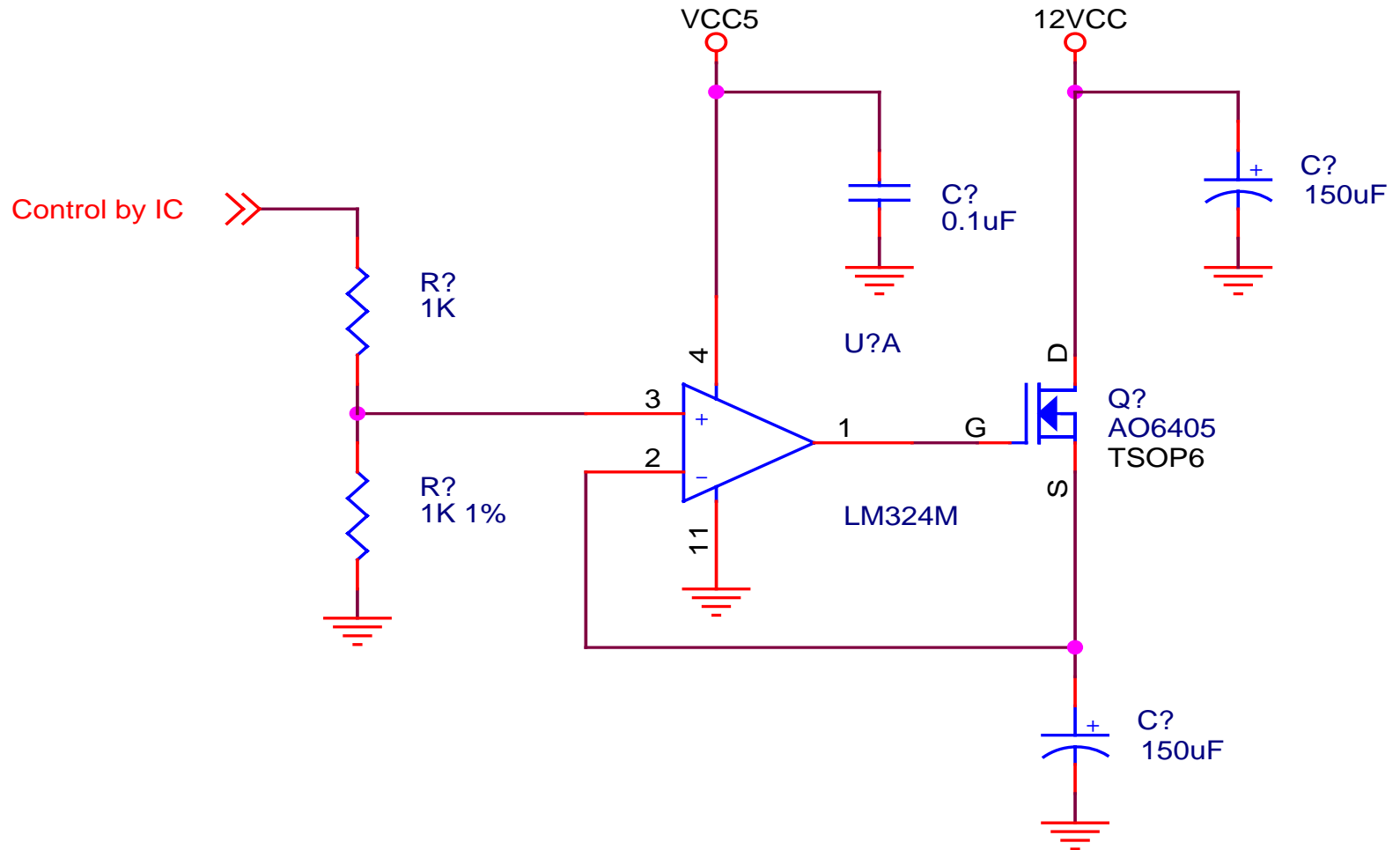
Table 4-14: Add-in Card Power Supply Limits

Symbol	Parameter	Condition	Min	Max	Units	Notes
Vddq1.5	I/O Supply Voltage	$I_{MAX} = 8.0 \text{ A}$	1.425	1.575	V	1
Vddq3.3	I/O Supply Voltage	$I_{MAX} = 8.0 \text{ A}$	3.15	3.45	V	1
VCC3.3	3.3 V Power Supply	$I_{MAX} = 6.0 \text{ A}$	3.15	3.45	V	
VCC5	5 V Power Supply	$I_{MAX} = 2.0 \text{ A}$	4.75	5.25	V	
VCC12	12 V Power Supply	$I_{MAX} = 1.0 \text{ A}$	11.4	12.6	V	

Note:

1. The Vddq current is due mostly to the AC switching transients of the A.G.P. I/O buffers. The maximum current listed will not be seen in practice, but represents the current carrying capability of the connector. Actual average currents will be less than 2.0 A.

# Fan Control Circuit



# TO-252 Efficiency Report 1

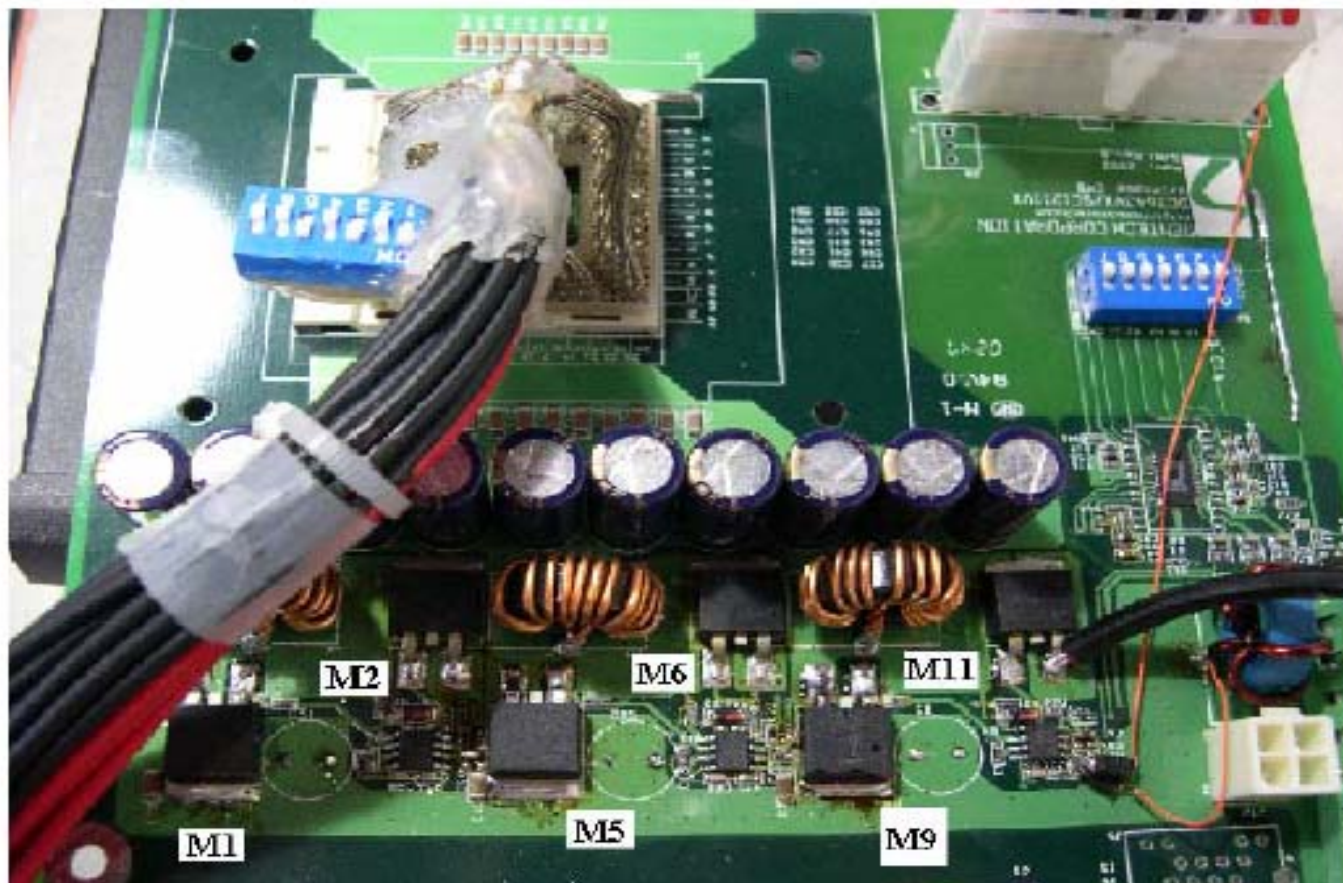


Fig1. Controller sc2643, Driver=sc1211. Three phase solution of SEMTECH.

# TO-252 Efficiency Report 2

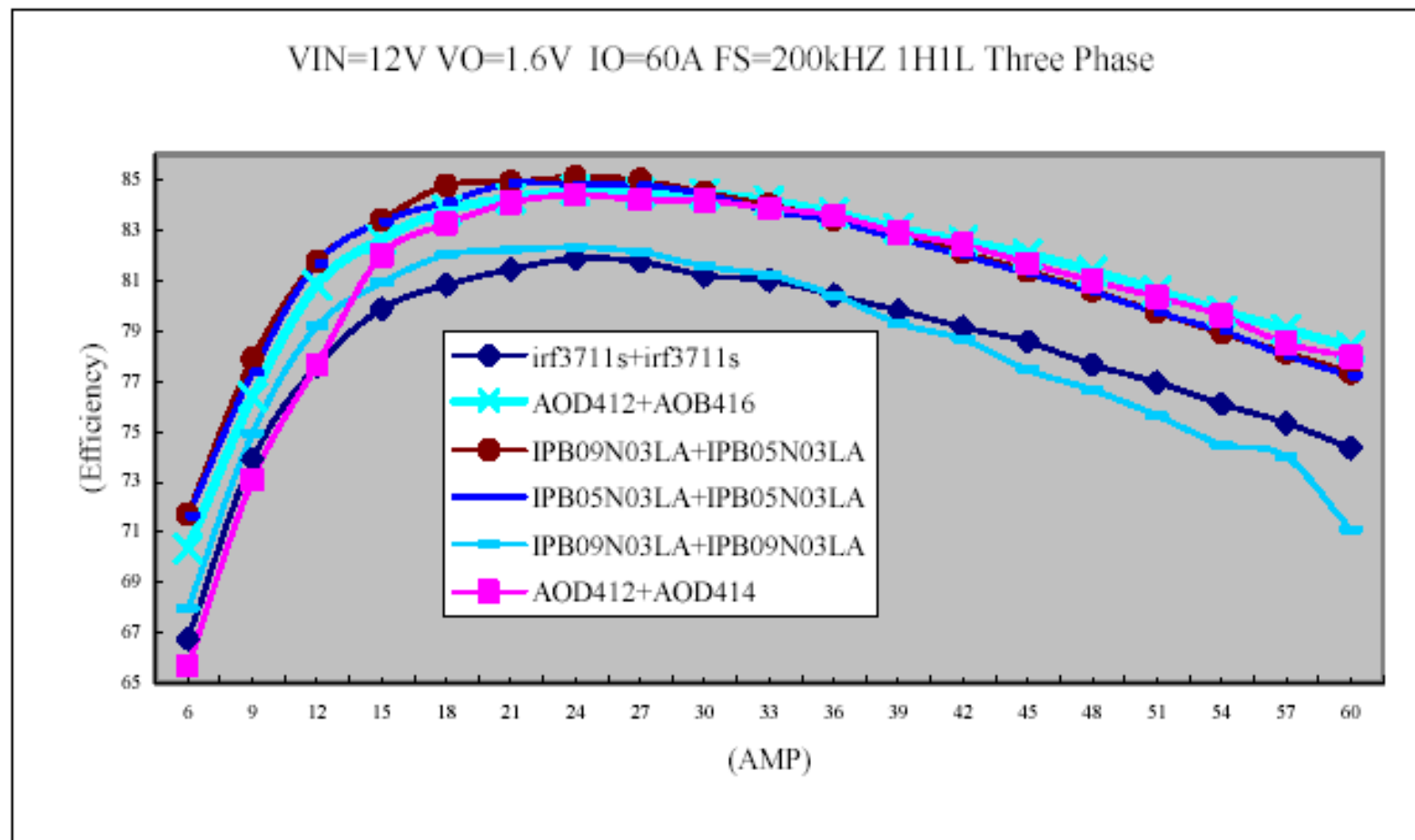


Fig2. Efficiency comparison



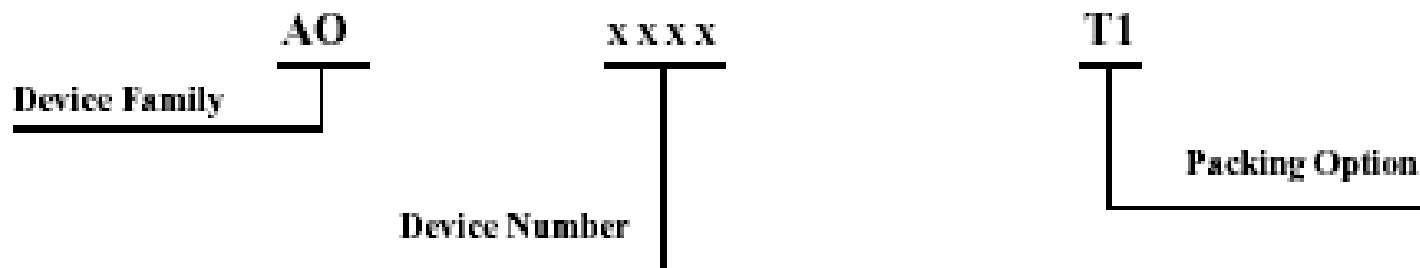
# TO-252 Efficiency Report 3

$V_{IN}=12V$	M9 HS	M11 LS	M5 HS	M6 LS	M1 HS	M2 LS
irf3711s+irf3711s	116.8°C	99°C	129.3°C	135°C	97.3°C	129.3°C
AOD412+AOD416	102.9°C	89.7°C	112.7°C	102°C	89°C	96.1°C
AOD412+AOD414	106.3°C	102.1°C	111.5°C	121.1°C	87.9°C	113.6°C
IPB09N03LA+IPB05N03LA	103.8°C	91°C	112.2°C	108.2°C	92.5°C	103.5°C
IPB09N03LA+IPB09N03LA	116°C	115.6°C	124.5°C	132.6°C	103.2°C	136.6°C
IPB05N03LA+IPB05N03LA	100°C	93.9°C	114.2°C	113.1°C	92.6°C	104.5°C

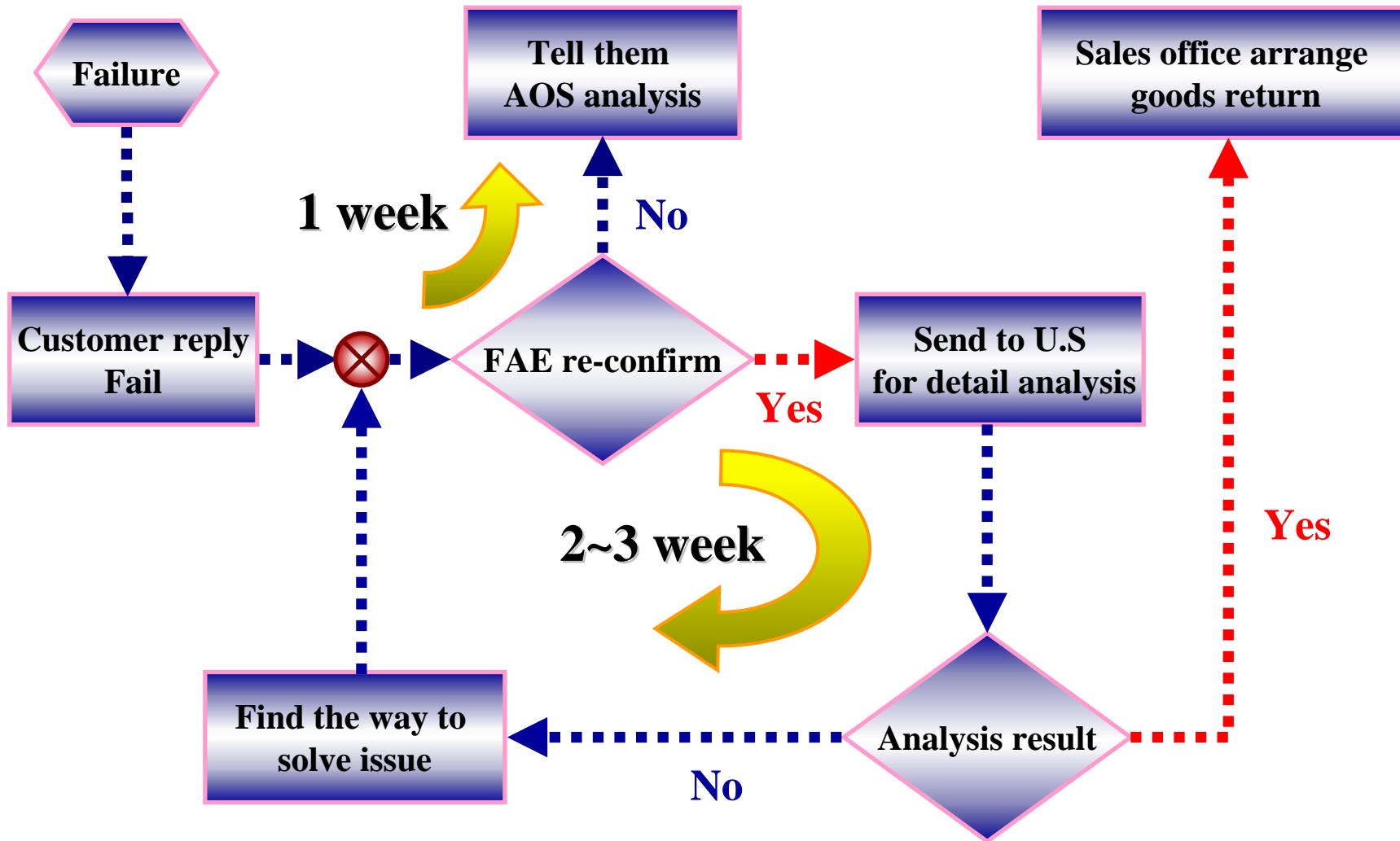
Table1.  $I_o=60A$  (20A/per phase),  $T_A=25^{\circ}C$

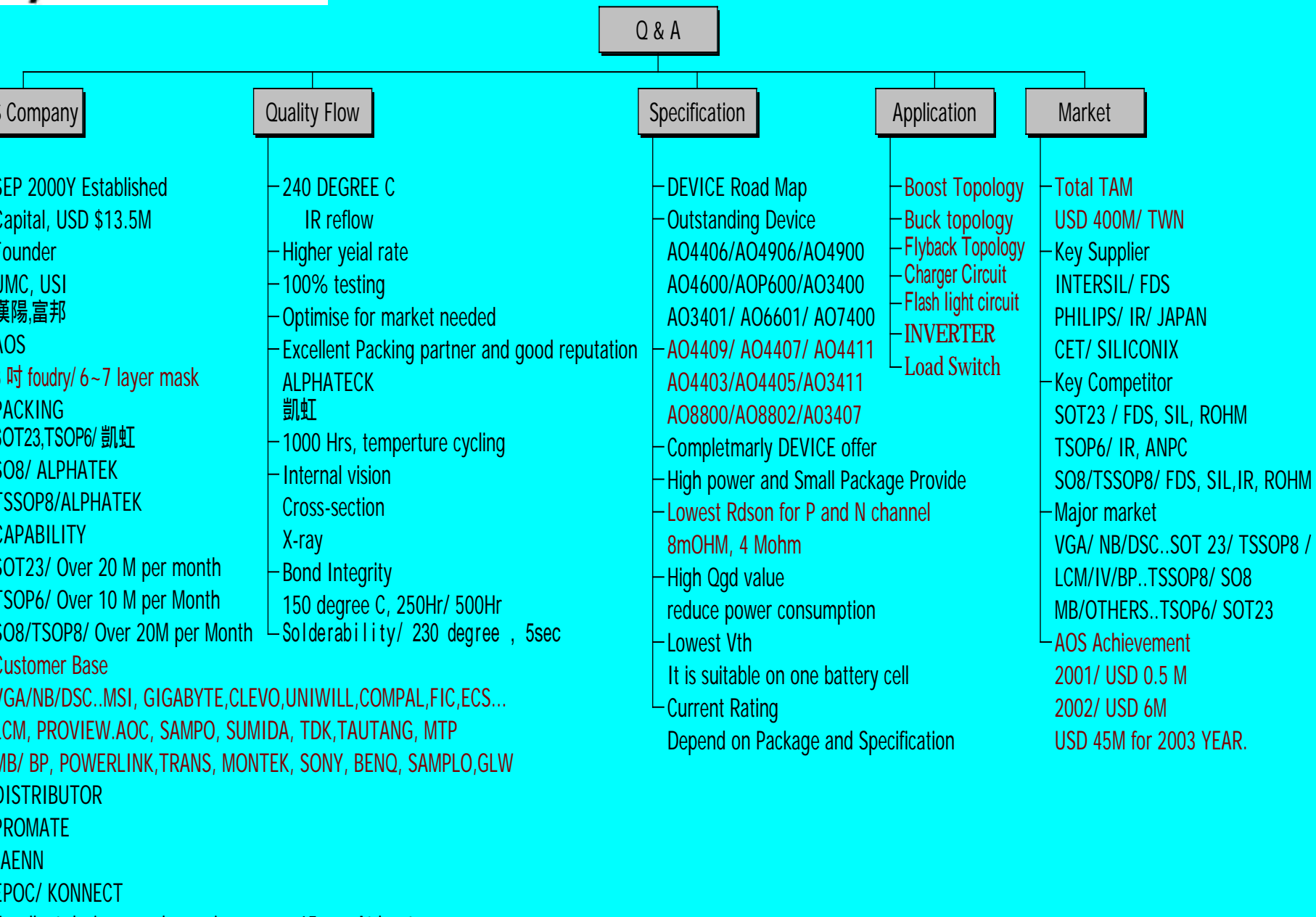


# Product Nomination



1 <sup>ST</sup> Digit		2 <sup>ND</sup> Digit	
4	SOS	4	Single
8	TSSOP8	6	Complemently
6	TSOP6	8	Dual
3	SOT23	9	Three in one
7	SC70	7	With Schottky
2	CSP		
5	ChipFET		
B	D2PAK	3,4	Digit
D	DPAK		even N Channel
P	TO220		odd P Channel
Y	Power SO		





# Q & A



BYE BYE!!