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# Li-lon BATTERY PROTECTOR R5422NxxxC/xxxE series

#### OUTLINE

The R5422NxxxC/E Series are protection ICs for over-charge/discharge of rechargeable one-cell Lithium-ion (Li+) excess load current, further include a short circuit protector for preventing large external short circuit current.

Each of these ICs is composed of three voltage detectors, a reference unit, a delay circuit, a short circuit protector, and a logic circuit. When charging voltage crosses the detector threshold from a low value to a value higher than V<sub>DET1</sub>, the output of C<sub>OUT</sub> pin, the output of over-charge detector/VD1, switches to low level, charger's negative pin level after the delay time by setting an external capacitor. After detecting over-charge the VD1 can be reset and the output of C<sub>OUT</sub> becomes high when the V<sub>DD</sub> voltage is coming down to a level lower than "V<sub>REL1</sub>", or when a kind of loading is connected to V<sub>DD</sub> after a charger is disconnected from the battery pack while the V<sub>DD</sub> level is in between "V<sub>DET1</sub>" and "V<sub>REL1</sub>" in the R5422NxxxC/E version.

The output of Dout pin, the output of over-discharge detector/VD2, switches to low level after internally fixed delay time passed, when discharging voltage crosses the detector threshold from a high value to a value lower than VDET2.

After R5422NxxxC/E Series detect the over-discharge voltage, connect a charger to the battery pack, and when the battery supply voltage becomes higher than the over-discharge detector threshold, VD2 is released and the voltage of Dout becomes "H" level.

An excess load current can be sensed and cut off after internally fixed delay time passed through the built in excess current detector, VD3, with Dout being enabled to low level. Once after detecting excess current, the VD3 is released and Dout level switches to high by detaching a battery pack from a load system.

Further, short circuit protector makes Dout level to low immediately with external short circuit current and removing external short circuit leads Dout level to high. After detecting over-discharge, supply current will be kept extremely low by halt some internal circuits operation. Output type of Cout and Dout is CMOS. 6-pin, SOT23-6 is available.

#### **■** FEATURES

High Voltage Process Technology	Absolute Maximum Rating of Voltage level between VDD-V- 28v			
Low supply current	Supply current	Тур. 6.0μΑ		
	Standby current (detecting over-disch	narge) Typ. 0.1μA		
High accuracy detector threshold	Over-charge detector (Topt=25°C)	±25mV		
	(Topt=0 to $50^{\circ}$ C	$\pm 30 \text{ mV}$		
	Over-discharge detector	±2.5%		
	Excess current detector	±20mV		
Variety of detector threshold	Over-charge detector threshold	4.0V - 4.5V step of 0.01V		
	Over-discharge detector threshold	2.0V - 3.0V step of 0.01V		
Built-in protection circuit	Excess current protection	0.05V - 0.4V step of 0.01V		
Output delay of over-charge	Time delay at C=0.01µF tVDET1=75r	ns, tVREL1=20ms		
Output delay of over-discharge	Internally fixed TYP. 17ms			
Output delay of excess-current	Internally fixed TYP. 10ms			
2 Over-current modes	Excess Current Mode / Short Mode			
Charging Available / non-available at 0V Cell	Acceptable of 0V Cell C version	n		
	Non-acceptable of 0V Cell E version	n		
Small package	SOT-23-6 / 6-pin			

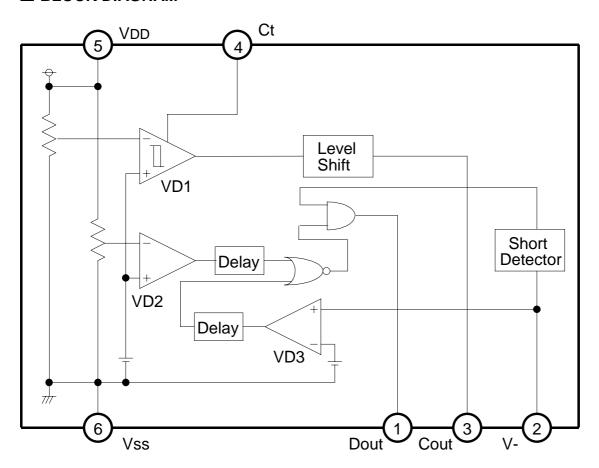
#### APPLICATIONS

Li+ one-cell protector for battery pack

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# **■ BLOCK DIAGRAM**



# **■ SELECTION GUIDE**

In the R5422Nxxxx Series, three of the input threshold for over-charge, over-discharge and excess current detectors can be designated.

Part Number is designated as follows:

R5422N 
$$\underline{XXX}X - \underline{XX} \leftarrow Part Number$$

$$\uparrow \uparrow \uparrow$$

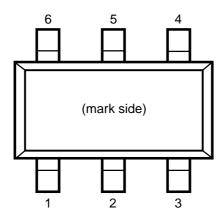
$$a \quad b \quad c$$

Code	Description
	Serial Number for the R5422N Series designating input threshold for over-charge, over-discharge
a	and excess current detectors as well as hysteresis range for over-charge detector.
b	Designation of version symbols
С	Taping Type: TR (refer to Taping Specification)



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# **■ PIN CONFIGURATION**



# **■ PIN DESCRIPTION**

Pin No.	Symbol	Pin description		
1	Dout	Output of over-discharge		
1	<b>D</b> 001	detection, CMOS output		
2	V-	Pin for charger negative input		
2	Соит	Output of over-charge		
3	Соот	detection, CMOS output		
4	Ct	Pin for external capacitor		
4	Ci	setting output delay of VD1		
5	$V_{\mathrm{DD}}$	Power supply(Substrate voltage		
3	<b>V</b> DD	level of the IC)		
6	Vss	Ground(Ground pin of the IC)		

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# ■ ABSOLUTE MAXIMUM RATINGS

Vss=0V

Symbol	Item	Ratings	Unit
$V_{ m DD}$	Supply voltage	-0.3 to 12	V
	Input Voltage		
V-	V - pin	$V_{\rm DD}$ -28 to $V_{\rm DD}$ +0.3	V
VCt	Ct pin	$V_{SS}$ -0.3 to $V_{DD}$ +0.3	V
	Output voltage		
VCout	Cout pin	$V_{\rm DD}$ -28 to $V_{\rm DD}$ +0.3	V
$VD_{OUT}$	Dout pin	$V_{SS}$ -0.3 to $V_{DD}$ +0.3	V
PD	Power dissipation	150	mW
Topt	Operating temperature range	-40 to 85	°C
Tstg	Storage temperature range	-55 to 125	°C

#### ABSOLUTE MAXIMUM RATINGS

Absolute Maximum ratings are threshold limit values that must not be exceeded ever for an instant under any conditions. Moreover, such values for any two items must not be reached simultaneously. Operation above these absolute maximum ratings may cause degradation or permanent damage to the device. These are stress ratings only and do not necessarily imply functional operation below these limits.

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# ■ ELECTRICAL CHARACTERISTIC

## • R5422N111C

Unless otherwise provided, Topt=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
V <sub>DD1</sub>	Operating input voltage	Voltage defined as VDD - Vss	1.5		10	V
Vst	Minimum operating Voltage for 0V charging	Voltage defined as V <sub>DD</sub> - V- , V <sub>DD</sub> - V <sub>SS</sub> =0V			1.2	V
V <sub>DET1</sub>	Over-charge threshold	R1=330 $\Omega$ (Topt=25°C) R1=330 $\Omega$ (Topt=0 to 50°C)*Note	4.225 4.220	4.250 4.250	4.275 4.280	V V
V <sub>REL1</sub>	Release voltage for over- charge detection		4.000	4.050	4.100	V
tV <sub>DET1</sub>	Output delay of over- Charge	$C=0.01\mu F, V_{DD}=3.6V \text{ to } 4.3V$	60	75	90	ms
tV <sub>REL1</sub>	Output delay of Release from Over-charge	C=0.01 $\mu$ F, V <sub>DD</sub> =4.3V to 4.1V	15	20	25	ms
V <sub>DET2</sub>	Over-discharge threshold	Detect falling edge of supply voltage	2.437	2.500	2.563	V
tVDET2	Output delay of over- Discharge	V <sub>DD</sub> =3.6V to 2.4V	12	17	22	ms
V <sub>DET3</sub>	Excess current threshold	Detect rising edge of 'V-' pin voltage	0.18	0.20	0.22	V
tV <sub>DET3</sub>	Output delay of excess Current		7	10	13	ms
Vshort	Short protection voltage	$V_{DD}=3.0V$	V <sub>DD</sub> -1.2	V <sub>DD</sub> -0.9	V <sub>DD</sub> -0.6	V
tshort	Output Delay of Short protection	V <sub>DD</sub> =3.0V		5	50	μs
Rshort	Reset resistance for Excess current protection	V <sub>DD</sub> =3.6V, V-=1.0V	37	75	113	$k\Omega$
Vol1	Nch ON voltage of Cout	Iol=40μA, Vdd=4.4V		0.3	0.5	V
Voh1	Pch ON voltage of Cout	Ioh=-40μA, Vdd=3.9V	3.4	3.75		V
Vol2	Nch ON voltage of Dout	Iol=40μA, Vdd=2.2V		0.2	0.5	V
Voh2	Pch ON voltage of Dout	Ioh=-40μA, Vdd=3.9V	3.4	3.7		V
Idd	Supply current	V <sub>DD</sub> =3.9V, V-=0V		6.0	10.0	μA
Istandby	Standby current	V <sub>DD</sub> =2.0V			0.1	μA

<sup>\*</sup>Note: Considering of variation in process parameters, we compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

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## R5422N112C

Unless otherwise provided, Topt=25°C

Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
V <sub>DD1</sub>	Operating input voltage	Voltage defined as VDD - Vss	1.5		10	V
Vst	Minimum operating Voltage for 0V charging	Voltage defined as VDD - V-, VDD - VSS=0V			1.2	V
Vdeti	Over-charge threshold	R1=330Ω(Topt=25°C) R1=330Ω (Topt=0 to 50°C)*Note	4.325 4.320	4.350 4.350	4.375 4.380	V V
Vreli	Release voltage for over- charge detection		4.100	4.150	4.200	V
tV <sub>DET1</sub>	Output delay of over- Charge	C=0.01 $\mu$ F, V <sub>DD</sub> =3.6V to 4.4V	61	77	93	ms
tV <sub>REL1</sub>	Output delay of Release from Over-charge	C=0.01 $\mu$ F, V <sub>DD</sub> =4.4V to 4.1V	15	20	25	ms
V <sub>DET2</sub>	Over-discharge threshold	Detect falling edge of supply Voltage	2.437	2.500	2.563	V
tV <sub>DET2</sub>	Output delay of over- Discharge	V <sub>DD</sub> =3.6V to 2.4V	12	17	22	ms
V <sub>DET3</sub>	Excess current threshold	Detect rising edge of 'V-' pin Voltage	0.18	0.20	0.22	V
tV <sub>DET3</sub>	Output delay of excess Current		7	10	13	ms
Vshort	Short protection voltage	$V_{DD}=3.0V$	V <sub>DD</sub> -1.2	V <sub>DD</sub> -0.9	V <sub>DD</sub> -0.6	V
tshort	Output Delay of Short protection	V <sub>DD</sub> =3.0V		5	50	μs
Rshort	Reset resistance for excess current protection	V <sub>DD</sub> =3.6V, V-=1.0V	37	75	113	$k\Omega$
Vol1	Nch ON voltage of Cout	Iol=40μA, V <sub>DD</sub> =4.4V		0.3	0.5	V
Voh1	Pch ON voltage of Cout	Ioh=-40μA, V <sub>DD</sub> =3.9V	3.40	3.75		V
Vol2	Nch ON voltage of Dout	Iol=40μA, V <sub>DD</sub> =2.2V		0.2	0.5	V
Voh2	Pch ON voltage of Dout	Ioh=-40μA, V <sub>DD</sub> =3.9V	3.4	3.7		V
Idd	Supply current	V <sub>DD</sub> =3.9V,V-=0V		6.0	10.0	μA
Istandby	Standby current	$V_{DD}=2.0V$			0.1	μΑ

<sup>\*</sup>Note: Considering of variation in process parameters, we compensate for this characteristic related to temperature by laser-trim, however this specification is guaranteed by design, not production tested.

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# R5422N111E

Unless otherwise provided, Topt=25°C

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Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
$V_{\mathrm{DD1}}$	Operating input voltage	Voltage defined as VDD - Vss	1.5		10	V
Vnochg	Maximum Voltage for disable of 0V charging	Voltage defined as $V_{DD}$ - $V_{SS}$ , $V_{DD}$ - $V$ -=4 $V$	0.5	0.8		V
Vdeti	Over-charge threshold	R1=330Ω(Topt=25°C) R1=330Ω (Topt=0 to 50°C)*Note	4.225 4.220	4.250 4.250	4.275 4.280	V V
Vreli	Release voltage for over- charge detection		4.000	4.050	4.100	V
$tV_{\text{DET1}}$	Output delay of over- Charge	C=0.01µF, V <sub>DD</sub> =3.6V to 4.3V	60	75	90	ms
$tV_{\text{REL1}}$	Output delay of Release from Over-charge	$C=0.01\mu F, V_{DD}=4.3V \text{ to } 4.1V$	15	20	25	ms
VDET2	Over-discharge threshold	Detect falling edge of supply Voltage	2.437	2.500	2.563	V
$tV_{\mathrm{DET2}}$	Output delay of over- Discharge	V <sub>DD</sub> =3.6V to 2.4V	12	17	22	ms
$V_{DET3}$	Excess current threshold	Detect rising edge of 'V-' pin Voltage	0.18	0.20	0.22	V
$tV_{\text{DET3}}$	Output delay of excess Current		7	10	13	ms
Vshort	Short protection voltage	$V_{DD}=3.0V$	V <sub>DD</sub> -1.2	V <sub>DD</sub> -0.9	$V_{\rm DD}$ -0.6	V
tshort	Output Delay of Short protection	V <sub>DD</sub> =3.0V		5	50	μs
Rshort	Reset resistance for excess current protection	V <sub>DD</sub> =3.6V, V-=1.0V	37	75	113	kΩ
Vol1	Nch ON voltage of Cout	Iol=40μA, V <sub>DD</sub> =4.4V		0.3	0.5	V
Voh1	Pch ON voltage of Cout	Ioh=-40µA, Vdd=3.9V	3.4	3.75		V
Vol2	Nch ON voltage of Dout	Iol=40μA, V <sub>DD</sub> =2.2V		0.2	0.5	V
Voh2	Pch ON voltage of Dout	Ioh=-40µA, Vdd=3.9V	3.4	3.7		V
${ m I}_{ m DD}$	Supply current	$V_{DD}=3.9V,V-=0V$		6.0	10.0	μA
Istandby	Standby current	$V_{DD}=2.0V$			0.1	μA

<sup>\*</sup>Note: Considering of variation in process parameters, we compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

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# • R5422N112E

Unless otherwise provided, Topt=25°C

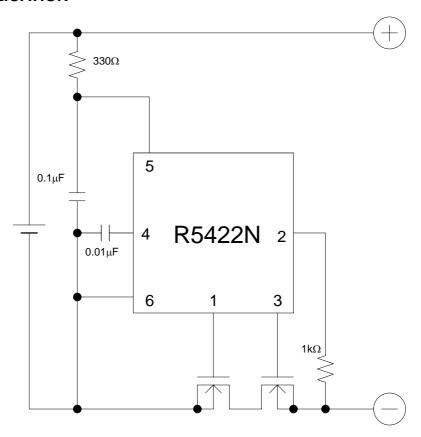
Symbol	Item	Conditions	MIN.	TYP.	MAX.	Unit
V <sub>DD1</sub>	Operating input voltage	Voltage defined as VDD - Vss	1.5		10	V
Vnochg	Maximum Voltage for disable of 0V charging	Voltage defined as VDD - VSS , VDD - V-=4V	0.5	0.8		V
V <sub>DET1</sub>	Over-charge threshold	R1=330Ω(Topt=25°C) R1=330Ω (Topt=0 to 50°C)*Note	4.325 4.320	4.350 4.350	4.375 4.380	V V
Vreli	Release voltage for over- charge detection		4.100	4.150	4.200	V
tV <sub>DET1</sub>	Output delay of over- Charge	C=0.01µF, Vdd=3.6V to 4.4V	61	77	93	ms
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V <sub>DET2</sub>	Over-discharge threshold	Detect falling edge of supply Voltage	2.437	2.500	2.563	V
tVDET2	Output delay of over- Discharge	V <sub>DD</sub> =3.6V to 2.4V	12	17	22	ms
V <sub>DET3</sub>	Excess current threshold	Detect rising edge of 'V-' pin Voltage	0.18	0.20	0.22	V
tV <sub>DET3</sub>	Output delay of excess Current	V <sub>DD</sub> =3.0V	7	10	13	ms
Vshort	Short protection voltage	$V_{DD}=3.0V$	V <sub>DD</sub> -1.2	V <sub>DD</sub> -0.9	V <sub>DD</sub> -0.6	V
tshort	Output Delay of Short protection	V <sub>DD</sub> =3.0V		5	50	μs
Rshort	Reset resistance for excess current protection	V <sub>DD</sub> =3.6V, V-=1.0V	37	75	113	kΩ
Vol1	Nch ON voltage of Cout	Iol=40μA, V <sub>DD</sub> =4.4V		0.3	0.5	V
Voh1	Pch ON voltage of Cout	Ioh=-40µA, Vdd=3.9V	3.4	3.7		V
Vol2	Nch ON voltage of Dout	Iol=40μA, V <sub>DD</sub> =2.2V		0.2	0.5	V
Voh2	Pch ON voltage of Dout	Ioh=-40μA, V <sub>DD</sub> =3.9V	3.4	3.7		V
$\mathbf{I}_{ extsf{DD}}$	Supply current	$V_{DD}=3.9V,V-=0V$		6.0	10.0	μA
Istandby	Standby current	$V_{DD}=2.0V$			0.1	μA

<sup>\*</sup>Note: Considering of variation in process parameters, we compensate for this characteristic related to temperature by laser-trim, however, this specification is guaranteed by design, not production tested.

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#### TYPICAL APPLICATION



#### **APPLICATION HINTS**

R1 and C1 will stabilize a supply voltage to the R5422Nxxxx. A recommended R1 value is less than  $1k\Omega$ . A larger value of R1 leads higher detection voltage, makes some errors, because of shoot through current flowed in the R5422Nxxxx.

R2 will stabilize a V- pin voltage. The resetting from over-discharge with connecting a charger possibly be disabled by larger value of R2. Recommended value is less than 1 k $\Omega$ .

R1 and R2 can operate also as a part of current limit circuit against for setting cell reverse direction or for applying excess charging voltage to the R5422Nxxxx, battery pack, while smaller R1 and R2 may cause a power consumption over rating of power dissipation of the R5422Nxxxx and a total of 'R1+R2' should be more than  $1k\Omega$ .

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