Advanced Analog Circuits Data Sheet

1.5A STEP-DOWN/STEP-UP/INVERTING DC-DC CONVERTER

AZ34063A

General Description

The AZ34063A is a monolithic switching regulator control circuit which contains the primary functions required for DC-DC converters. This device consists of internal temperature compensated reference, voltage comparator, controlled duty cycle oscillator with active current limit circuit, driver and high current output switch.

The AZ34063A is specifically designed as a general DC-DC converter to be used in Step-Down, Step-Up and Voltage-Inverting applications with a minimum number of external components.

The AZ34063A is available in 2 packages: SOIC-8 and DIP-8.

Features

- Operation from 3.0V to 36V Input
- Low Standby Current
- Current Limiting
- Output Switch Current to 1.5A
- Output Voltage Adjustable
- Operation Frequency up to 180KHz
- Precision 2% Reference

Applications

- · Battery Chargers
- ADSL Modems
- Hubs
- Negative Voltage Power Supplies

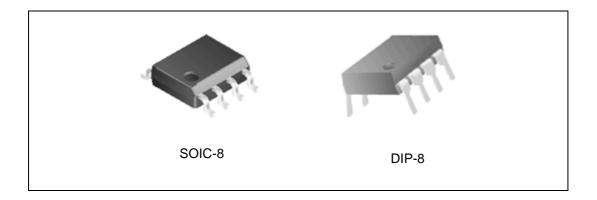
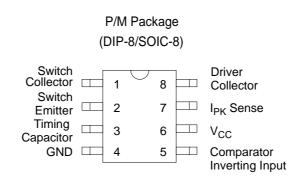


Figure 1. Package Types of AZ34063A



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Pin Configuration



Top View

Figure 2. Pin Configuration of AZ34063A

Functional Block Diagram

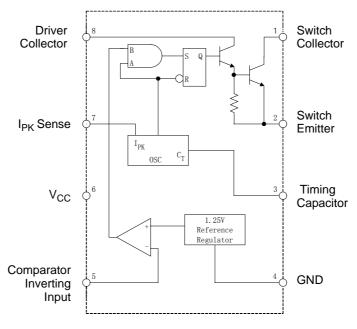


Figure 3. Functional Block Diagram of AZ34063A

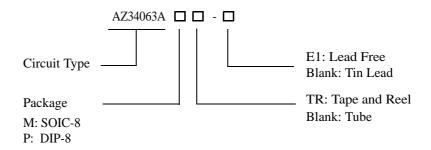
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Pin Description

Pin Number	Pin Name	Function
1	Switch Collector	Internal switch transistor collector
2	Switch Emitter	Internal switch transistor emitter
3	Timing Capacitor	Timing Capacitor to control the switching frequency
4	GND	Ground pin for all internal circuits
5	Comparator Inverting Input	Inverting input pin for internal comparator
6	V _{CC}	Voltage supply
7	I _{PK} Sense	Peak Current Sense Input by monitoring the voltage drop across an external current sense resistor to limit the peak cur- rent through the switch
8	Driver Collector	Voltage driver collector

Ordering Information



Package	Temperature	Part Number		Mark	Packing Type	
	Range	Tin Lead	Lead Free	Tin Lead	Lead Free	1 acking Type
SOIC-8	-40 to 85°C	AZ34063AM	AZ34063AM-E1	34063AM	34063AM-E1	Tube
		AZ34063AMTR	AZ34063AMTR-E1	34063AM	34063AM-E1	Tape & Reel
DIP-8	-40 to 85°C	AZ34063AP	AZ34063AP-E1	AZ34063AP	AZ34063AP-E1	Tube

BCD Semiconductor's Pb-free products, as designated with "E1" suffix in the part number, are RoHS compliant.

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Absolute Maximum Ratings (Note 1)

Parameter	Symbol	Value	Unit	
Power Supply Voltage	V _{CC}	40	V	
Comparator Input Voltage Range		V _{IR}	-0.3 to 40	V
Switch Collector Voltage		V _C (switch)	40	V
Switch Emitter Voltage (V _{PIN 1} =40V)		V _E (switch)	40	V
Switch Collector to Emitter Voltage		V _{CE} (switch)	40	V
Driver Collector Voltage	V _C (driver)	40	V	
Driver Collector Current (Note 2)	I _C (driver)	100	mA	
Switch Current	I_{SW}	1.5	A	
Power Dissipation ($T_A=25 ^{\circ}\text{C}$)	DIP-8	P_{D}	1.25	W
Power Dissipation (1 _A =23 °C)	SOIC-8	T D	780	mW
Thermal Resistance	DIP-8	$R_{ heta JA}$	100	°C /W/
Thermal Resistance	SOIC-8	$\kappa_{\theta JA}$	160	°C /W
Operating Junction Temperature		T_{J}	150	$^{\circ}$ C
Lead Temperature (Soldering, 10s)		T _{LEAD}	260	$^{\circ}$ C
Storage Temperature Range	T _{STG}	-65 to 150	$^{\circ}\!\mathbb{C}$	
ESD (Human body model)		2000	V	

Note 1: Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

Note 2: Maximum package power dissipation limits must be observed.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
Supply Voltage	V_{CC}	3	36	V
Ambient Temperature	T_A	-40	85	°C



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Electrical Characteristics

 $(V_{CC}=5.0 \text{ V}, T_A=-40 \text{ to } 85^{\circ}\text{C}, \text{ unless otherwise specified.})$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit	
OSCILLATOR							
Frequency	f_{OSC}	V _{PIN5} =0V, C _T =1.0nF T _A =25°C	30	38	45	KHz	
Charge Current	I _{CHG}	V _{CC} =5.0V to 36V, T _A =25°C	30	38	45	μΑ	
Discharge Current	I _{DISCHG}	V _{CC} =5.0V to 36V, T _A =25°C	180	240	290	μΑ	
Discharge to Charge Current Ratio	I _{DISCHG} /I _{CHG}	Pin 7 to V _{CC} , T _A =25°C	5.2	6.5	7.5		
Current Limit Sense Voltage	V _{IPK} (sense)	I _{CHG} =I _{DISCHG} , T _A =25°C 250 I _{SW} =1.0A, Pins 1, 8 connected, Common Emitter		300	350	mV	
OUTPUT SWITCH (Note 3)			l.			
Saturation Voltage, Dalington Connection	V _{CE} (sat)	2		1.0	1.3	V	
Saturation Voltage (Note 4.)	V _{CE} (sat)	I_{SW} =1.0A, R_{PIN8} =82 Ω to V_{CC} , Forced β =20, Common Emitter		0.45	0.7	V	
DC Current Gain	h _{FE}	I_{SW} =1.0A, V_{CE} =5.0V, T_{A} =25°C		75			
Collector Off-State Current	I _C (off)	I _C (off) V _{CE} =36V		0.01	100	μΑ	
COMPARATOR	I			l			
	V _{TH}	T _A =25°C	1.225	1.250	1.275		
Threshold Voltage		T _A =-40 to 85°C	1.21	1.250	1.29	V	
Threshold Voltage Line Regulation	R _{EGLINE}	V _{CC} =3.0V to 36V		1.4	5	mV	
Input Bias Current	I _{IB}	V _{IN} =0V		-20	-400	nA	
TOTAL DEVICE							
Supply Current I _{CC}		V_{CC} =5.0V to 36V, C_T =1.0nF, V_{PIN7} = V_{CC} , V_{PIN5} > V_{TH} , V_{PIN2} =GND, other pins open			4	mA	

Note 3: Low duty cycle pulse technique are used during test to maintain junction temperature as close to ambient temperature as possible.

Note 4: If the output switch is driven into hard saturation (non-Darlington configuration) at low switch currents (\leq 30mA) and high driver currents (\geq 30mA), it may take up to 2.0us for it to come out of saturation. This condition will shorten the off time at frequencies 30KHz, and is magnified at high temperatures. This condition does not occur with a Darlington configuration, since the output switch cannot saturate. If a non-Darlington configuration is used, the following output drive condition is recommended:

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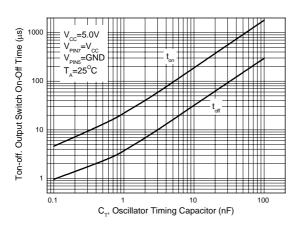
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Electrical Characteristics (Continued)

Forced β of output switch: $\frac{I_C \ output}{I_C driver \ - \ 7.0 mA^*} {\geq 10}$

Typical Performance Characteristics



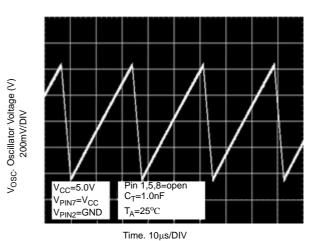
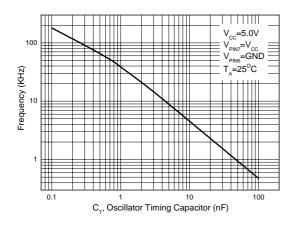


Figure 4. Output Switch On-Off Time vs. Oscillator Timing Capacitor

Figure 5. Timing Capacitor Waveform



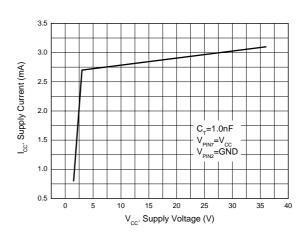


Figure 6. Oscillator Frequency vs. Timing Capacitor

Figure 7. Standard Supply Current vs. Supply Voltage

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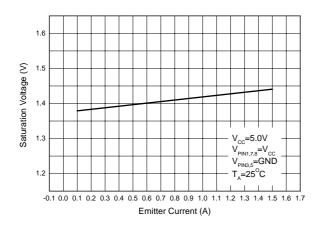
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^{*} The 100Ω resistor in the emitter of the driver device requires about 7.0 mA before the output switch conducts.



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Typical Performance Characteristics (Continued)



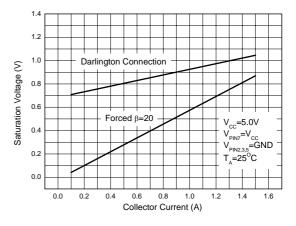


Figure 8. Emitter Follower Configuration Output Saturation Voltage vs. Emitter current

Figure 9. Common Emitter Configuration Output Switch Saturation Voltage vs. Collector Current

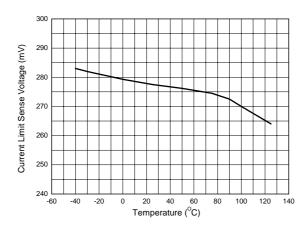


Figure 10. Current Limit Sense Voltage vs. Temperature



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Typical Applications

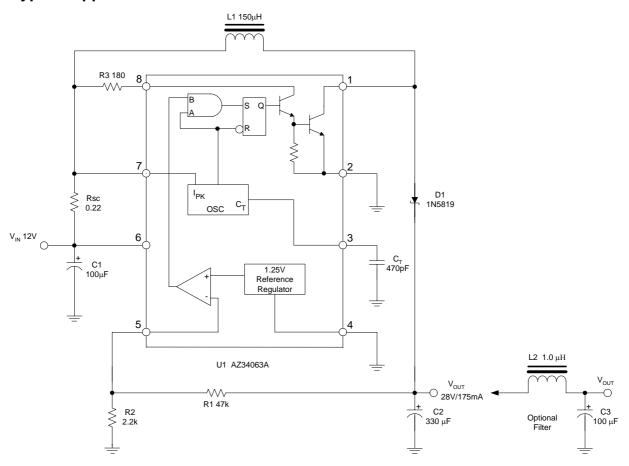


Figure 10. Step-Up Converter (Note 5)

Note 5: This is a typical step-up converter configuration. In the steady state, if the resistor divider voltage at pin 5 is greater than the voltage in the non-inverting input, which is 1.25V determined by the internal reference, the output of the comparator will go low. At the next swithching period, the output switch will not conduct and the output voltage will eventually drop below its nominal voltage until the divider voltage at pin 5 is lower than 1.25V. Then the output of the comparator will go high, the output switch will be allowed to conduct. Since $V_{PIN5} = V_{OUT}^* R2/(R1+R2)=1.25(V)$, the output voltage can be decided by $V_{OUT}=1.25*(R1+R2)/R2(V)$.



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Typical Applications (Continued)

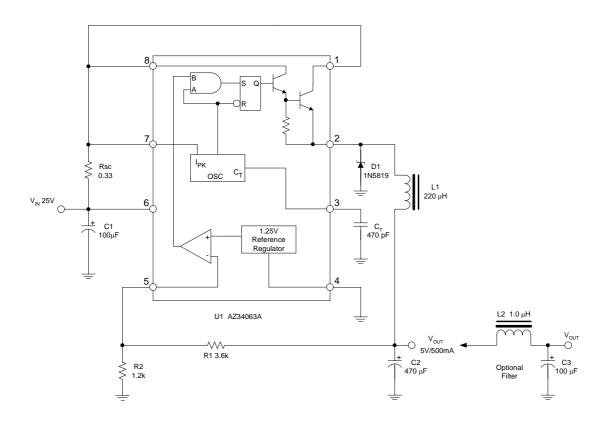


Figure 11. Step-Down converter (Note 6)

Note 6: This is a typical step-down converter configuration. The working process in the steady state is similar to step-up converter, $V_{PIN5}=V_{OUT}*R2/(R1+R2)=1.25$ (V), the output voltage can be decided by $V_{OUT}=1.25*(R1+R2)/R2$ (V).



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Typical Applications (Continued)

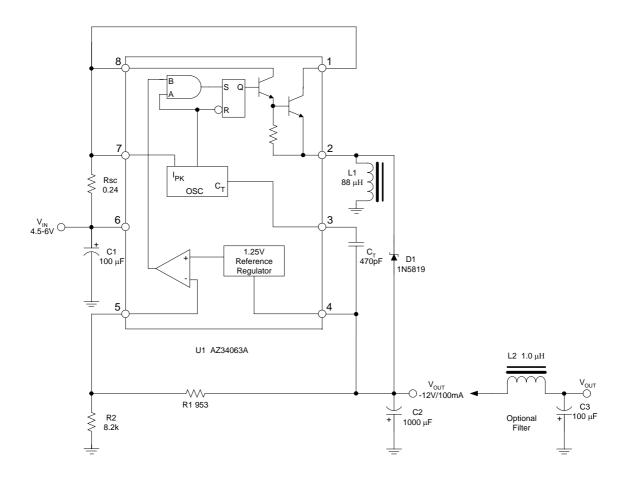


Figure 12. Voltage Inverting Converter (Note 7)

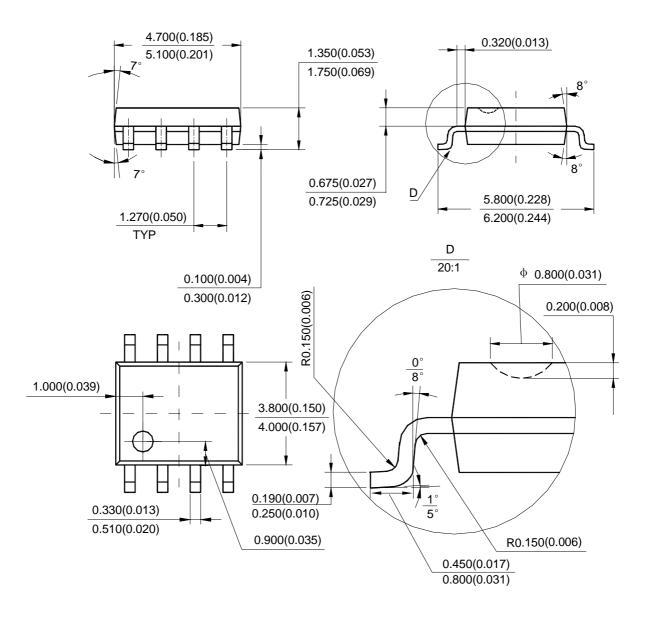
Note 7: This is a typical inverting converter configuration. The working process in the steady state is similar to step-up converter, the difference in this situation is that the voltage at the non-inverting pin of the comparator is equal to 1.25V+V $_{OUT}$, then V $_{PIN5}$ =V $_{OUT}$ *R2/(R1+R2)=1.25V+V $_{OUT}$, so the output voltage can be decided by V $_{OUT}$ =-1.25*(R1+R2)/R1 (V).



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Mechanical Dimensions

SOIC-8 Unit: mm(inch)

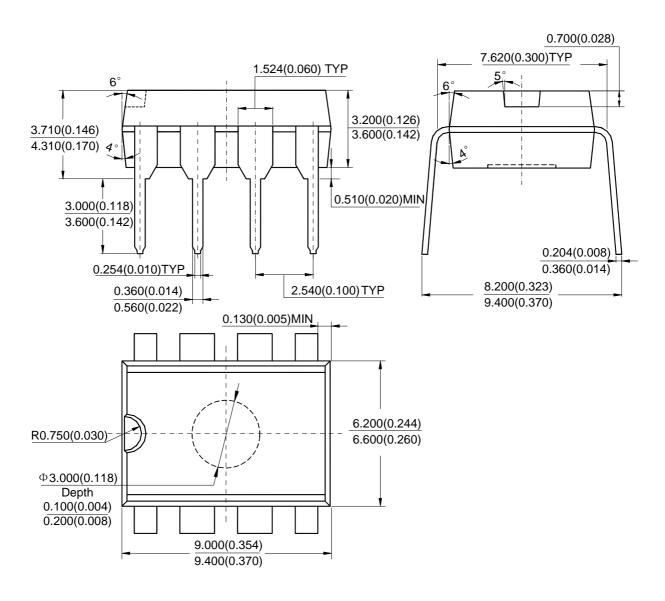




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Mechanical Dimensions (continued)

DIP-8 Unit: mm(inch)





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