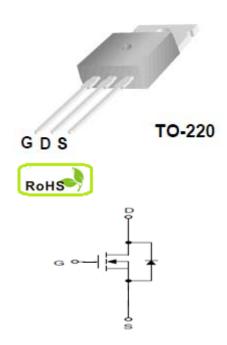
SKD408T 40V N-Channel MOSFET

■ FEATURES

- 40V/250A³
 RDS(ON)= 2.3m**Ω typ@** VGS=10V
- Lead free and Green Device Available
- Low Rds-on to Minimize Conductive Loss
- High avalanche Current
- Application
- Power Supply
- Power Tool
- Load Switch Control

■ PIN DESCRIPTION



Absolute Maximum Ratings (T_A=25°C unless otherwise noted)

Symbol	Parameter		Maximum	Unit
V_{DSS}	Drain-to-Source Voltage		40	V
V_{GSS}	Gate-to-Source Voltage		±20	V
I_D^3	Continuous Drain Current	T _C =25°C	250	
		T _C =100°C	162	A
l _{DP} ⁴	Pulsed Drain Current	T _C =25°C	800	
IAS ⁵	Avalanche Current		33	
EAS⁵	Avalanche energy		1.5	J
PD	Maximum Power Dissipation	T _C =25°C	285	W
		T _C =100°C	145	VV
$T_{J_1} T_{STG}$	Junction & Storage Temperature Range		-55~175	°C

Thermal Characteristics

Symbol	Parameter	Typical	Unit
Rθjc	Thermal Resistance-Junction to Case	0.52	°C/W
Rθja	Thermal Resistance-Junction to Ambient	62.5	0/88

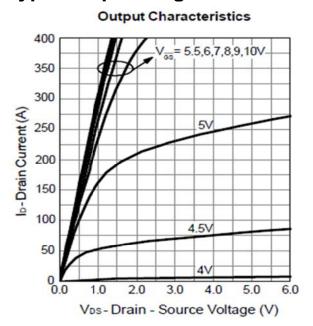
SKD408T 40V N-Channel MOSFET

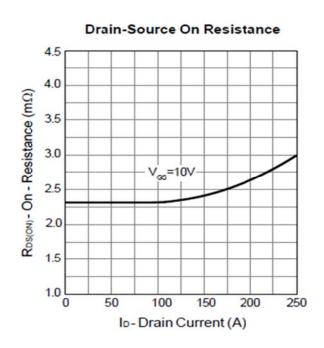
Electrical	Characteristics	(TA=25°C	unless	otherwise no	oted)

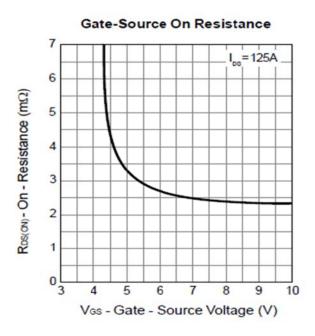
Symbol	Parameter	Test Conditions	Min.	Тур	Max.	Unit	
Static Characteristics							
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D =250uA	40	_	_	V	
	Zero Gate Voltage Drain Current	V _{DS} =32V,V _{GS} =0V	_	_	1		
I _{DSS}		T _J =125°C	_	_	10	uA	
$V_{GS(th)}$	Gate Threshold Voltage	V _{DS} =V _{GS} ,I _D =250uA	2	3	4	V	
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	_	_	±100	nA	
	Drain-Source On-Resistance	V _{GS} =10V, I _D =60A		2.3	4	mΩ	
R _{DS(on)} ¹			_	_			
Diode Cha	aracteristics						
V _{SD} ¹	Diode Forward Voltage	I _{SD} =60A,V _{GS} =0V	_	_	1.3	V	
ls ³	Diode Continuous Forward Current		_	_	250	Α	
t _{rr}	Reverse Recovery Time	I _F =60A,	_	37	_	nS	
Q _{rr}	Reverse Recovery Charge	dl/dt=100A/us	_	62	_	nC	
Dynamic (Characteristics ²						
R_G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, Frequency=1MHz	_	1	_	Ω	
C _{iss}	Input Capacitance		_	7000	_		
Coss	Output Capacitance	-V _{GS} =0V, V _{DS} =25V -Frequency=1MHz	_	1850	_	pF	
C _{rss}	Reverse Transfer Capacitance		_	675		1 .	
$t_{d(on)}$	Turn-On Delay Time		_	35	_		
t _r	Turn-On Rise Time	V_{DD} =30V, I_{D} =60A,	_	20	_		
$t_{d(off)}$	Turn-Off Delay Time	V_{GS} =10V, R_{G} =6 Ω	_	45	_	nS	
t _f	Turn-Off Fall Time		_	62			
Gate Charg	ge Characteristics ²						
Q_{g}	Total Gate Charge	\/ -22\/\/ -40\/	_	190	_		
Q_gs	Gate-to-Source Charge	-V _{DS} =32V,V _{GS} =10V -I _D =60A	_	30	_	nC	
Q_{qd}	Gate-to-Drain Charge		_	80	_		

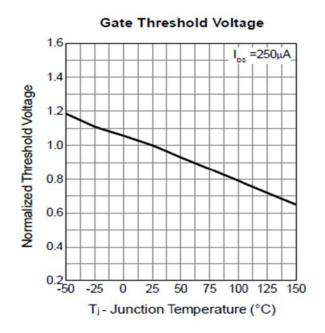
- 2: Guaranteed by design, not subject to production testing.
- 3: Calculated continuous current based on maximum allowable junction temperature. Package limitation current is 55A.
- 4: Repetitive rating, pulse width limited by max junction temperature.
- 5: Starting $TJ = 25^{\circ}C, L = 1mH$

Typical Operating Characteristics



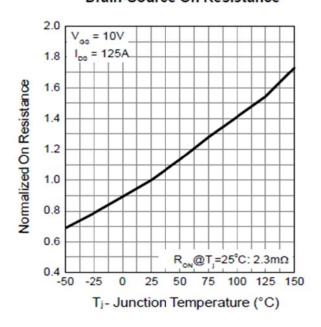




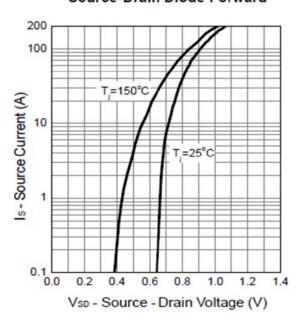


Typical Operating Characteristics

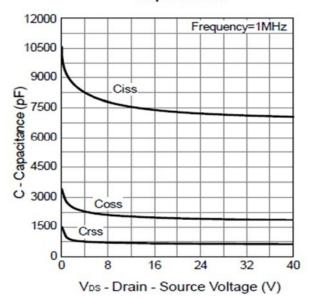
Drain-Source On Resistance



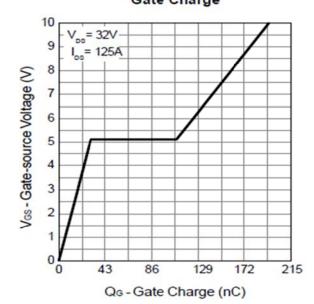
Source-Drain Diode Forward



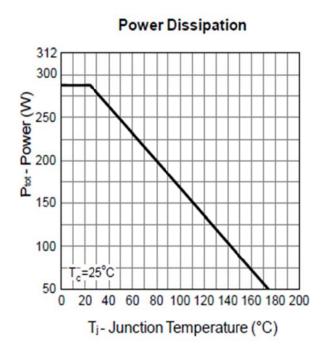
Capacitance

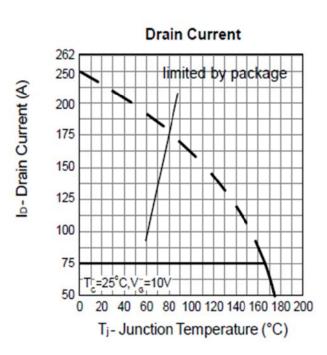


Gate Charge



Typical Operating Characteristics





Thermal Transient Impedance

