

Device Specification



ELECTRICAL CHARACTERISTICS

Part Number	I _{hold} (A)	I _{trip} (A)	V _{max} (Vdc)	I _{max} (A)	P _d ^{max.} (W)	Maximum Time To Trip		Resistance	
						Current (A)	Time (Sec.)	R _{min} (Ω)	R _{1max} (Ω)
SMD1206P150TFT	1.50	3.00	6	100	0.8	8.00	0.30	0.040	0.120

Note: I_{hold} = Hold current: maximum current device will pass without tripping in 20°C still air.

I_{trip} = Trip current: minimum current at which the device will trip in 20°C still air.

V_{max} = Maximum voltage device can withstand without damage at rated current (I_{max})

I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max})

P_d = Power dissipated from device when in the tripped state at 20°C still air.

R_{min} = Minimum resistance of device in initial (un-soldered) state.

R_{1max} = Maximum resistance of device at 20°C measured one hour after tripping or reflow soldering of 260°C for 20 sec.

*Value specified were determined using the PWB with 0.030"×1.5oz copper traces.

*Customer should verify the device performance in their specified conditions.

Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.

Recognitions:

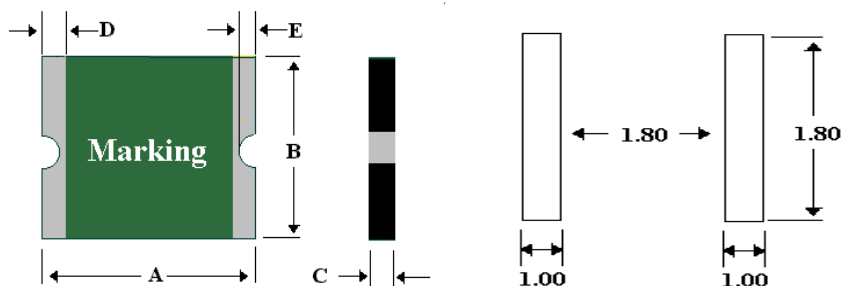


FIGURE

SOLDER PAD LAYOUTS

Marking:

└ Polytronics Logo
P₁₅
└ Part Identification



Note: Polystar is Polytronics's manufacturing site in China. The Polystar ID marking shall appear on smallest package.

PHYSICAL DIMENSIONS (mm)

Part Number	A		B		C		D		E	
	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Max.
SMD1206P150TFT	3.00	3.40	1.50	1.80	0.50	1.00	0.25	0.75	0.10	0.45

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EVERFUSETM

Polymeric PTC Resistor

Product: SMD1206P150TFT

Revision: C1

Date: September 3, 2008

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THERMAL DERATING CHART – Ihold/Itrip(Amps)

(RECOMMENDED DATA)

Model		Ambient Operation Temperature								
		-40℃	-20℃	0℃	23℃	40℃	50℃	60℃	70℃	85℃
SMD1206P150TFT	Ihold	2.20	1.99	1.77	1.50	1.34	1.23	1.10	1.01	0.84
SMD1206P150TFT	Itrip	4.39	3.97	3.56	3.00	2.68	2.48	2.28	2.06	1.67