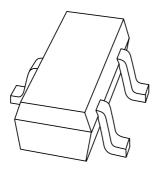
### **DISCRETE SEMICONDUCTORS**

# DATA SHEET



BC846T; BC847T NPN general purpose transistors

Preliminary specification Supersedes data of 1997 Jul 07 1999 Apr 26





### NPN general purpose transistors

### BC846T; BC847T

#### **FEATURES**

- Low current (max. 100 mA)
- Low voltage (max. 65 V).

#### **APPLICATIONS**

- General purpose switching and amplification, especially in portable communication equipment
- Electronic data processing (EDP) and consumer applications.

#### **DESCRIPTION**

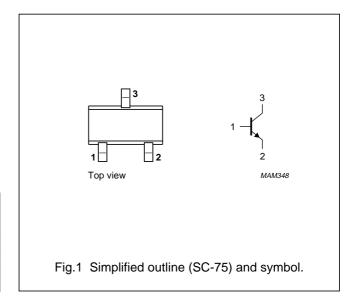
NPN transistor in an SC-75 plastic package. PNP complements: BC856T and BC857T.

#### **MARKING**

TYPE NUMBER	MARKING CODE	TYPE NUMBER	MARKING CODE
BC846AT	1A	BC847BT	1F
BC846BT	1B	BC847CT	1G
BC847AT	1E		

#### **PINNING**

PIN	DESCRIPTION
1	base
2	emitter
3	collector



#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BC846AT; BC846BT		_	80	V
	BC847AT; BC847BT; BC847CT		_	50	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BC846AT; BC846BT		_	65	V
	BC847AT; BC847BT; BC847CT		_	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	100	mA
I <sub>CM</sub>	peak collector current		_	200	mA
I <sub>BM</sub>	peak base current		_	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	150	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

# NPN general purpose transistors

BC846T; BC847T

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	833	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

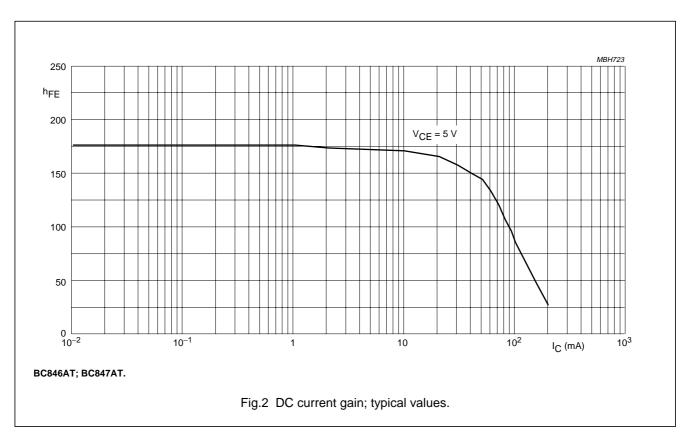
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V	_	_	15	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V; T <sub>j</sub> = 150 °C	_	_	5	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V				
	BC846AT; BC847AT		110	_	220	
	BC846BT; BC847BT		200	_	450	
	BC847CT		420	_	800	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA	_	_	200	mV
	voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; note 1	_	_	400	mV
$V_{BE}$	base-emitter voltage	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	580	_	700	mV
		I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 5 V	_	_	770	mV
C <sub>c</sub>	collector capacitance	I <sub>E</sub> = i <sub>e</sub> = 0; V <sub>CB</sub> = 10 V; f = 1 MHz	_	_	1.5	pF
C <sub>e</sub>	emitter capacitance	$I_C = i_c = 0$ ; $V_{EB} = 500 \text{ mV}$ ; $f = 1 \text{ MHz}$	_	11	_	pF
f <sub>T</sub>	transition frequency	$I_C = 10 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure	$I_C$ = 200 μA; $V_{CE}$ = 5 V; $R_S$ = 2 kΩ; $f$ = 1 kHz; $B$ = 200 Hz	_	_	10	dB

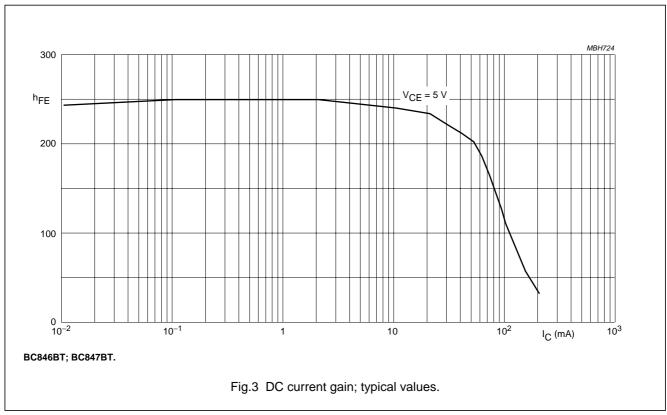
#### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

# NPN general purpose transistors

BC846T; BC847T



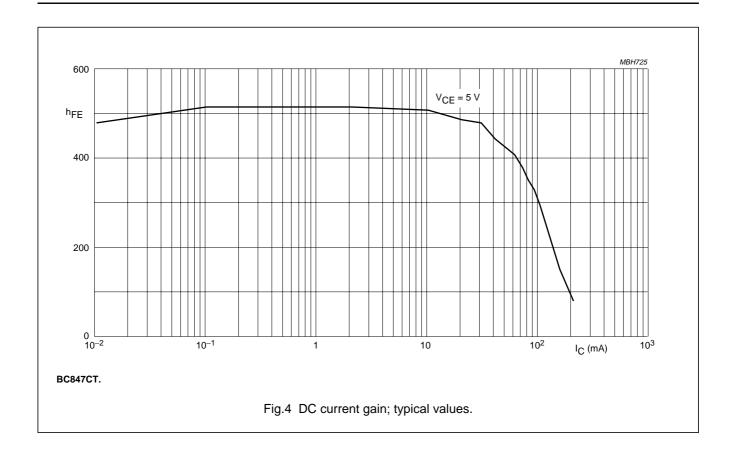


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1999 Apr 26

# NPN general purpose transistors

BC846T; BC847T



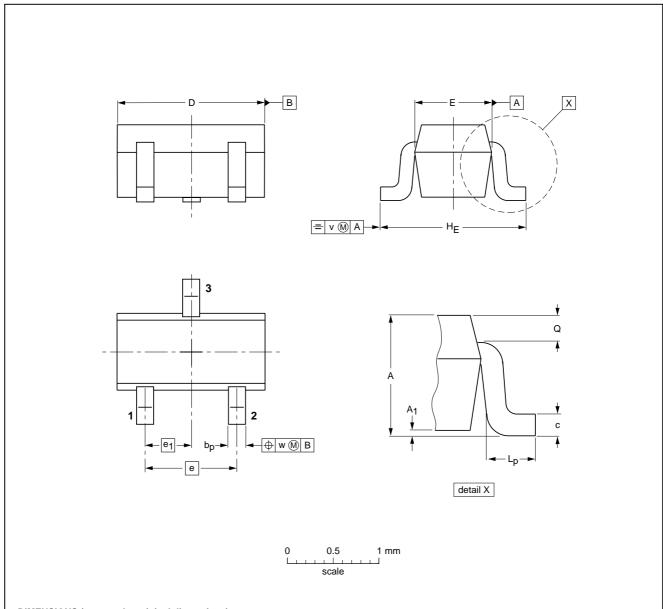
# NPN general purpose transistors

BC846T; BC847T

#### **PACKAGE OUTLINE**

Plastic surface mounted package; 3 leads

**SOT416** 



#### **DIMENSIONS** (mm are the original dimensions)

UNIT	Α	A <sub>1</sub> max	bp	С	D	E	е	e <sub>1</sub>	HE	Lp	Q	v	w
mm	0.95 0.60	0.1	0.30 0.15	0.25 0.10	1.8 1.4	0.9 0.7	1	0.5	1.75 1.45	0.45 0.15	0.23 0.13	0.2	0.2

OUTLINE		REFER	EUROPEAN ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT416			SC-75			97-02-28

### NPN general purpose transistors

BC846T; BC847T

#### **DEFINITIONS**

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values attacked in a considerate with the Abachita Marianum Dating Cristan (IFC 424). Chang above and				

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

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