

## SE8205

### N-Channel Enhancement Mode Field Effect Transistor

Revision:B

#### Features

- $V_{DS} = 20V, I_D = 6A$   
 $R_{DS(ON)} < 34.5m\Omega @ V_{GS} = 2.5V$   
 $R_{DS(ON)} < 24.5m\Omega @ V_{GS} = 4.5V$
- High Power and current handling capability
- Lead free product is acquired
- Surface Mount Package

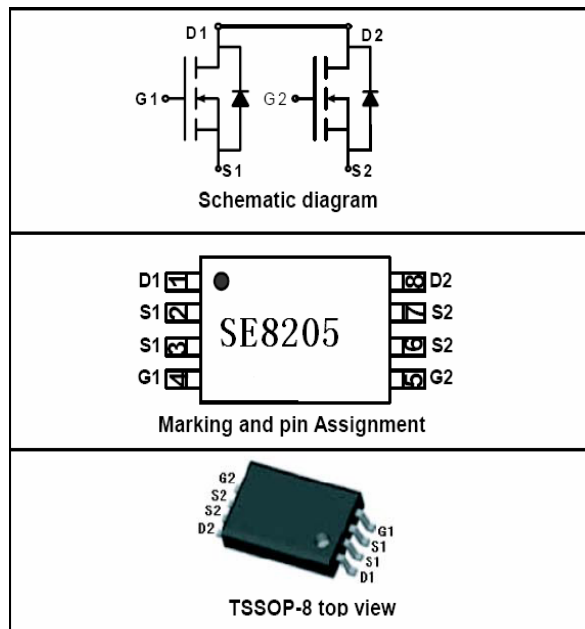
#### Applications

- Battery protection
- Load switch
- Power management

#### Construction

- Silicon epitaxial planer

#### External Dimensions: (Unit:mm)



#### Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	$V_{DS}$	20	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Drain Current-Continuous@ Current-Pulsed (Note 1)	$I_D$	6	A
	$I_{DM}$	25	A
Maximum Power Dissipation	$P_D$	1.5	W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150	°C

#### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	83	°C/W
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#### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	20			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 20V, V_{GS} = 0V$			0.8	$\mu A$
Gate-Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 10V, V_{DS} = 0V$			$\pm 80$	nA

ON CHARACTERISTICS (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ,I <sub>D</sub> =250μA	0.45	0.65	1.2	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4.5A		21	24.5	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =3.5A		30	34.5	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V,I <sub>D</sub> =4.5A	3			S
DYNAMIC CHARACTERISTICS (Note4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =8V,V <sub>GS</sub> =0V, F=1.0MHz		600		PF
Output Capacitance	C <sub>oss</sub>			330		PF
Reverse Transfer Capacitance	C <sub>rss</sub>			140		PF
SWITCHING CHARACTERISTICS (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =10V,I <sub>D</sub> =1A V <sub>GS</sub> =4.5V,R <sub>GEN</sub> =6Ω		10	20	nS
Turn-on Rise Time	t <sub>r</sub>			11	25	nS
Turn-Off Delay Time	t <sub>d(off)</sub>			35	70	nS
Turn-Off Fall Time	t <sub>f</sub>			30	60	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =10V,I <sub>D</sub> =6A, V=4.5V		10	15	nC
Gate-Source Charge	Q <sub>gs</sub>			2.3		nC
Gate-Drain Charge	Q <sub>gd</sub>			3		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V=0V,I=1.7A			1.2	V
Diode Forward Current (Note 2)	I <sub>s</sub>			1.7		A

## NOTES:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu s$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production testing.

## TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

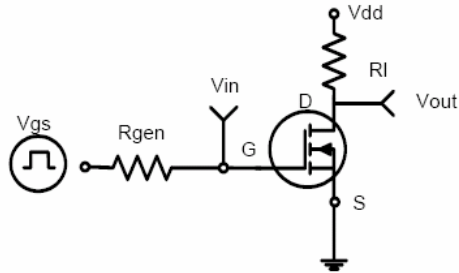


Figure 1: Switching Test Circuit

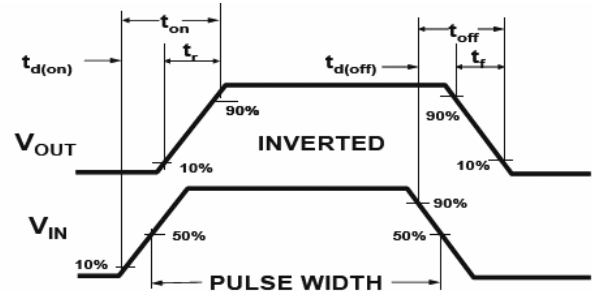


Figure 2: Switching Waveforms

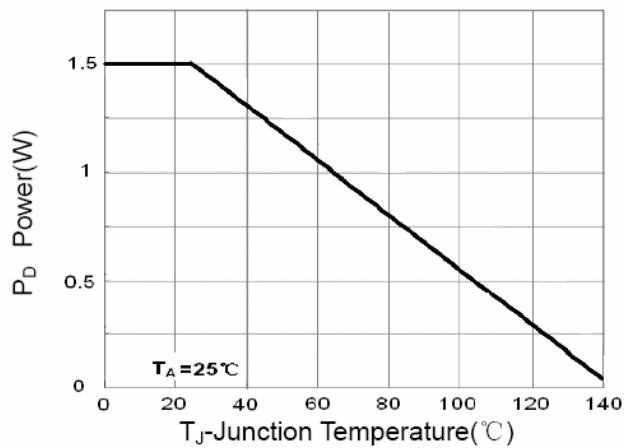


Figure 3 Power Dissipation

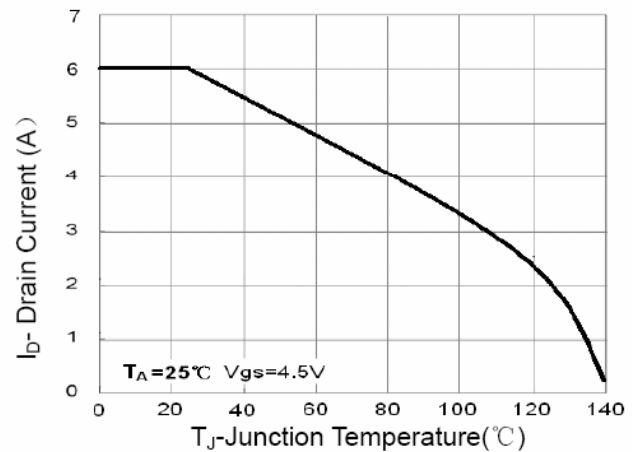


Figure 4 Drain Current

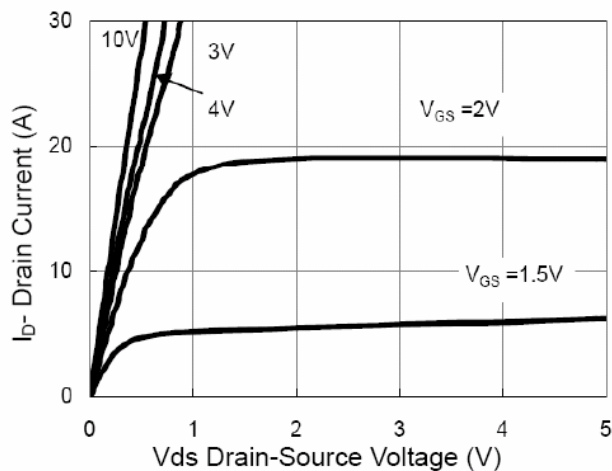


Figure 5 Output CHARACTERISTICS

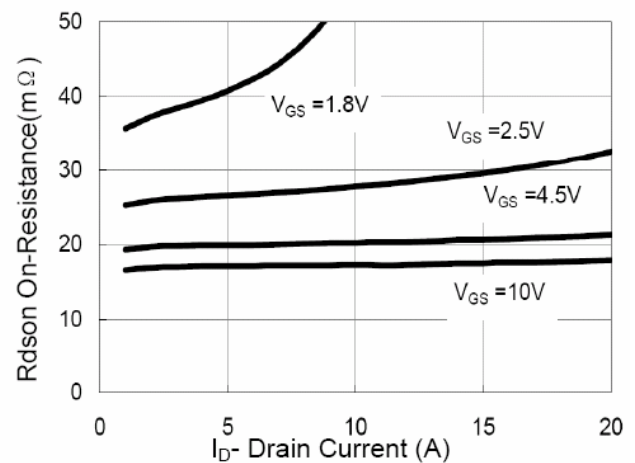


Figure 6 Drain-Source On-Resistance

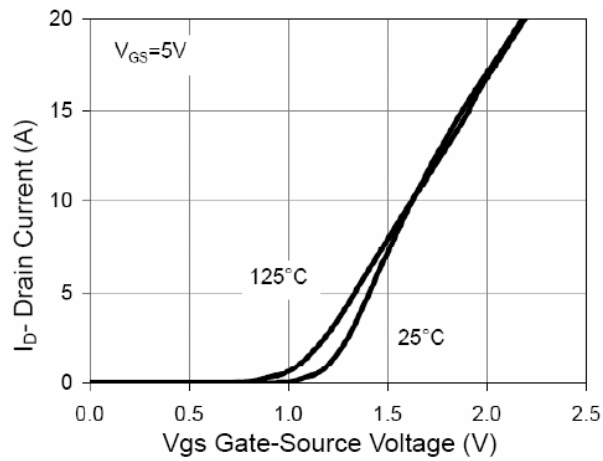


Figure 7 Transfer Characteristics

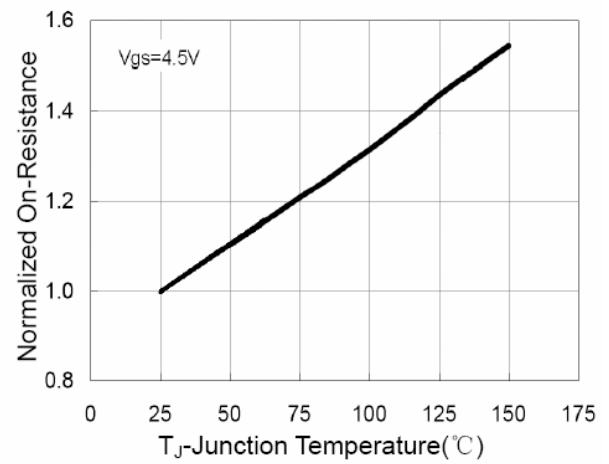


Figure 8 Drain-Source On-Resistance

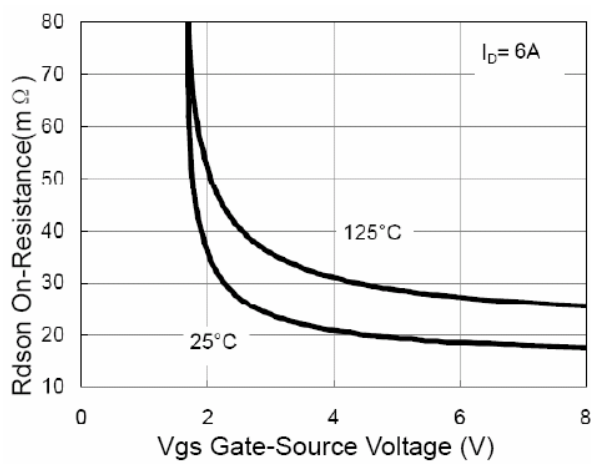


Figure 9 Rdson vs Vgs

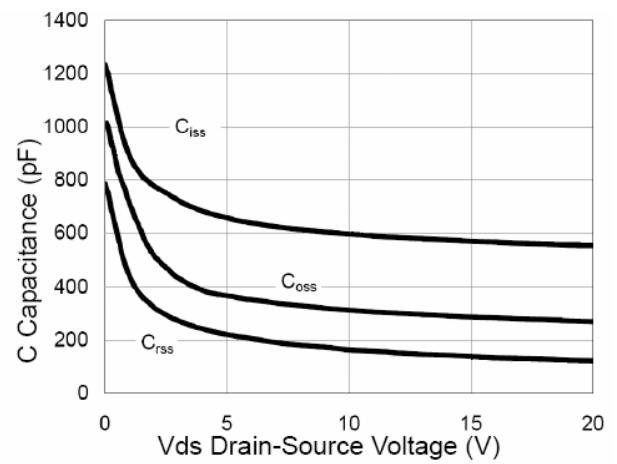


Figure 10 Capacitance vs Vds

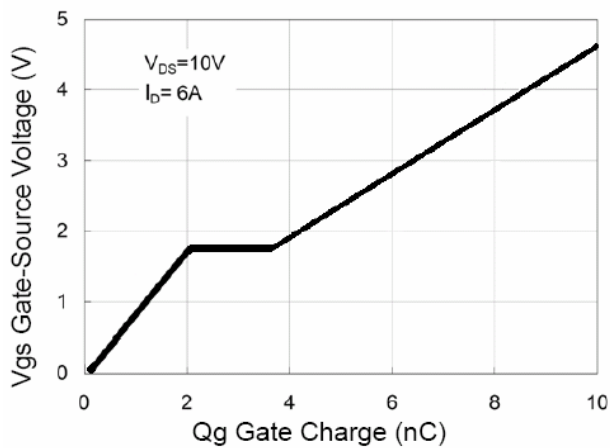


Figure 11 Gate Charge

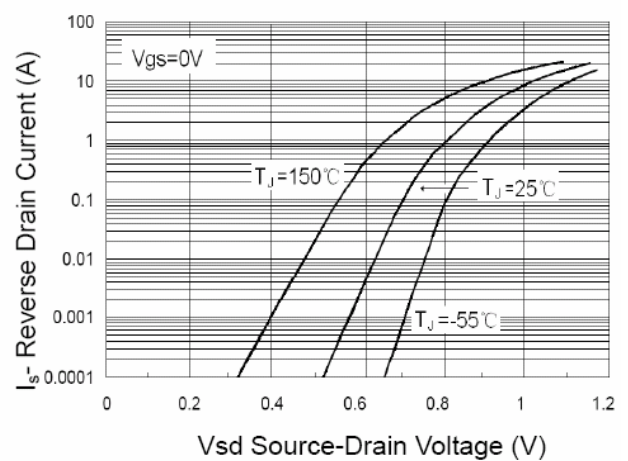
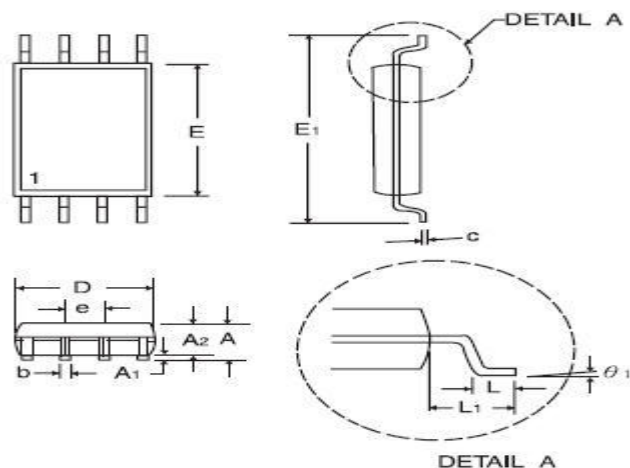


Figure 12 Source- Drain Diode Forward



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.05	1.20	0.041	0.047
A1	0.05	0.15	0.002	0.006
A2	-	1.05	-	0.041
b	0.20	0.28	0.008	0.011
c	0.127		0.005	
D-8	2.90	3.10	0.114	0.122
E	4.30	4.50	0.169	0.177
E1	6.20	6.60	0.244	0.260
e	0.65BSC		0.025BSC	
L	0.50	0.70	0.020	0.028
L1	1.00		0.039	
$\theta_1$	0°	8°	0°	8°

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