### **General description**

The AMS1117 is a low-dropout linear regulator that operates in the input voltage range from +2.5V to +7.0V and delivers 1.0A output current.

The AMS1117 is available in two types, fixed output voltage type or adjustable output voltage type. The fixed output voltage type is preset at an internally trimmed voltage 1.8V, 2.5V, or 3.3V. Other options 1.5V, 2.85V, 3.0V and 3.6V are available by special order only. The output voltage range of the adjustable type is from 1.25V to 5V.

The AMS1117 consists of a 1.25V bandgap reference, an error amplifier, and a P-channel pass transistor. Other features include short-circuit protection and thermal shutdown protection. The AMS1117 devices are available in SOT-223 and TO-252 packages.

### **Features**

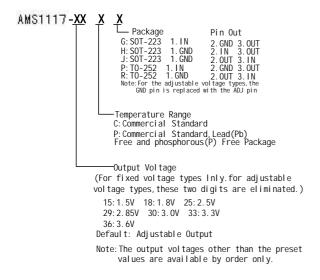
- ➤ Low dropout voltage 700mV at 1.0A typ.
- Adjustable output voltage (AMS1117-Cx) or fixed output voltage (AMS1117-xxCx) preset at 1.8V, 2.5V, or 3.3V
- High output voltage accuracy
- $\triangleright$  Fixed output voltage:  $\pm 35 \text{mV}$
- ➤ Adjustable output voltage: ±50mV
- > Small output capacitor
- Output current limit
- Thermal overload shutdown protection
- ➤ SOT-223 and TO-252 packages

## **Applications**

- ➤ Active SCSI terminators
- ➤ High efficiency linear regulators
- Monitor microprocessors
- ➤ Low voltage micro-controllers
- Post regulator for switching power

## AMS1117(文件编号: S&CIC0490) 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

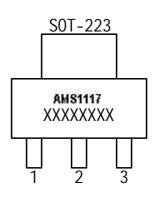
## **Ordering information**

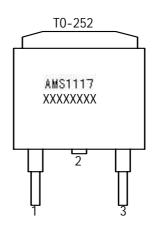


### Pin configurations

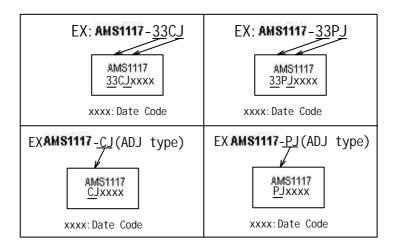
Part No.	Pin1 Pin2		Pin3
AMS1117-XXX0	G IN	GND/AD (TAP)	JOUT
AMS1117-XXXI	H GND/AD	IN J (TAP)	OUT
AMS1117-XXX.	J GND/AD	J OUT (TAP)	IN

Part No.	Pin1	Pin2	Pin3
AMS1117-XXXI	P IN	GND/AD (TAP)	JOUT
AMS1117-XXXI	R GND/AD	J OUT (TAP)	IN





## Package marking information



## Pin description

Part No	Symbol	Description	
AMS1117-xxCG	GND/ADJ	Ground pin or adjust terminal pin	
AMS1117-xxCH	IN	Regulator input pin	
AMS1117-xxCJ			
AMS1117-xxCP	OUT	Regulator output pin	
AMS1117-xxCR			

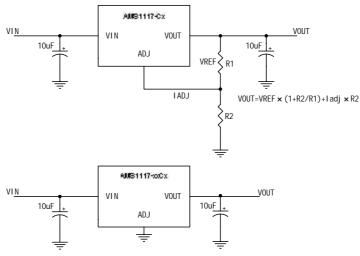
**IN** is the regulator input pin. Supply voltage can range from 2.5V to 7.0V. Bypass with a 10uF capacitor to GND.

**OUT** is the output voltage pin. Sources up to 1.0A. Bypass with a 10uF capacitor to GND.

**GND** provides the reference for all voltages.

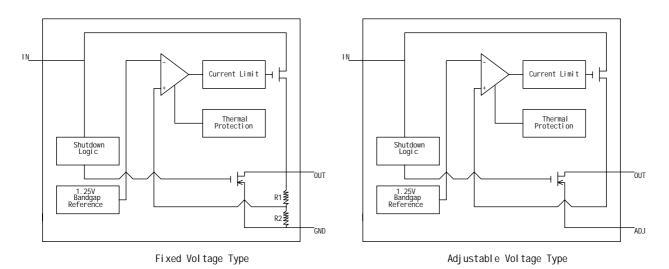
ADJ provides V<sub>REF</sub>=1.25V (TYP.) for adjustable output voltage.

## Typical application circuit



## AMS1117(文件编号: S&CIC0490) 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

# **Functional block diagrams**



## **Absolute maximum ratings**

Input voltage $V_{\rm IN}$ to GND	9V	
Output current limit, I <sub>(LIMIT)</sub>	1.3A	<b>L</b>
Continuous power dissipation	Inter	nally limited
Junction temperature, T <sub>J</sub>	+155	
Storage temperature range, T <sub>STG</sub>	55	to +150
Operating junction temperature range	40	to +125
Lead temperature (soldering, 10sec)	-260	

<sup>\*</sup> Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and function operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

# 

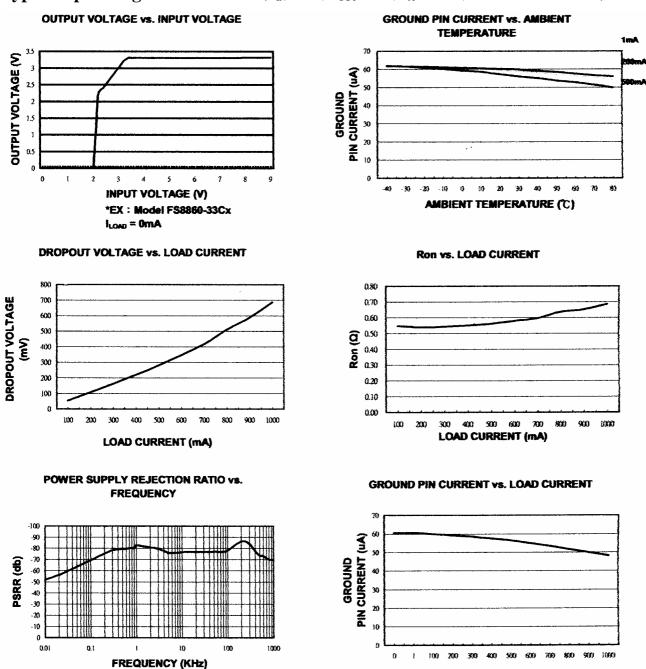
# $\label{eq:continuous} \textbf{Electrical characteristics}(C_{IN}\!\!=\!\!10 \text{uF},\,C_{OUT}\!\!=\!\!10 \text{uF},\,T_{A}\!\!=\!\!25 \quad \text{, unless otherwise noted.)}$

Symbol	Parameter	Test Conditions			Тур	Max	Unit
V <sub>IN</sub>	Input voltage			2.5		7.0	V
		Fixed voltage type		V <sub>OUT</sub> -	V	V <sub>OUT</sub> +	V
V <sub>OUT</sub>	Output voltage	$V_{IN}=$	V <sub>OUT</sub> +1.0V, I <sub>OUT</sub> =1mA	0.035	V <sub>OUT</sub>	0.035	v
V OUT	Output voltage	Adjustable voltage type		1.2	1.25	1.3	V
			V <sub>OUT</sub> +1.2V, I <sub>OUT</sub> =1mA	1.2	1.25		,
		V <sub>IN</sub> >V <sub>OUT</sub> +1.0V, V <sub>IN</sub> 7V(fixed voltage type)		-35		+35	mV
$V_{OUT}$	Output voltage accuracy	V <sub>IN</sub> >V <sub>OUT</sub> +1.2V, V <sub>IN</sub> 7V(adjustable voltage		-50		+50	mV
			type)				
$I_{MAX}$	Maximum output current			1.0			A
I <sub>LIMIT</sub>	Current limit					1.3	A
			V <sub>IN</sub> >V <sub>OUT</sub> +1.0V, (fixed voltage		650	760	mA
$I_{SC}$	Short-circuit current	$V_{OUT}=0V$	type)				
		001	V <sub>IN</sub> >V <sub>OUT</sub> +1.2V, (adjustable				
			voltage type)				
$I_Q$	Ground pin current		$MA \text{ to } 1A, V_{IN} = V_{OUT} + 1.0V$		65	90	uA
$I_{ADJ}$	ADJ pin current	I <sub>LOAD</sub> =01	$MA \text{ to } 1A, V_{IN} = V_{OUT} + 1.2V$		65	90	uA
	Dropout voltage		I <sub>OUT</sub> =100mA		60	100	mV
$V_{DROP}$	(fixed output voltage	I <sub>OUT</sub> =500mA			300	500	mV
	version)	I <sub>OUT</sub> =1.0A			700	1000	mV
		$V_{OUT}+1.0V < V_{IN} < 7V, I_{LOAD}=1 \text{mA}$		0.2	0.3	%/V	
$V_{LINE}$	Line regulation	(fixed voltage type)					, , ,
LINE		$V_{OUT}+1.2V < V_{IN} < 7V, I_{LOAD}=1 \text{ mA}$			0.2	0.3	%/V
			(adjustable voltage type)				
	Load regulation	$I_{OUT}=0$ mA to 1.0A			0.02	0.03	%/mA
$V_{LOAD}$		(fixed voltage type)					
EOND		$I_{OUT}=0$ mA to 1.0A			0.1	0.15	%/mA
		(adjustable voltage type)					
EN	Output noise	F=1Hz to 10KHz, C <sub>OUT</sub> =10uF			80		U/V <sub>RMS</sub>
PSRR	Ripple rejection	F=10KHz, C <sub>OUT</sub> =10uF			75		dB
$T_{\mathrm{SD}}$	Thermal shutdown			155			
	Temperature						
$T_{HYS}$	Thermal shutdown				20		
-1113	Hysteresis				_~		
	Thermal resistance(NO		SOT-223		155		/W
JA	Heat-sink, No air flow)	at-sink, No air flow) TO-252			90		/W

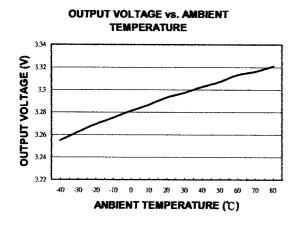
## 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

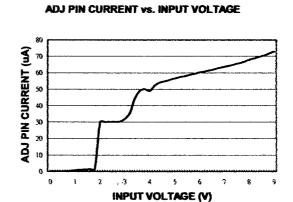
LOAD CURRENT (mA)

**Typical operating characteristics**( $C_{IN}$ =10uF,  $C_{OUT}$ =10uF,  $T_A$ = +25 , unless otherwise noted.)

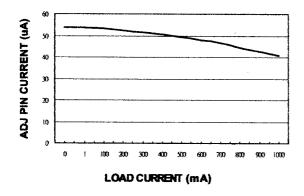


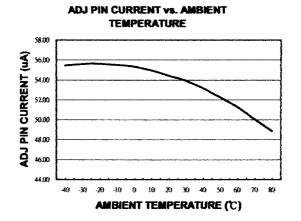
# 1.0A Adjustable & Fixed Voltage LDO Linear Regulator



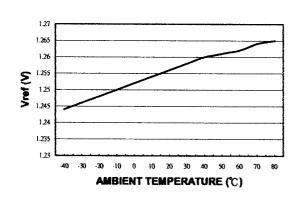


### ADJ PIN CURRENT vs. LOAD CURRENT

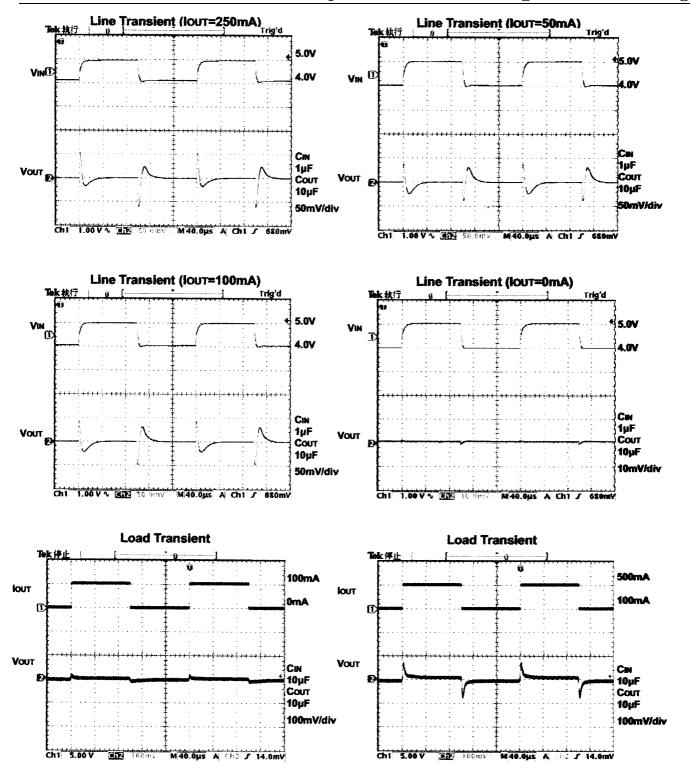




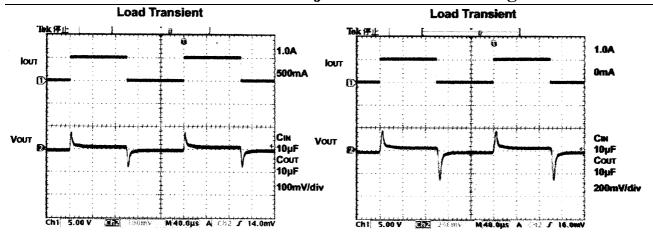
### Vref vs. AMBIENT TEMPERATURE



# Output Noise DC to 1MHz Ch1 1.389kHz Ch1 72.384V M 20988 A Ch1 / 3448V R cod=0



### 1.0A Adjustable & Fixed Voltage LDO Linear Regulator



### **Detail description**

The AMS1117 is a low-dropout linear regulator. The device provides preset 1.8V, 2.5V and 3.3V output voltages for output current up to 1.0A. adjustable output voltage and other mask options for special output voltages are also available. As illustrated in function block diagram, it consists of a 1.25V bandgap reference, an error amplifier, a P-channel pass transistor and an internal feedback voltage divider (fixed voltage types).

The 1.25V bandgap reference is connected to the eror amplifier, which comares this reference with the feedback voltage and amplifies the voltage difference. If the feedback voltage is lower than the reference voltage, the pass-transistor gate is pulled lower, which allows more current to pass to the output pin and increases the output voltage. If the feedback voltage is too high, the pass-transistor gate is pulled up to decrease the output voltage.

The output voltage is feed back through an internal resistive divider (or external resistive divider for adjustable output voltage type) connected to OUT pin. Additional blocks include an output current limiter, thermal sensor, and shutdown logic.

### **Internal P-channel pass transistor**

The AMS1117 features a P-channel MOSFET pass transistor. Unlike similar designs using PNP pass transistors, P-channel MOSEFTS require no base drive, which reduces ground pin current. PNP-based regulators also waste considerable current in dropout when the pass transistor saturates, and use high base-drive currents under large loads. The AMS1117 does not suffer from these problems and consumes only 65uA(Typ) of fround pin current under heavy loads as well as in dropout conditions.

### **Output voltage selection**

For fixed voltage type of AMS1117, the output voltage is preset at an internally trimmed voltage. The first two digits of part number suffix identify the output voltage (see *Ordering Information*). For example, the AMS1117-33CJ has a preset 3.3V output voltage.

For adjustable voltage type of AMS1117, the output voltage is set by comparing the feedback voltage at adjust terminal to the internal bandgap reference voltage. The reference voltage  $V_{REF}$  is 1.25V. The output voltage is given by the equation:

 $V_{OUT} = V_{REF} * (1 + R2/R1) + I_{ADJ} * R2$ 

(see Typical Application Schematic)

## AMS1117(文件编号: S&CIC0490) 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

### **Current limit**

The AMS1117 also includes a fold back current limiter. It monitors and controls the pass transistor's gate voltage, estimates the output current, and limits the output current within 1.3A.

### Thermal overload protection

Thermal overload protection limits total power dissipation is the AMS1117. When the junction temperature exceeds  $\pm 155$ , a thermal sensor turns off the pass transistor, allowing the IC to cool down. The thermal sensor turns the pass transistor on again after the junction temperature cools down by 20, resulting in a pulsed output during continuous thermal overload conditions.

### Operating region and power dissipation

Maximum power dissipation of the AMS1117 depends on the thermal resistance of the case and circuit board, the temperature difference between the die junction and ambient air, and the rate of airflow. The power dissipation across the devices is  $P=I_{OUT} \times (V_{IN}-V_{OUT})$ . The resulting maximum power dissipation is:

$$P_{MAX} = (T_I - T_A)/(I_C + I_C) = (T_I - T_A)/I_A$$

Where  $(T_J-T_A)$  is the temperature difference between the AMS1117 die junction and the surrounding air,  $_{JC}$  is the thermal resistance of the package chosen, and  $_{CA}$  is the thermal resistance through the printed circuit board, copper traces and other materials to the surrounding air. For better heat-sinking, the copper area should be equally shared between the IN, OUT, and GND pins.

If the AMS1117 uses a SOT-223 package and this package is mounted on a double sided printed circuit board with two square inches of copper allocated for "neat spreading", the resulting JA is 80 /W.

Based on the maximum operating junction temperature 125 with an ambient of 25 , the maximum power dissipation will be:

$$P_{MAX} = (T_J - T_A)/(I_{C} + I_{C}) = (125 - 25)/80 = 1.25W$$

Thermal characteristics were measured using a double-sided board with 1"  $\times$  2" square inches of copper area connected to the GND pin for "heat spreading".

### **Input-output voltage**

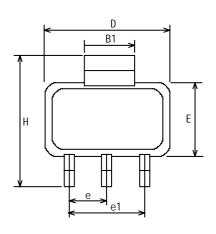
A reguator's minimum input-output voltage differential, or dropout voltage, determines the lowest usable supply voltage. In battery-powered systems, this will determine the useful end-of-life battery voltage. The AMS1117 uses a P-channel MOSEFT pass transistor, its dropout voltage is a function of drain-to-source on-resistance ( $R_{DS(ON)}$ ) multiplied by the output current.

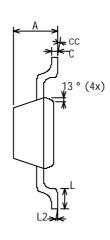
$$V_{DROPOUT}=V_{IN}-V_{OUT}=R_{DS(ON)}$$
 ×  $I_{OUT}$ 

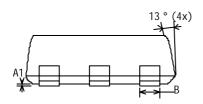
# 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

# Package outline

## SOT-223



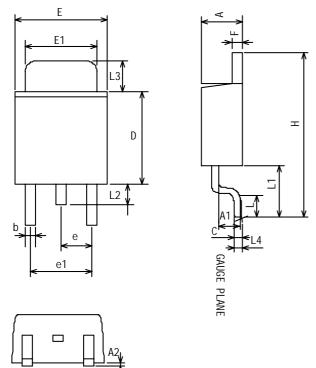




SYMBDLS	MILLIMETERS		DNCHES		
	MIN	MAX	MIN	MAX	
A	1.55	1.90	0.061	0.071	
A1	0.02	0.12	0.0008	0.0047	
В	0.60	0.80	0.024	0.031	
B1	2.90	3.10	0.114	0.122	
С	0.24	0.32	0.009	0.013	
D	6.20	6.70	0.248	0.264	
Е	3.30	3.70	0.120	0.146	
e	2.30	2.30 BSC		0.090 BSC	
e1	4.60 BSC		0.181 BSC		
Н	6.70	7.30	0.264	0.287	
L	0.90 MIN		0.026 MIN		
L2	0.06 BSC		0.0024 BSC		
CC	0°	10 °	0° 10°		

# 1.0A Adjustable & Fixed Voltage LDO Linear Regulator

TO-252



C1 1	MILLIN	1ETERS	DNCHES		
Symbol	MIN	MAX	MIN	MAX	
A	2.19	2.38	0.086	0.094	
A1	0.99	1.27	0.035	0.050	
A2	0.00	0.13	0.00	0.005	
b	0.51	0.99	0.02	0.035	
c	0.48	0.58	0.18	0.023	
D	5.97	6.22	0.235	0.245	
Е	6.36	6.73	0.250	0.265	
E1	5.21	5.48	0.205	0.215	
e	2.28	BSC	0.090 BSC		
e1	3.98	5.16	0.156	0.204	
F	0.48	0.58	0.018	0.023	
L	1.40	1.78	0.055	0.070	
L1	2.67(REF)		0.105(REF)		
L2	0.64	1.02	0.025	0.040	
L3	1.52	2.03	0.030	0.050	
L4	0.51 BSC		0.020BSC		
Н	9.40	10.4	0.370	0.410	
Q	0 °	8°	0 °	8°	