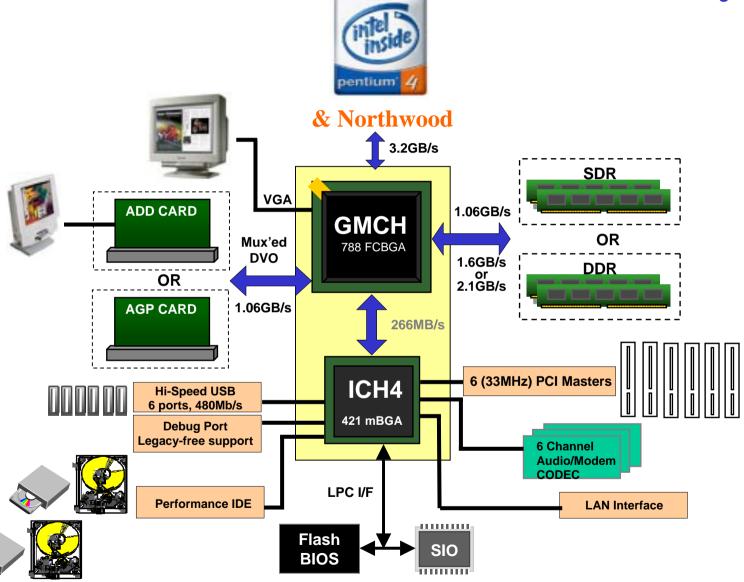


AOS for Mother Board Solutions

May 2003



P SEMICONDUCT Brookdale-G Feature Summary





ATX POWER CONNECTOR & Voltage Tolerances

Table 3. DC Output Voltage Regulation

Output	Range	Min.	Nom.	Max.	Unit
+12VDC ⁽¹⁾	±5%	+11.40	+12.00	+12.60	Volts
+5VDC	±5%	+4.75	+5.00	+5.25	Volts
+3.3VDC	±5%	+3.14	+3.30	+3.47	Volts
-5VDC	±10%	-4.50	-5.00	-5.50	Volts
-12VDC	±10%	-10.80	-12.00	-13.20	Volts
+5VSB	±5%	+4.75	+5.00	+5.25	Volts

⁽¹⁾ At +12 VDC peak loading, regulation at the +12 VDC output can go to ± 10%.

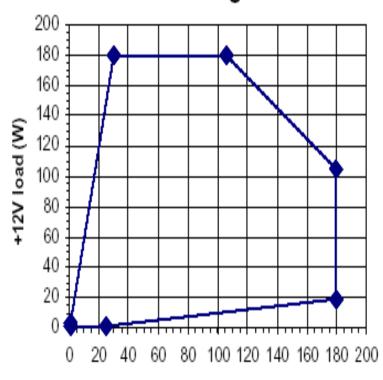
ALSO MAIN 3.3V SENSE)	3.3V	(I) [I]	3.3V
	-12V	@ @	3.3V
	COM	® ③	COM
	PS-ON	(1) (1)	5V
	COM	15 (5)	COM
	COM	66	5V
	COM	0 0	COM
	-5V	18 8	PW-OK
	5V	19 9	5VSB
	5V	199 (19	12V



ATX Power Disspation

Table 10. Typical Power Distribution for a 300 W ATX12V Configuration

Output	Min. Current (amps)	Max. Current (amps)	Peak Current (amps)
+12 VDC	0.0	15.0	18.0
+5 VDC	0.3	30.0	
+3.3 VDC	0.3	28.0	
-12 VDC	0.0	0.8	
+5 VSB	0.0	2.0	2.5



See graph at right for power sharing.

+5V & +3.3V combined load (W)



Timing of Power ON, Power OK

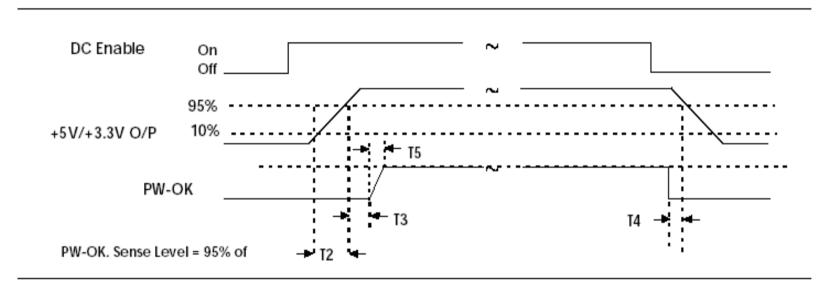


Figure 12: Timing of PS-ON, PW-OK, and Germane Voltage Rails

Although there is no requirement to meet specific timing parameters, the following signal timings are recommended:

$$2\text{ms} \le T_2 \le 20 \text{ ms}$$

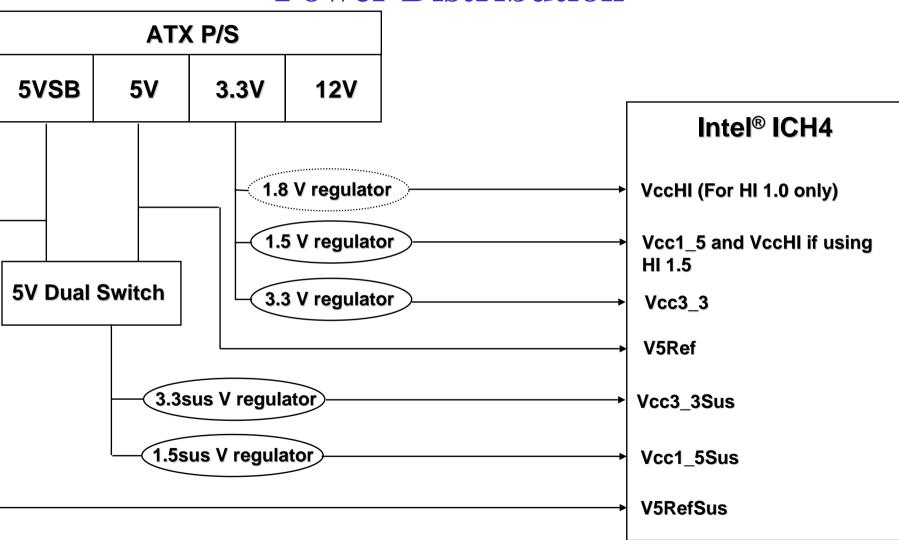
100 ms < T₃ < 2000 ms

$$T_4 > 1 \text{ ms}$$

 $T_s \le 10 \text{ms}$

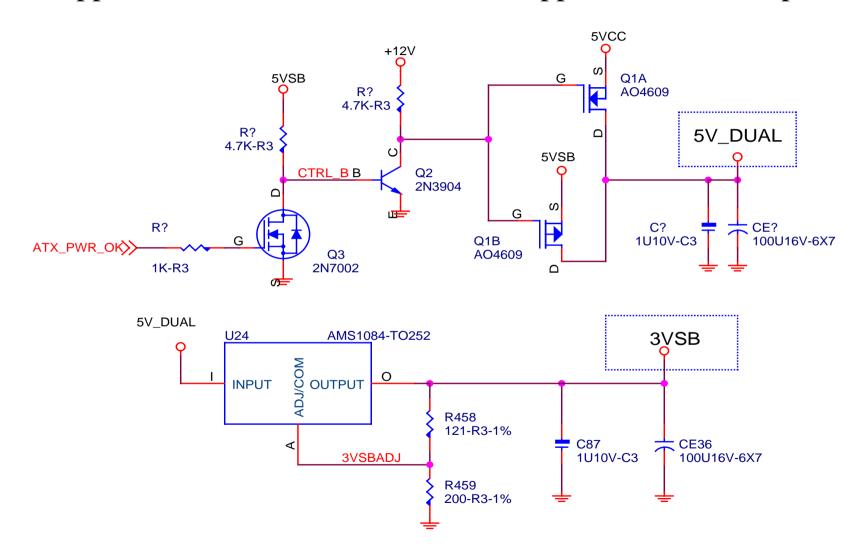


Power Distribution



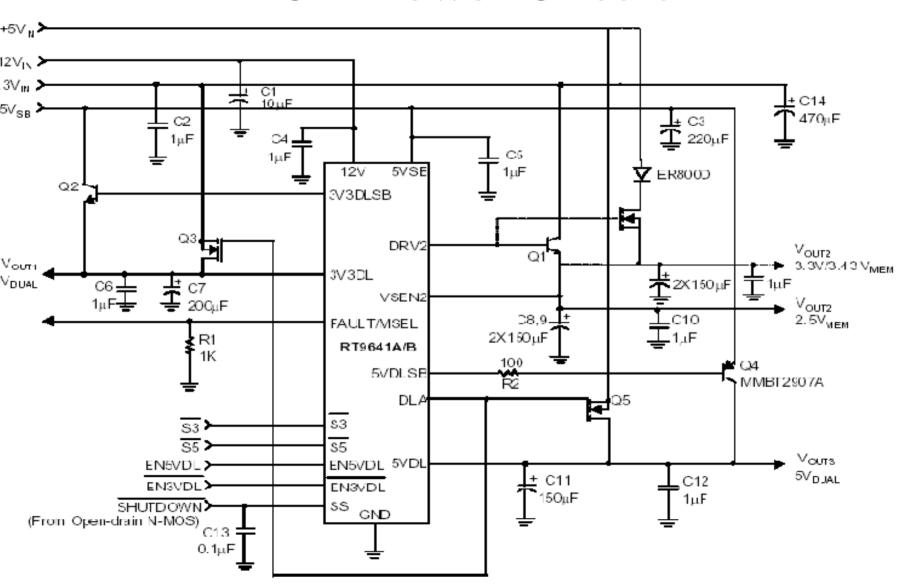


Application For 8 Channel USB & Support LAN wake up



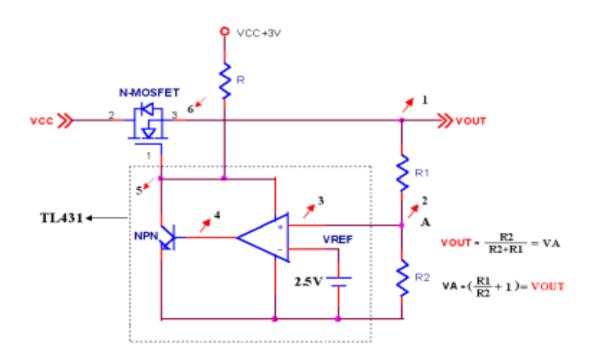


ACPI Power Circuit



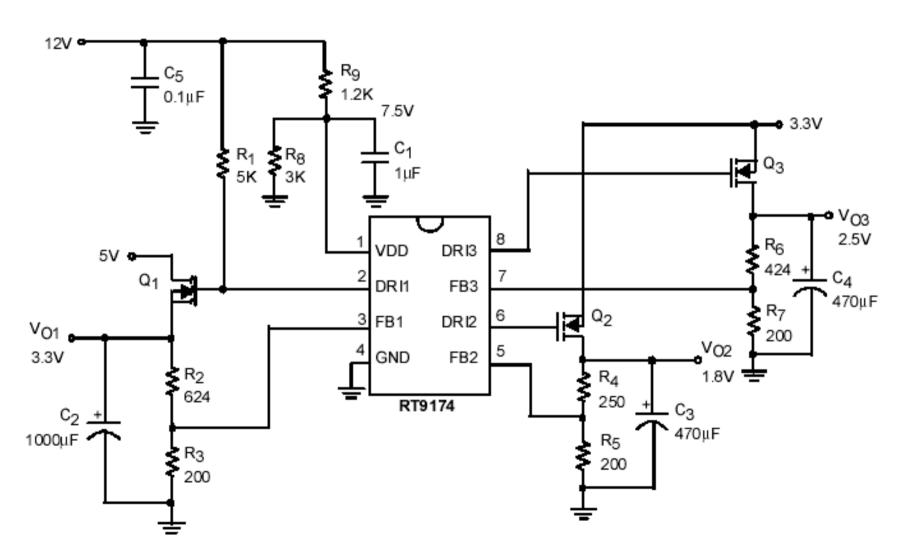


普遍的線性電路 (TL-431+3055)



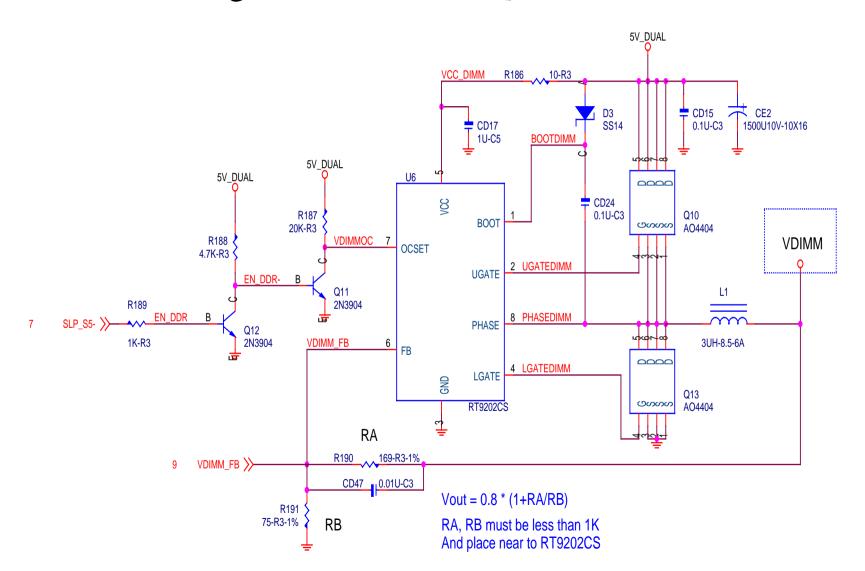


Triple LDOs Driving MOS Application



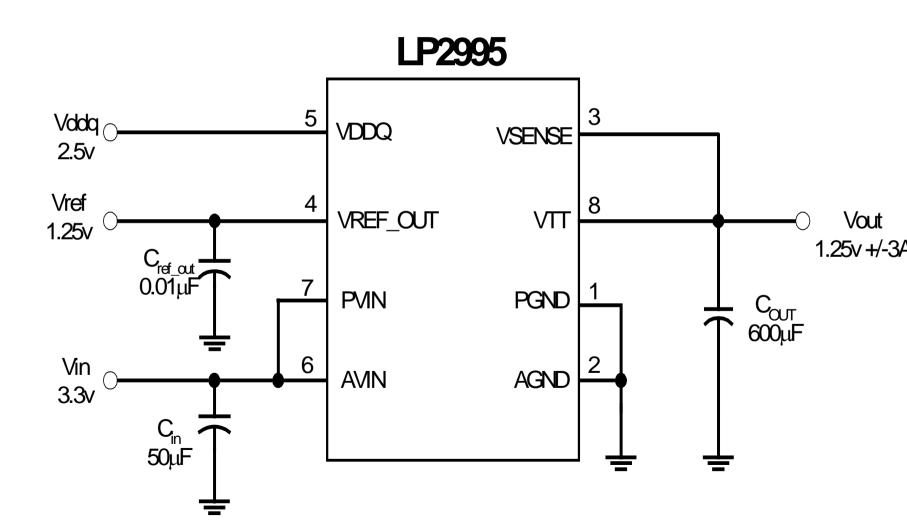


DDR Voltage or VGA VDDQ For PWM Solution





DDR Termination Voltage





AGP Power supply limit

Table 4-14: Add-in Card Power Supply Limits

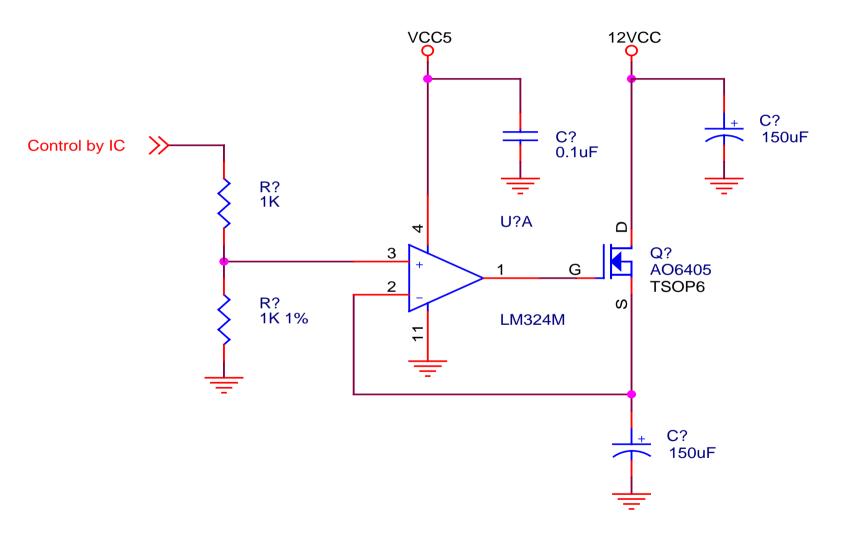
Symbol	Parameter	Condition	Min	Max	Units	Notes
Vddq1.5	I/O Supply Voltage	I _{MAX} = 8.0 A	1.425	1.575	٧	1
Vddq3.3	I/O Supply Voltage	I _{MAX} = 8.0 A	3.15	3.45	٧	1
VCC3.3	3.3 V Power Supply	I _{MAX} = 6.0 A	3.15	3.45	٧	
VCC5	5 V Power Supply	I _{MAX} = 2.0 A	4.75	5.25	٧	
VCC12	12 V Power Supply	I _{MAX} = 1.0 A	11.4	12.6	٧	

Note:

 The Vddq current is due mostly to the AC switching transients of the A.G.P. I/O buffers. The maximum current listed will not be seen in practice, but represents the current carrying capability of the connector. Actual average currents will be less than 2.0 A.



Fan Control Circuit





TO-252 Efficiency Report 1

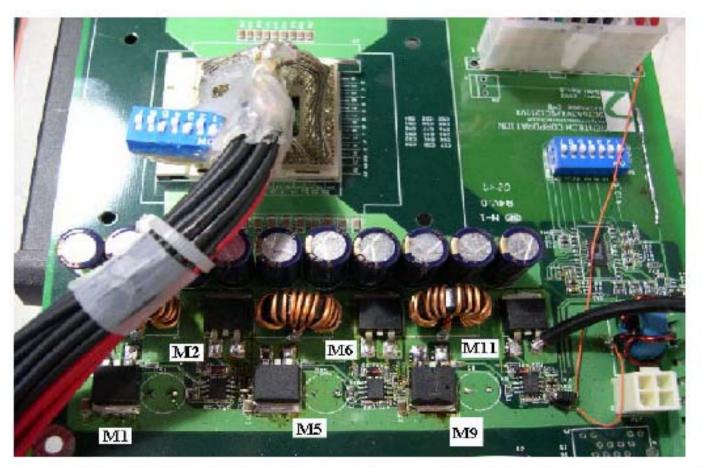


Fig1. Controller sc2643, Driver=sc1211. Three phase solution of SEMTECH.



TO-252 Efficiency Report 2

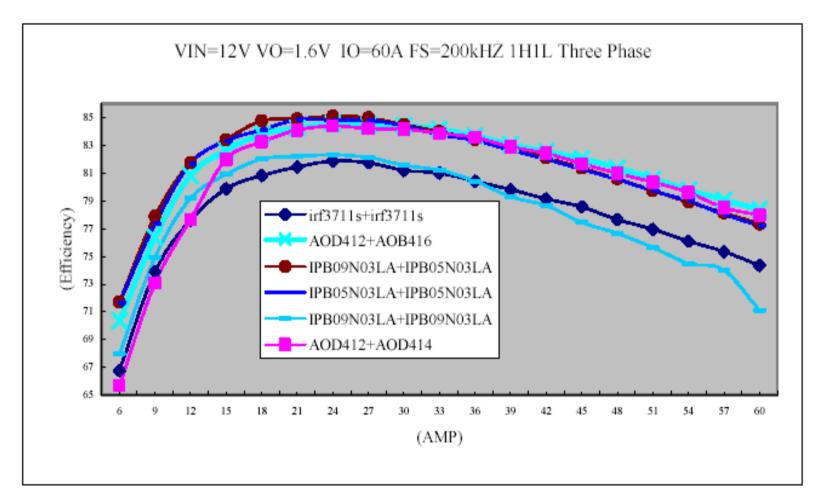


Fig2. Efficiency comparison



TO-252 Efficiency Report 3

$V_{IN}=12V$	M9 HS	M11 LS	M5 HS	M6 LS	M1 HS	M2 LS
irf3711s+irf3711s	116.8℃	99°C	129.3℃	135℃	97.3℃	129.3℃
AOD412+AOB416	102.9℃	89.7℃	112.7℃	102℃	89℃	96.1℃
AOD412+AOD414	106.3℃	102.1℃	111.5℃	121.1℃	87.9℃	113.6℃
IPB09N03LA+IPB05N03LA	103.8℃	91℃	112.2°C	108.2℃	92.5℃	103.5℃
IPB09N03LA+IPB09N03LA	116℃	115.6℃	124.5℃	132.6℃	103.2℃	136.6℃
IPB05N03LA+IPB05N03LA	100°C	93.9℃	114.2°C	113.1℃	92.6°C	104.5℃

Table 1. Io=60A (20A/per phase), T_A =25°C

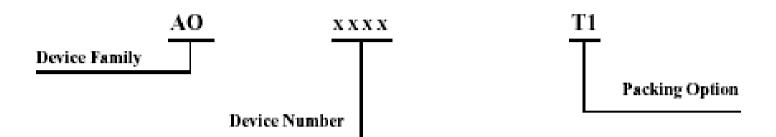


AOS Mosfet Selection Guide

Part	Part Package Description		Vgs Ids		F	Pd Rds(on)/max				Vgs(th)/	Ciss	Crss	Qg	Qgd	Td(on)	Td(off) /			
number	rackage	Description	מעו	(+/-)	25'C	70'C	25' C	70'C	10V	4.5V	2.5V	1.8V	max	/pF	/pf	/ nC	/ nC	/ Ns	nS
TO-252																			
AOD402	TO252	Single-N	30	25	18	12	60	30	18	44			3	769	131	15.9	4.92	6.2	16
AOD404	TO252	Single-N	30	12	85	65	100	50	7	8			2	2100	165	19.7	7.9	5.9	36.2
AOD406	TO252	Single-N	30	12	85	75	100	50	5	5.7			1.5	9130	387	72.4	16.8	14.7	105.5
AOD408	TO252	Single-N	30	20	20	18	60	30	16	26			3	1040	110	19.8	3.5	4.5	17.5
AOD410	TO252	Single-N	30	20	8	6	4.2	2.6	65	100			3	288	39	6.72	1.78	3.7	15.6
AOD412	TO252	Single-N	30	20	85	65	100	50	7	10			2.5	1320	154	26	6.6	7.2	22
AOD414	TO252	Single-N	30	20	85	65	100	50	5.2	7			2.4	6060	355	96.4	15.6	15.7	55.5
AOD420	TO252	Single-N	30	20	10	8	60	30	25	35			3	710	72	14.4	2.7	5.6	15.6
AOD422	TO252	Single-N	20	8	10	8	50	20		22	26	34	1	1160	146	16	3.8	6.2	51.7



Product Nomination

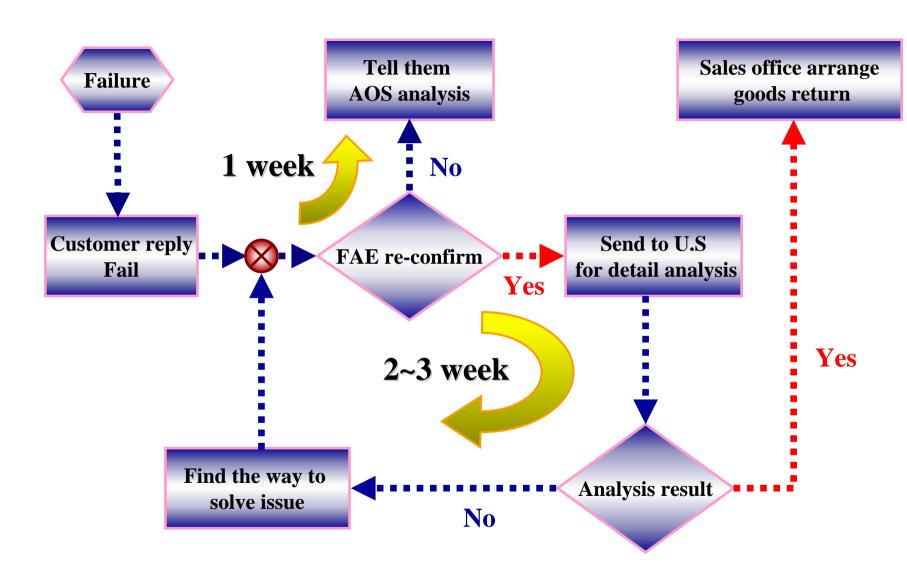


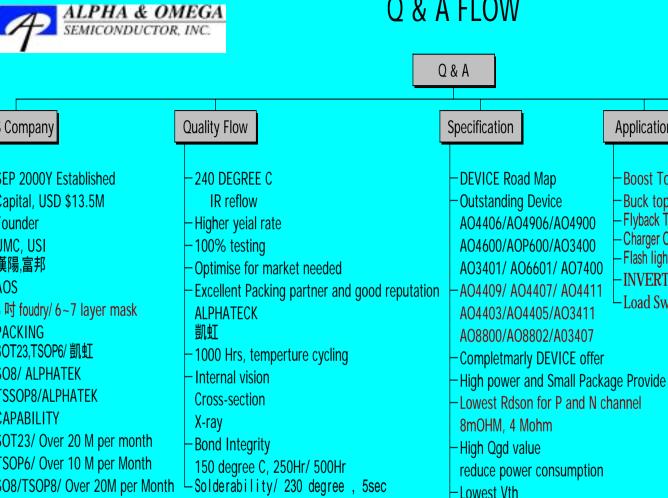
1	Digit	2.00	Digit
4	SOS	4	Single
8	TSSOP8	6	Complemently
6	TSOP6	8	Dual
3	SOT23	9	Three in one
7	SC70	7	With Schottky
2	CSP		
5	ChipFET		
В	D2PAK	3.4	Digit
D	DPAK	- P	en N Channel
P	TO220	nd	

Y

Power SO







Customer Base

ISTRIBUTOR ROMATE **AENN**

POC/ KONNECT

'GA/NB/DSC..MSI, GIGABYTE,CLEVO,UNIWILL,COMPAL,FIC,ECS...

MB/ BP, POWERLINK,TRANS, MONTEK, SONY, BENQ, SAMPLO,GLW

CM, PROVIEW.AOC, SAMPO, SUMIDA, TDK, TAUTANG, MTP

Boost Topology -Total TAM Buck topology USD 400M/ TWN Flyback Topology -Key Supplier **Charger Circuit** -Flash light circuit

Application

It is suitable on one battery cell

Depend on Package and Specification

-Current Rating

INTERSIL/ FDS PHILIPS/ IR/ JAPAN -INVERTER **CET/ SILICONIX** -Load Switch

Market

Key Competitor SOT23 / FDS, SIL, ROHM

TSOP6/ IR. ANPC SO8/TSSOP8/ FDS, SIL, IR, ROHM -Maior market

VGA/ NB/DSC..SOT 23/ TSSOP8 / LCM/IV/BP..TSSOP8/ SO8 MB/OTHERS..TSOP6/ SOT23

-AOS Achievement

2002/ USD 6M

2001/ USD 0.5 M

USD 45M for 2003 YEAR.



Q & A



BYE BYE!!