

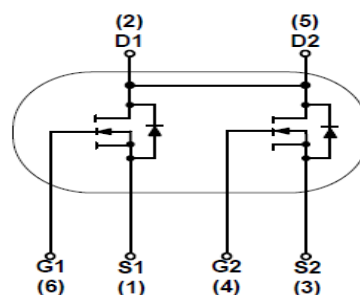
FEATURES

- 20V/6.5A
 $R_{DS(ON)} = 25m\Omega @ V_{GS} = 4.5V$
 $R_{DS(ON)} = 37m\Omega @ V_{GS} = 2.5V$
- High Cell Density
- Lead free and Green Device Available

Application

- Battery pack protection

PIN DESCRIPTION



TSSOP-8 Package

Absolute Maximum Ratings ($T_A = 25^\circ C$ unless otherwise noted)

Symbol	Parameter		Maximum	Unit
V_{DSS}	Drain-to-Source Voltage		20	V
V_{GSS}	Gate-to-Source Voltage		± 10	V
I_D	Continuous Drain Current	$T_C = 25^\circ C$	6	A
		$T_C = 70^\circ C$	4.8	A
I_{DP}	Pulsed Drain Current	$T_C = 25^\circ C$	20	A
PD	Maximum Power Dissipation	$T_C = 25^\circ C$	1.5	
		$T_C = 70^\circ C$	0.9	
T_J, T_{STG}	Junction & Storage Temperature Range		-55~150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typical	Unit
$R_{\theta ja}$	Thermal Resistance-Junction to Ambient	84	$^\circ C/W$

Electrical Characteristics (TA=25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min.	Typ	Max.	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	20	—	—	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =16V, V _{GS} =0V	—	—	1	uA
		T _J =85°C	—	—	10	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	0.5	0.7	1.2	V
I _{GSS}	Gate Leakage Current	V _{GS} =±10V, V _{DS} =0V	—	—	±30	uA
R _{DS(on)} ¹	Drain-Source On-Resistance	V _{GS} =4.5V, I _D =5A		22	25	mΩ
		V _{GS} =2.5V, I _D =3A	—	27	37	
Diode Characteristics						
V _{SD} ¹	Diode Forward Voltage	I _{SD} =5A, V _{GS} =0V	—	0.8	1.1	V
I _S	Diode Continuous Forward Current			5		A
Dynamic Characteristics ²						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =8V Frequency=1MHz	—	600		pF
C _{oss}	Output Capacitance		—	330		
C _{rss}	Reverse Transfer Capacitance		—	140		
t _{d(on)}	Turn-On Delay Time	V _{DD} =10V, I _D =1A, V _{GS} =4.5V R _G =6Ω	—	10		ns
t _r	Turn-On Rise Time		—	11		
t _{d(off)}	Turn-Off Delay Time		—	35		
t _f	Turn-Off Fall Time		—	30		
Gate Charge Characteristics ²						
Q _g	Total Gate Charg	V _{DS} =10V, V _{GS} =4.5V I _D =5A	—	10		nC
Q _{gs}	Gate-to-Source Charge		—	2.3		
Q _{gd}	Gate-to-Drain Charge		—	1.5		

Note: 1: Pulse test; pulse width $\leq 300ns$, duty cycle $\leq 2\%$.

2: Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

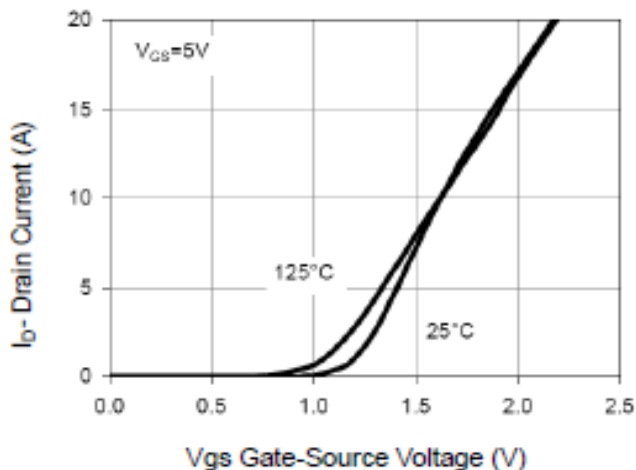


Figure 7 Transfer Characteristics

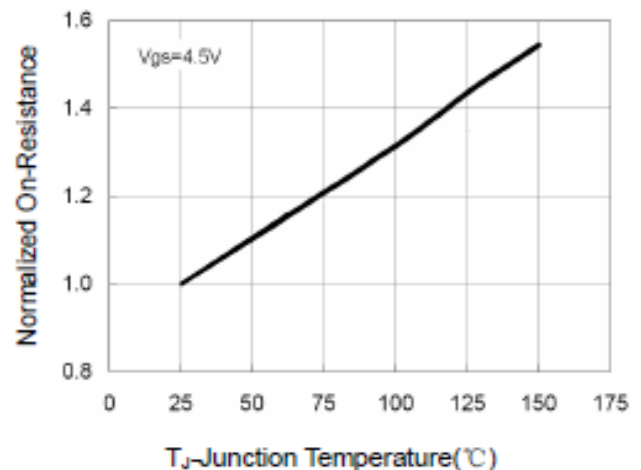


Figure 8 Drain-Source On-Resistance

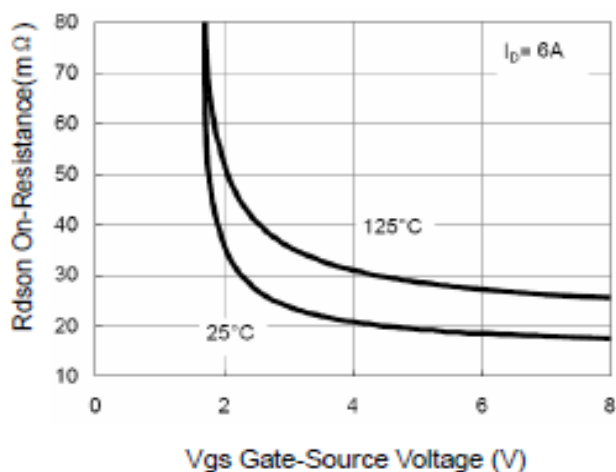


Figure 9 $R_{DS(on)}$ vs V_{GS}

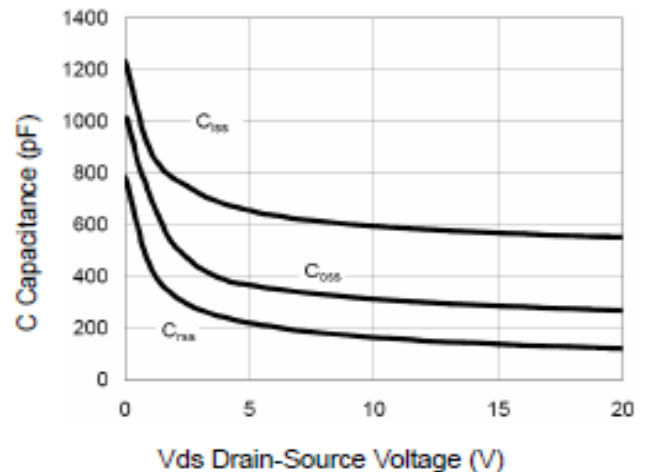


Figure 10 Capacitance vs V_{DS}

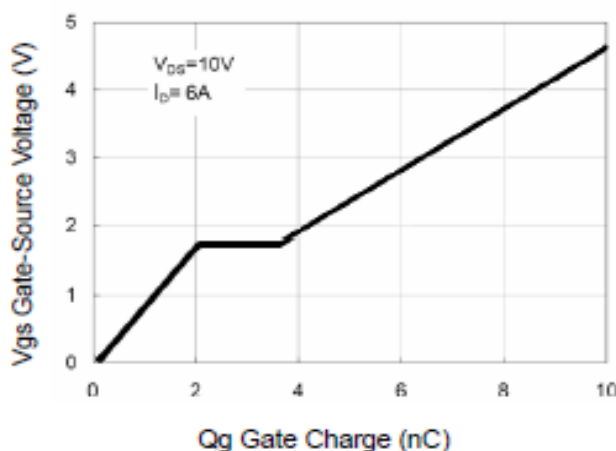


Figure 11 Gate Charge

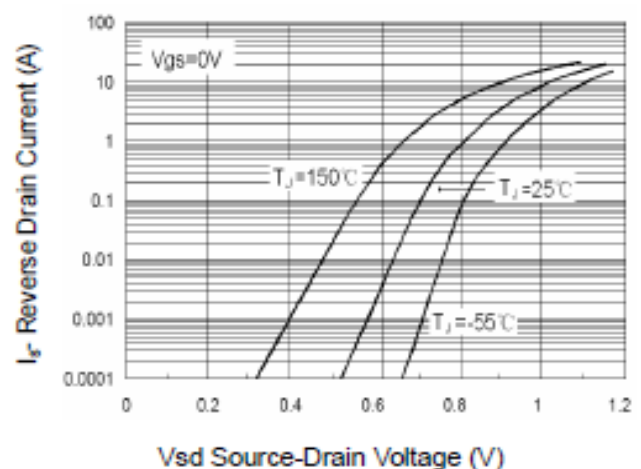


Figure 12 Source- Drain Diode Forward

Typical Operating Characteristics

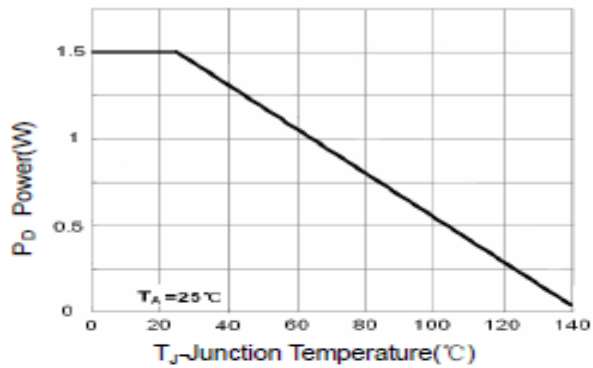


Figure 3 Power Dissipation

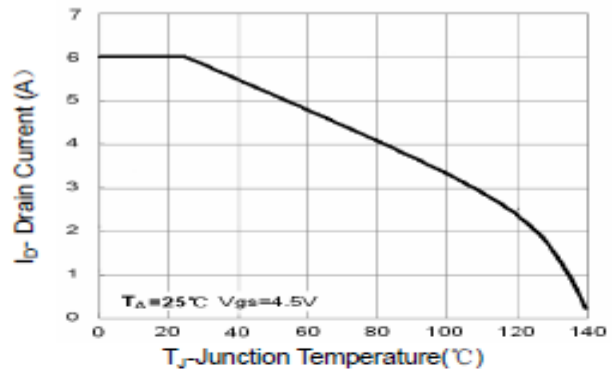


Figure 4 Drain Current

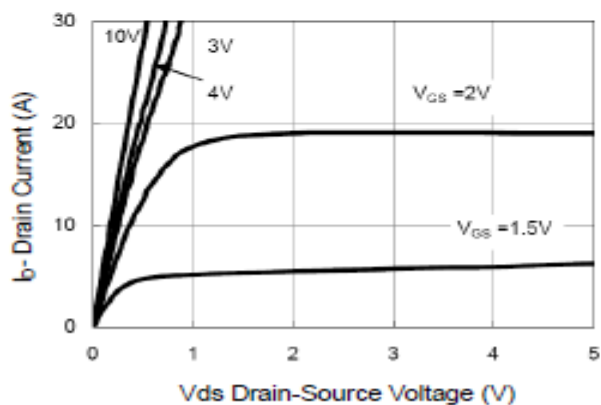


Figure 5 Output CHARACTERISTICS

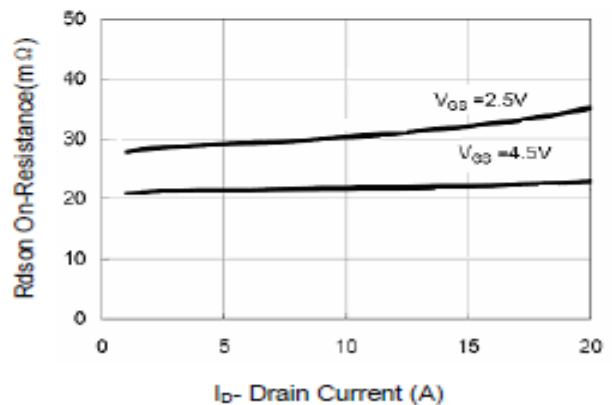


Figure 6 Drain-Source On-Resistance

Typical Operating Characteristics

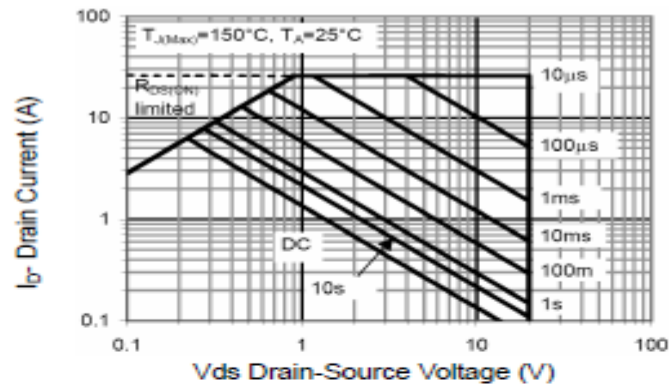


Figure 13 Safe Operation Area

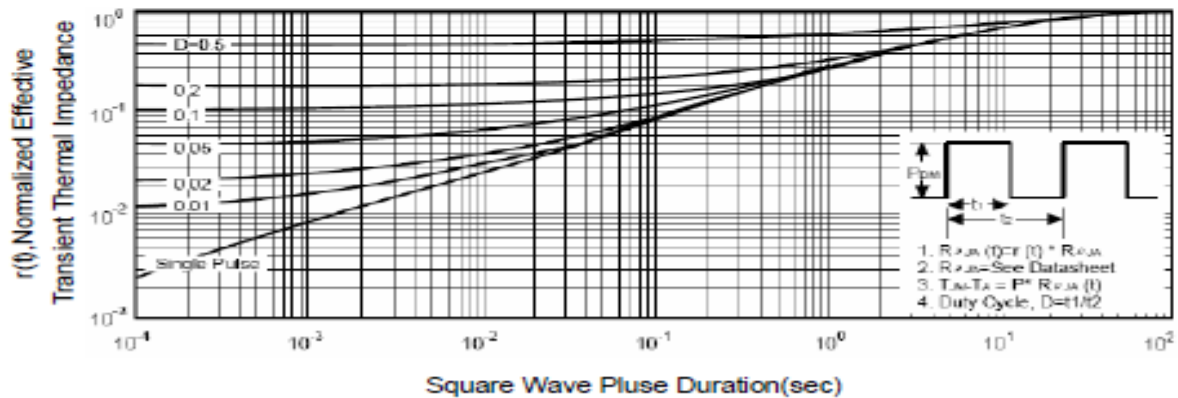


Figure 14 Normalized Maximum Transient Thermal Impedance