Protection of Lithium Ion Batteries (three cells in series) Monolithic IC MM1309

Outline

This is a 3-cell series protection IC is for protecting a lithium ion battery from overcharging and excess discharging. If abnormalities occur during charging and excess voltage is applied, it has a function that turns off the external FET switch (overcharging detection). It also has a function that turns off the external FET switch when the voltage for each battery falls below a set voltage, to prevent excess discharge when discharging the battery (discharging detection). At that time, the IC is switched to low current consumption mode.

These functions comprise a protection circuit, with few external parts, for lithium ion batteries.

Features

1. Current consumption (for Vcc pin)	VCELL=4.4V	700μA typ.
2. Current consumption (for Vcc pin)	VCELL=4.2V	300µA typ.
3. Current consumption (for Vcc pin)	VCELL=3.8V	25μA typ.
4. Current consumption (for Vcc pin)	VCELL=2.2V	0.1μA max.
5. Current consumption (for BATH pin)	VCELL=4.4V	12µA typ.
6. Current consumption (for BATH pin)	VCELL=3.8V	8μA typ.
7. Current consumption (for BATH pin)	VCELL=2.2V	1μA typ.
8. Charge prohibit voltage (Ta=-20°C~+70°C	3)	B: 4.35V±50mV
		C: 4.25V±50mV
9. Charge prohibit release voltage		Vcellu-45mV
10. Charge prohibit detection function operation	B : 4.20V typ.	
		C : 4.10V typ.
11.Excess discharge detection voltage		2.40V±0.09V, 2.35V±0.09V
12.Discharge resumption voltage		B: 2.65V±0.16V
		C: 2.60V±0.16V
13.Excess discharge detection hysteresis volt	age	250±75mV

Package

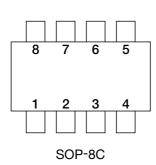
SOP-8C (MM1309 ☐ F)

*The box represents the rank resulting from the combination of protection functions.

Applications

- 1. Notebook PCs
- 2. Portable terminals
- 3. Others

Pin Assignment

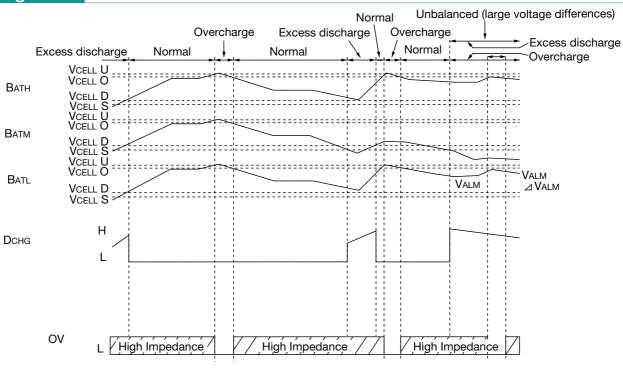


1	Dchg		
2	N. C		
3	OV		
4	GND		
5	Batl		
6	Ватм		
7	Ватм		
8	Vcc		

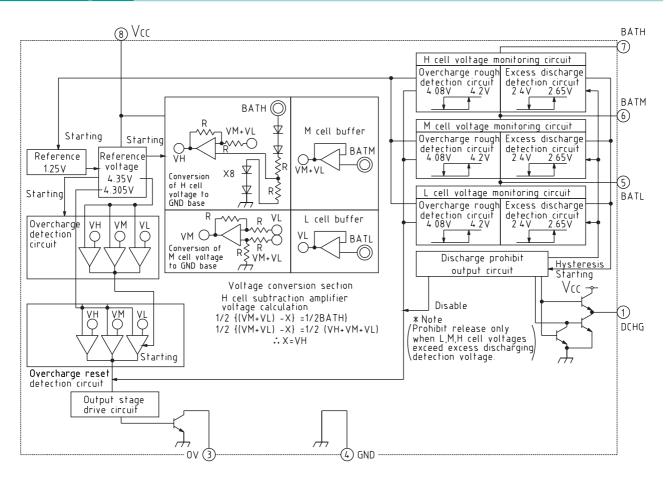
Pin Description

No.	Pin	Output	Function		
1	Dchg	Output	FET drive pin for excess discharge control		
2	N. C				
3	OV	Output	FET drive pin for overcharge control		
1	4 GND		Negative connection pin for the low side battery. Also, IC GND pin.		
4			(IC reference power supply pin)		
5	Dury Input		Batl Input Positive connection pin for the low sid		Positive connection pin for the low side battery, and negative connection pin for the
5 BAIL	DAIL Input	middle side battery.			
6	Batm Input		Positive connection pin for the middle side battery, and negative connection pin for		
O DAIM	DAIM		the high side battery.		
7	Ватм	Input	Positive connection pin for the high side battery.		
8	Vcc		IC positive power supply input pin.		

Timing Chart



Block Diagram



Output Pin Conditions

Voltage	Excess discharge Normal 2.4V/CELL	Overcharge 4.35V/CELL
DCHG	H	L
OV	High Impedance	Ti L

Absolute Maximum Ratings (Ta=25°C)

Item	Symbol	Rating	Units
Storage temperature	Tstg	-40~+125	°C
Operating temperature	Topr	-20~+70	°C
Charging voltage	VBAT max.	15	V
Power supply voltage	Voc max.	15	V
OV2 pin applied voltage	Vo max.	18	V
Allowable power dissipation	Pd	300	mW

Electrical Characteristics (Unless otherwise specified Ta=25°C, VIN=15V, VCELL=VBATH=VBATM=VBATL)

Item		Symbol	Measurement Conditions	Min	Тур.	Max.	Units
Current consumption (Vcc pin) 1	Icc1	Vcell=4.4V		0.7	1.1	mA
Current consumption (Vcc pin) 2	Icc2	Vcell=4.2V		300	450	μA
Current consumption (Vcc pin) 3	Icc3	Vcell=3.8V		25.0	40.0	μA
Current consumption (Vcc pin) 4	Icc4	Vcell=2.3V			0.1	μA
Current consumption (B	BATH pin) 1	Іватн1	Vcell=4.4V		12.0	20.0	μA
Current consumption (B	BATH pin) 2	Іватн2	Vcell=3.8V		8.0	12.0	μA
Current consumption (B	BATH pin) 3	Іватн3	Vcell=2.3V		1.0	2.0	μA
Charge prohibit voltage	MM1309BF	- VcellU	Ta=-20~70°C	4.30	4.35	4.40	V
Onarge promoti voltage	MM1309CF	VCELLO	$V_{\text{CELL}}=4.0V \rightarrow 4.5V$	4.20	4.25	4.30	
Charge prohibit releas	e voltage	VcellO	$V_{\text{CELL}}=4.5V\rightarrow4.0V$	VCELLU	VCELLU	VCELLU	V
Onarge prombit releas	e voitage	VCELLO	VCELL-4.5V · 4.0V	-60mV	-45mV	-30mV	
Charge prohibit detection	MM1309BF	V_{ALM}	$V_{\text{CELL}}=3.8V \rightarrow 4.4V$	4.05	4.20	4.25	V
function operation voltage	MM1309CF	V ALIVI	VCELE-0.0V 1.1V	3.95	4.10	4.25	v
Charge prohibition sensing operation		∠VALM	Vcell=4.4V→3.8V	50	90	130	mV
voltage Hysteresis v	/oltage	Z V TILIVI	Verial 1.17 0.07				111 1
Excess discharging	MM1309BF	VCELLS	$V_{\text{CELL}}=3.0V\rightarrow2.0V$	2.31	2.40	2.49	V
detection voltage	MM1309CF	V CLEES	Vehil 6.6 V 2.6 V	2.26	2.35	2.44	,
Discharge resumption MM1309BF		VCELLD	Vcell=2.0V→3.0V	2.49	2.65	2.81	v
voltage	MM1309CF	V CLLLID	TELL 2.0 TO CO.	2.44	2.60	2.76	
Excess discharge de hysteresis volta		∠VcsD	VCELLD-VCELLS	175	250	325	mV
BATL pin input vol	tage 1	IBATL	Vcell=3.8V			±300	nA
BATL pin input vol	tage 2	IBATLA	Vcell=4.4V	0.7	1.0	1.3	uA
BATM pin input voltage 1		I BAT M	Vcell=3.8V			±300	nA
BATM pin input vol	tage 2	ІватМА	Vcell=4.4V	0.7	1.0	1.3	uA
DCHG pin source v	oltage	IsoDcн	VCELL < VCELLS	20			uA
DCHG sink voltage		IsiDcн	VCELL > VCELLS	20			uA
DCHG output voltage L		VтнDcL	BATH-DCHG IS=20uA			1.0	V
DCHG output volta	age M	VтнDcH	Dchg-GND IS=-20uA			0.8	V
OV pin sink curr	ent	IsiOv	VOv=0.4, Ta=-20~70°C	200			uA

Application

