Standalone Li-Lon Switch Mode Battery Charger

Features

- Input Supply Range: 4.7V-6V
- > High Efficiency Current Mode PWM Controller
- End Charge Current Detection Output
- Constant Switching Frequency for Minimum Noise
- > Automatic Battery Recharge
- Automatic Shutdown When Input Supply is Removed
- Automatic Trickle Charging of Low Voltage Batteries
- Battery Temperature Sensing
- > Stable with Ceramic Output Capacitor
- 8-Lead SO Package

Application

- Charging Docks
- Handheld Instruments
- Portable Computers

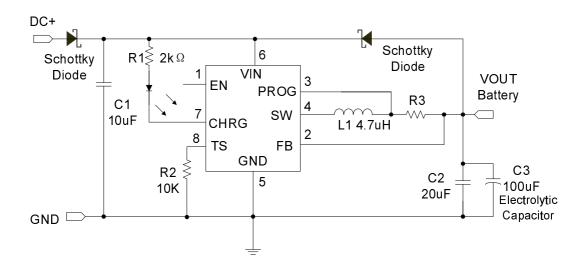
Description

The PJ8201S is a complete battery charger controller for one (4.2V) cell lithium-ion battery. The PJ8201S provides a small, simple and efficient solution to fast charge Li-ion battery. An external sense resistor sets the charge current with high accuracy. An internal resistor divider and precision reference set the final float voltage to 4.22V per cell with $\pm 1\%$ accuracy.

When the input supply is removed, the PJ8201S automatically enters a low current sleep mode, dropping the battery drain current to $4\mu A$. After the charge cycle ends, if the battery voltage drops below 4.05V per cell, a new charge cycle will automatically begin.

The PJ8201S is available in the 8-lead SO Package.

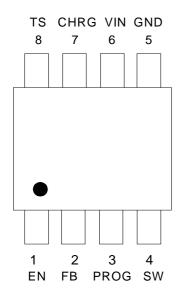
Typical Application



 \star The charge current can be set by $I_{OUT} = 0.17V/R3$.

Pin Assignment

Top View



PIN NUMBER	PIN NAME	FUNCTION	
1	EN	ON/OFF Control	
2	FB	Feedback	
3	PROG	Charge Current Program	
4	SW	Switch Output	
5	GND	Ground	
6	VIN	Input	
7	CHRG	Open-Drain Charge Status for Output	
8	TS	Temperature Sense	

SOP-8L

Electrical Characteristics

Operating Conditions: $T_A=25\,^{\circ}\text{C}$, $V_{IN}=5V$, R3 = 0.1 Ω unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
V_{IN}	Input Supply Voltage		4.7	5.0	6	V
		Current Mode		300		
I _{IN}	Input Supply Current	Shutdown Mode		19		μΑ
		Sleep Mode		4		
V _{BAT(FLT)}	Battery Regulated Float Voltage	0°C≤T _A ≤85°C, I _{OUT} = 1.2A	4.17	4.22	4.26	V
Vsns(chg)	Constant Current Sense Voltage	3V ≤ VBAT ≤ 4V		170		mV
VSNS(TRKL)	Trickle Current Sense Voltage	VBAT = 0V		10		mV
Vtrkl	Trickle Charge Threshold Voltage	VBAT Rising		2.9		V
Δ Vuv	VIN Undervoltage Lockout Hysteresis Voltage			300		mV
Vasd	Automatic Shutdown Threshold Voltage	VIN — VBAT		150		mV
ITS	TS Pin Output Current	VTS = 0.85V		82		μA
$V_{\text{TS-HOT}}$	TS Pin Threshold Voltage (Hot)	VTs Falling Hysteresis		0.54		V
V _{TS-COLD}	TS Pin Threshold Voltage (Cold)	VNTC Rising Hysteresis		2.36		V
Δ VRECHRG	Recharge Battery Voltage Offset from Full Charged Battery Voltage	VBAT(FULLCHARGED) — VRECHRG, VBAT Falling		170		mV
fosc	Switching Frequency			1		MHz
DC	Maximum Duty Cycle				100	%

Absolute Maximum Ratings (Note 1)

\triangleright	VIN, CHRG, SW, PROG Voltage0.3V to	6.5V
>	SW Pin Current	3.8A
>	Operating Temperature Range (Note 2)40°C to	85℃
>	Maximum Junction Temperature	25℃
>	Storage Temperature Range65 °C to 1	25 ℃
>	Lead Temperature (S8 Package) (Soldering, 10 sec)	00℃

Note 1: Absolute Maximum Rating are those values beyond which the life of a device may be impaired.

Note 2: The PJ8201S is guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Pin Description

EN (Pin 1): ON/OFF Control (High Enable) .

FB (Pin 2): Feedback Pin. Receives the feedback voltage from an external resistor across the output.

PROG (Pin 3): Charge Current Program. The output current is set by an external resistor according to the following formula: $I_{OUT} = 0.17V/R3$.

SW (Pin 4): Charge Current Output. It provides charge current to the battery and regulates the final float voltage to 4.22V.

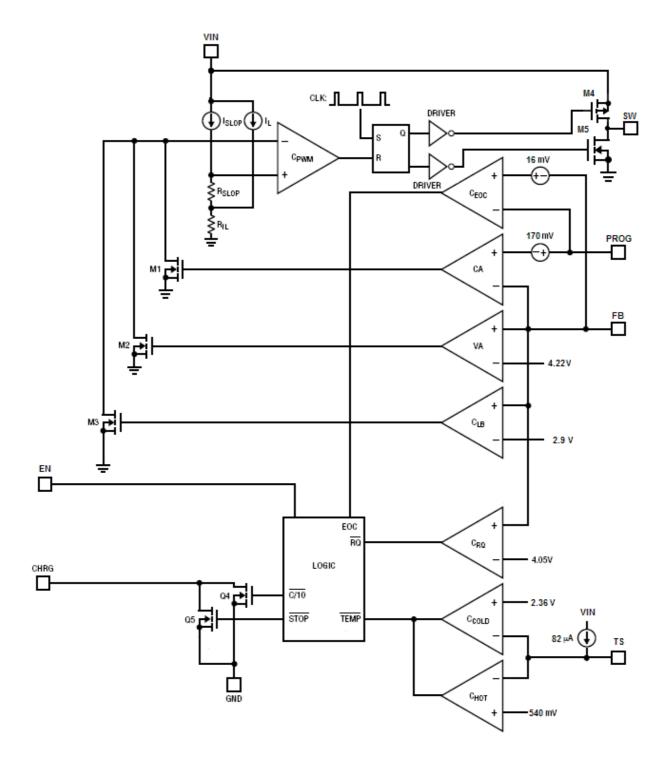
GND (Pin 5): IC Ground.

VIN (Pin 6): Positive Supply Voltage Input. VIN can range from 4.7V to 6V. A $10\mu\text{F}$ low ESR capacitor is required at the source pins of the power P-channel MOSFET.

CHRG (Pin 7): When the charge current drops below the End-of-Charge threshold for more than 120µs, the N-channel MOSFET turns off and a weak current source is connected from the CHRG pin to GND. When the input supply is removed, the weak current source is turned off and the CHRG pin becomes high impedance.

TS (**Pin 8**): TS Thermistor Input. With an external $10k\,\Omega$ negative temperature coefficient thermistor to ground, this pin senses the temperature of the battery pack and stops the charger when the temperature is out of range. When the voltage at this pin drops below 540mV at hot temperature or rises above 2.36V at cold temperature, charging is suspended and the internal timer stops. The CHRG pin output is not affected during this hold state. To disable the temperature qualification function, ground the TS pin.

Block Diagram



Operation

The PJ8201S is a constant current, constant voltage Li-Ion battery charger controller that uses a current mode PWM step-down (buck) switching architecture. The charge current is set by an external sense resistor (R3) across the PROG and FB pins. The final battery float voltage is internally set to 4.22V. For batteries like lithium-ion that require accurate final float voltage, the internal reference, voltage amplifier and the resistor divider provide regulation with high accuracy.

A charge cycle begins when the voltage at the VIN pin rises above the UVLO level and is 300mV or greater than the battery voltage. At the beginning of the charge cycle, if the battery voltage is less than the trickle charge threshold, the charger goes into trickle charge mode. The trickle charge current is internally set to 8% of the full-scale current.

When the battery voltage exceeds the trickle charge threshold, the charger goes into the full-scale constant current charge mode. In constant current mode, the charge current is set by the external sense resistor R3 and an internal 170mV reference; I_{OUT} = 170mV/R3.

When the battery voltage approaches the programmed float voltage, the charge current will start to decrease. When the current drops to 8% of the full-scale charge current, an internal comparator turns off the internal pull-down N-channel MOSFET at the CHRG pin, and connects a weak current source to ground to indicate a end-of-charge condition and then the charge cycle is terminated and the CHRG pin is forced high impedance.

To restart the charge cycle, remove and reapply the input voltage or momentarily shut the charger down. Also, a new charge cycle will begin if the battery voltage drops below the recharge threshold voltage.

When the input voltage is present, the charger can be shut down. When the input voltage is not present, the charger goes into sleep mode. This will greatly reduce the current drain on the battery and increase the standby time.

A $10k\Omega$ TS (negative temperature coefficient) thermistor can be connected from the TS pin to ground for battery temperature qualification.

Application Information

Functional Description

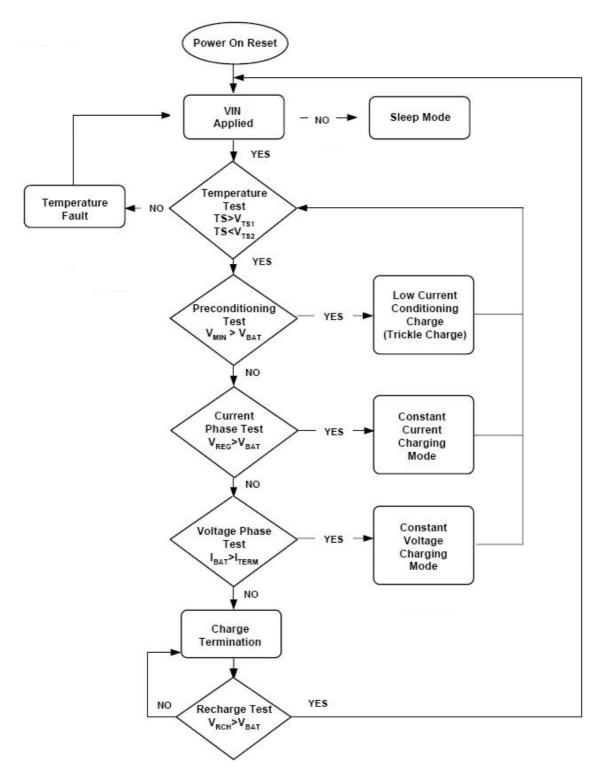


Figure 1: Operation Flow Chart

Qualification and Precharge

The PJ8201S suspends charge if the battery temperature is outside the V_{TS1} to V_{TS2} range and suspends charge until the battery temperature is within the allowed range. The PJ8201S also checks the battery voltage. If the battery voltage is below the precharge threshold $V_{(min)}$, the PJ8201S uses precharge to condition the battery. The conditioning charge rate $I_{(PRECHG)}$ is set at approximately 8% of the regulation current. See Figure 2 for a typical charge-profile.

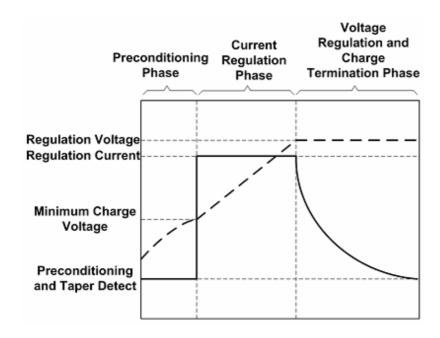


Figure 2: Typical Charge Profile

Charge Termination Recharge

The PJ8201S monitors the charging current during the voltage-regulation phase. The PJ8201S declares a done condition and terminates charge when the current drops to the charge termination threshold. A new charge cycle begins when the battery voltage falls below 4.05V.

Battery Temperature Monitoring

A negative temperature coefficient (NTC) thermistor located close to the battery pack can be used to monitor battery temperature and will not allow charging unless the battery temperature is within an acceptable range.

Connect a $10k\,\Omega$ thermistor from the TS pin to ground. With the $82\mu\text{A}$ pull-up current source, the Hot temperature voltage threshold is 540mV. For Cold temperature, the voltage threshold is set at 2.36V with $82\mu\text{A}$ of pull-up current. The charge cycle begins or resumes once the temperature is within the acceptable range.

Charge Status Indication

The PJ8201S reports the status of the charge on the CHRG pin. The following Table 1 summarized the operation of the CHRG pin. The CHRG pin can be used to drive a chip LED.

Condition	CHRG pin
Battery conditioning and charging	Low
Charge complete(done)	Hi-Z
Temperature fault or sleep mode	Hi-Z

Table 1

Undervoltage Lockout (UVLO)

An undervoltage lockout circuit monitors the input voltage and keeps the charger off until VIN rises above the UVLO threshold (4.2V) and at least 150mV above the battery voltage. To prevent oscillation around the threshold voltage, the UVLO circuit has 300mV of built-in hysteresis. When specifying minimum input voltage requirements, the voltage drop across the input blocking diode must be added to the minimum supply voltage specification.

Trickle Charge

At the beginning of a charge cycle, if the battery voltage is below the trickle charge threshold, the charger goes into trickle charge mode with the charge current reduced to 8% of the full-scale current.

Shutdown

The PJ8201S can be shut down by pulling the EN pin to ground. In shutdown, the output of the CHRG pin is high impedance and the quiescent current remains at 19uA.

Input and Output Capacitors

Since the input capacitor is assumed to absorb all input switching ripple current in the converter, it must have an adequate ripple current rating. Worst-case RMS ripple current is approximately one-half of output charge current. Actual capacitance value is not critical. Solid tantalum capacitors have a high ripple current rating in a relatively small surface mount package, but caution must be used when tantalum capacitors are used for input bypass. High input surge currents can be created when the adapter is hot-plugged to the charger and solid tantalum capacitors have a known failure mechanism when subjected to very high turn-on surge currents. Selecting the highest possible voltage rating on the capacitor will minimize problems. Consult with the manufacturer before use. The selection of output capacitor COUT is primarily determined by the ESR required to minimize ripple voltage and load step transients. The output ripple \triangle VouT is approximately bounded by:

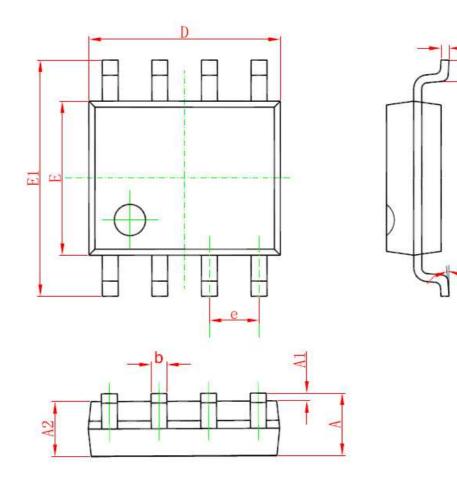
$$\Delta V_{OUT} \leq \Delta I_L \Biggl(\mathsf{ESR} + \frac{1}{8 f_{OSC} C_{OUT}} \Biggr)$$

Since $\Delta \text{ IL}$ increases with input voltage, the output ripple is highest at maximum input voltage. Typically, once the ESR requirement is satisfied, the capacitance is adequate for filtering and has the necessary RMS current rating.

Switching ripple current splits between the battery and the output capacitor depending on the ESR of the output capacitor and the battery impedance. EMI considerations usually make it desirable to minimize ripple current in the battery leads. Ferrite beads or an inductor may be added to increase battery impedance at the 500kHz switching frequency. If the ESR of the output capacitor is $0.2\,\Omega$ and the battery impedance is raised to $4\,\Omega$ with a bead or inductor, only 5% of the current ripple will flow in the battery.

Packaging Information

SOP-8L Package Outline Dimension



Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min	Max	Min	Max	
Α	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
е	1.270(BSC)		0.050(BSC)		
L	0.400	1.270	0.016	0.050	
	0°	8°	0°	8°	