

EVERFUSE

Polymeric PTC Resistor

Product: SMD1210P175TF-C Revision: Preliminary 0.3 Date: February 02, 2009

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TM

Device Specification (Preliminary)



ELECTRICAL CHARACTERISTICS

						Maximum Time To Trip		Resistance	
Part Number	I _{hold} (A)	I _{trip} (A)	V _{max} (Vdc)	I _{max} (A)	P _{d max.} (W)	Current (A)	Time (Sec.)	R_{min} (Ω)	R_{1max} (Ω)
SMD1210P175TF-C	1.75	3.50	6	100	0.8	8.00	0.60	0.020	0.080

Note: I_{hold} = Hold current: maximum current device will pass without tripping in 23°C still air.

I $_{trip}$ = Trip current: minimum current at which the device will trip in 23°C still air.

 V_{max} = Maximum voltage device can withstand without damage at rated current (I $_{\text{max}}$)

 I_{max} = Maximum fault current device can withstand without damage at rated voltage (V_{max})

Pd = Power dissipated from device when in the tripped state at 23° C still air.

R min= Minimum resistance of device in initial (un-soldered) state.

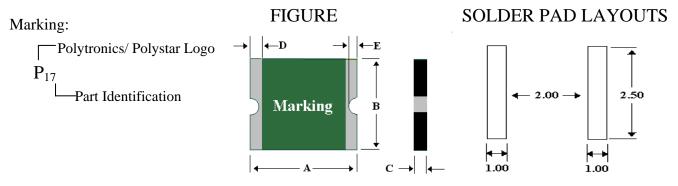
R 1max= Maximum resistance of device at 23°C measured one hour after tripping or reflow soldering of 260°C for 20 sec.

*Value specified were determined using the PWB with 0.030"*1.5oz copper traces.

Caution: Operation beyond the specified rating may result in damage and possible arcing and flame.

Recognitions:





Note: Polystar is Polytronics's manufacturing site in China. The Polystar ID marking shall appear on smallest package.

PHYSICAL DIMENSIONS (mm)

Part Number	A		В		С		D		Е	
	Min.	Max.								
SMD1210P175TF-C	3.00	3.43	2.35	2.80	0.50	0.75	0.25	0.75	0.20	0.50

O Specifications are subject to change without notice.

^{*}Customer should verify the device performance in their specified conditions.



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THERMAL DERATING CHART – Ihold/Itrip(Amps)

RECOMMENDED DATA

Model		Ambient Operation Temperature									
		-40°C	-20°C	0° C	23℃	40°C	50°C	60°C	70°C	85°C	
SMD1210P175TF-C	Ihold	2.45	2.22	2.01	1.75	1.45	1.26	1.10	0.98	0.80	
	Itrip	4.90	4.40	4.00	3.50	2.90	2.50	2.20	1.95	1.60	