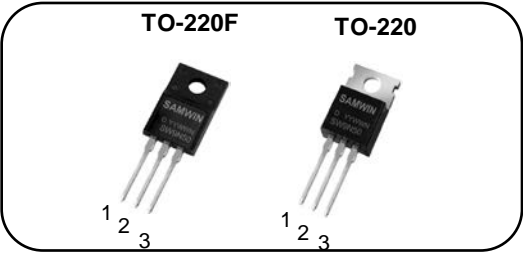


N-channel Enhanced mode TO-220F/TO-220 MOSFET

Features

- High ruggedness
- Low $R_{DS(ON)}$ (Typ 0.68Ω) @ $V_{GS}=10V$
- Low Gate Charge (Typ 31nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application: DC-DC , LED , PC



1. Gate 2. Drain 3. Source

BV_{DSS} : 500V
I_D : 9A
R_{DS(ON)} : 0.68Ω

General Description

This power MOSFET is produced with advanced technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.



Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW F 9N50D	SW9N50D	TO-220F	TUBE
2	SW P 9N50D	SW9N50D	TO-220	TUBE

Absolute maximum ratings

Symbol	Parameter	Value		Unit
		TO-220F	TO-220	
V _{DSS}	Drain to source voltage	500		V
I _D	Continuous drain current (@T _C =25°C)	9*		A
	Continuous drain current (@T _C =100°C)	5.7*		A
I _{DM}	Drain current pulsed (note 1)	36		A
V _{GS}	Gate to source voltage	±30		V
E _{AS}	Single pulsed avalanche energy (note 2)	462		mJ
E _{AR}	Repetitive avalanche energy (note 1)	64		mJ
dv/dt	Peak diode recovery dv/dt (note 3)	5		V/ns
P _D	Total power dissipation (@T _C =25°C)	23.3	200	W
	Derating factor above 25°C	0.19	1.6	W/°C
T _{STG} , T _J	Operating junction temperature & storage temperature	-55 ~ + 150		°C
T _L	Maximum lead temperature for soldering purpose, 1/8 from case for 5 seconds.	300		°C

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value		Unit
		TO-220F	TO-220	
R _{thjc}	Thermal resistance, Junction to case	5.36	0.63	°C/W
R _{thja}	Thermal resistance, Junction to ambient	53	60	°C/W

Electrical characteristic ($T_C = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV_{DSS}	Drain to source breakdown voltage	$V_{GS}=0V, I_D=250\mu A$	500			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown voltage temperature coefficient	$I_D=250\mu A$, referenced to 25°C		0.57		$V/^\circ\text{C}$
I_{DSS}	Drain to source leakage current	$V_{DS}=510V, V_{GS}=0V$			1	μA
		$V_{DS}=408V, T_C=125^\circ\text{C}$			50	μA
I_{GSS}	Gate to source leakage current, forward	$V_{GS}=30V, V_{DS}=0V$			100	nA
	Gate to source leakage current, reverse	$V_{GS}=-30V, V_{DS}=0V$			-100	nA
On characteristics						
$V_{GS(TH)}$	Gate threshold voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2.5		4.5	V
$R_{DS(ON)}$	Drain to source on state resistance	$V_{GS}=10V, I_D=4.5A$		0.68	0.8	Ω
G_{fs}	Forward transconductance	$V_{DS}=30V, I_D=4.5A$		7		S
Dynamic characteristics						
C_{iss}	Input capacitance	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$		1479		pF
C_{oss}	Output capacitance			126		
C_{rss}	Reverse transfer capacitance			22.4		
$t_{d(on)}$	Turn on delay time	$V_{DS}=250V, I_D=9A, R_G=25\Omega$, $V_{GS}=10V$ (note 4,5)		19		ns
t_r	Rising time			41		
$t_{d(off)}$	Turn off delay time			74		
t_f	Fall time			37		
Q_g	Total gate charge	$V_{DS}=400V, V_{GS}=10V, I_D=9A$ (note 4,5)		31		nC
Q_{gs}	Gate-source charge			7		
Q_{gd}	Gate-drain charge			13		

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			9	A
I_{SM}	Pulsed source current				36	A
V_{SD}	Diode forward voltage drop.	$I_S=9A, V_{GS}=0V$			1.4	V
t_{rr}	Reverse recovery time	$I_S=9A, V_{GS}=0V$, $di_f/dt=100A/\mu s$		319		ns
Q_{rr}	Reverse recovery charge			3.48		μC

※. Notes

1. Repeitative rating : pulse width limited by junction temperature.
2. $L = 11.4\text{mH}$, $I_{AS} = 9A$, $V_{DD} = 50V$, $R_G=25\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 9A$, $di/dt = 100A/\mu s$, $V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. Essentially independent of operating temperature.

Fig. 1. On-state characteristics

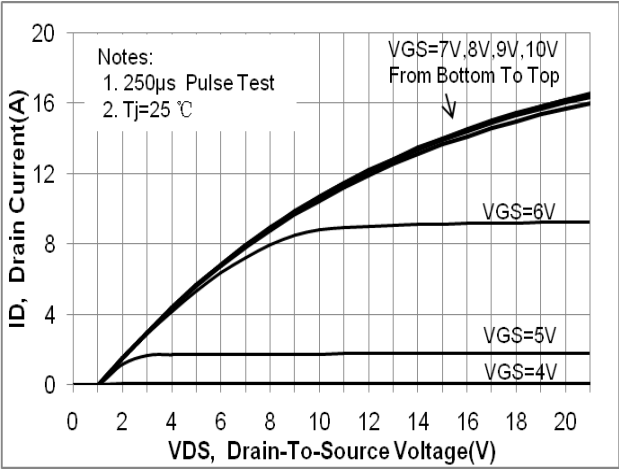


Fig. 2. On-resistance variation vs. drain current and gate voltage

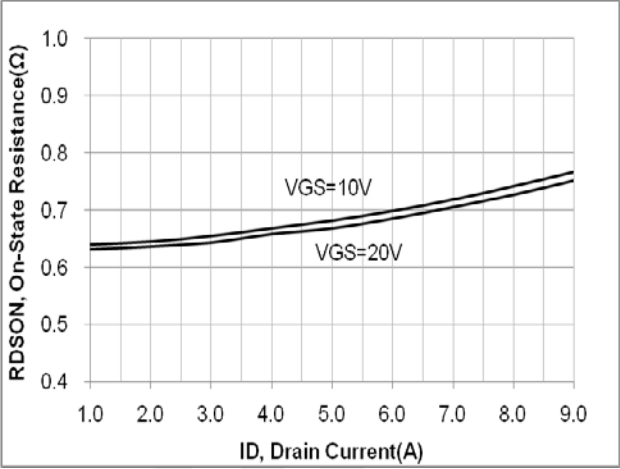


Fig. 3. Gate charge characteristics

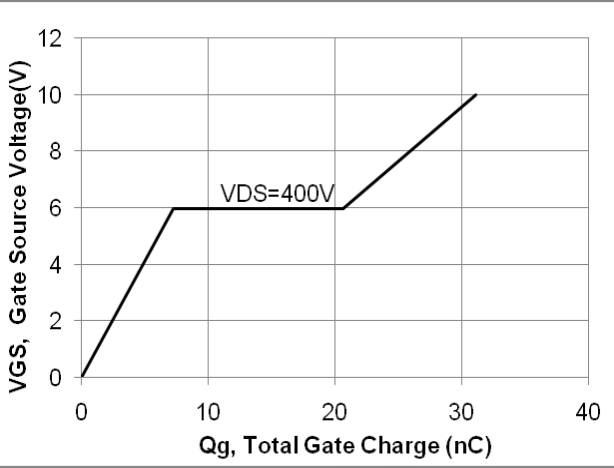


Fig. 4. On state current vs. diode forward voltage

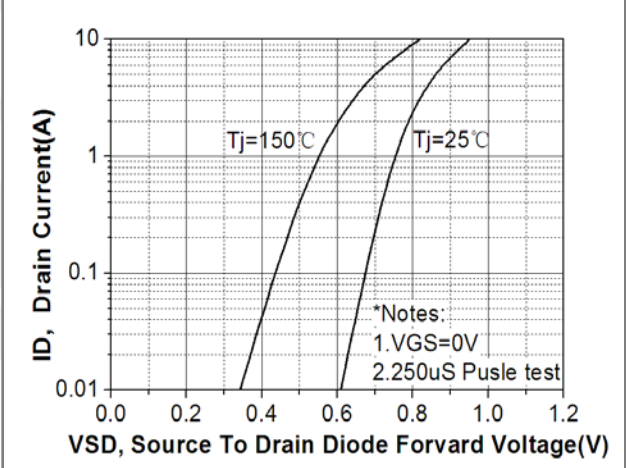


Fig 5. Breakdown Voltage Variation vs. Junction Temperature

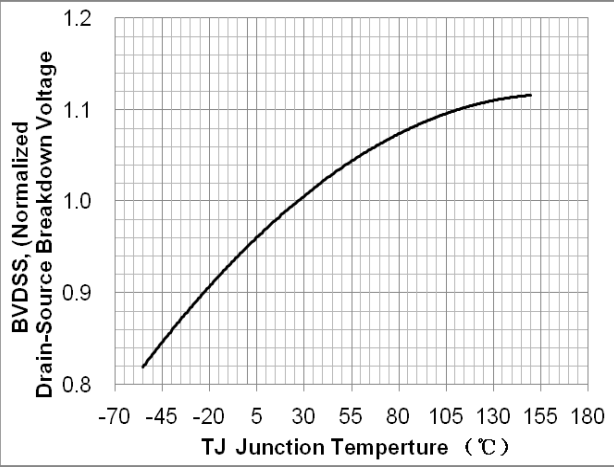


Fig. 6. On resistance variation vs. junction temperature

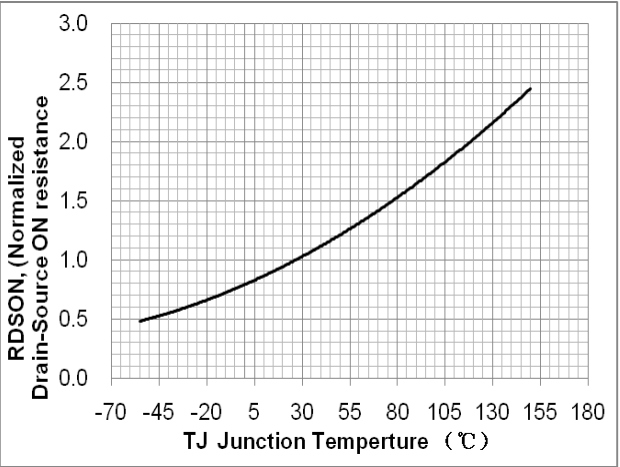


Fig. 7. Maximum safe operating area(TO-220F)

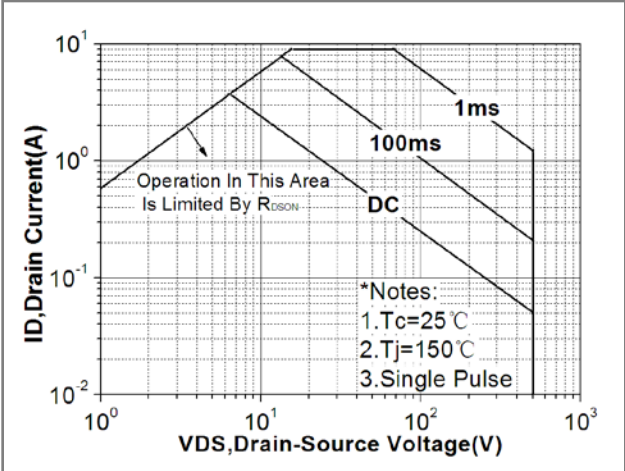


Fig. 8. Maximum safe operating area(TO-220)

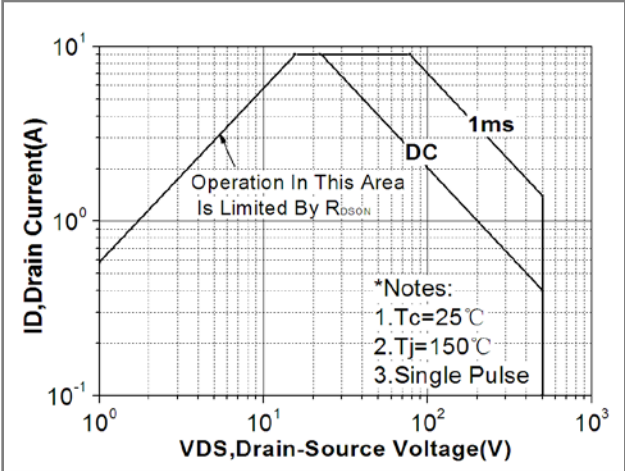


Fig. 9. Capacitance Characteristics

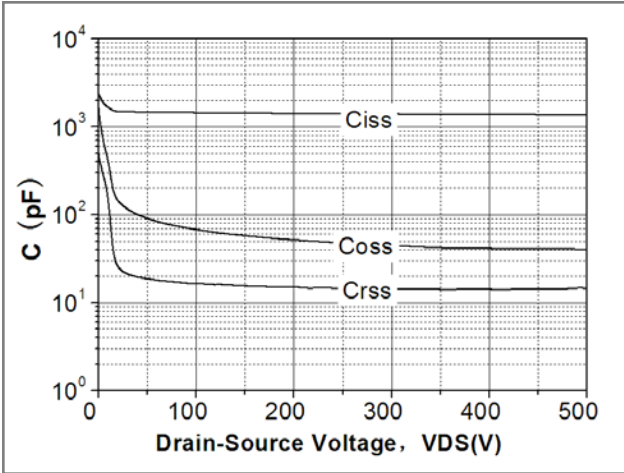


Fig. 10. Transient thermal response curve(TO-220F)

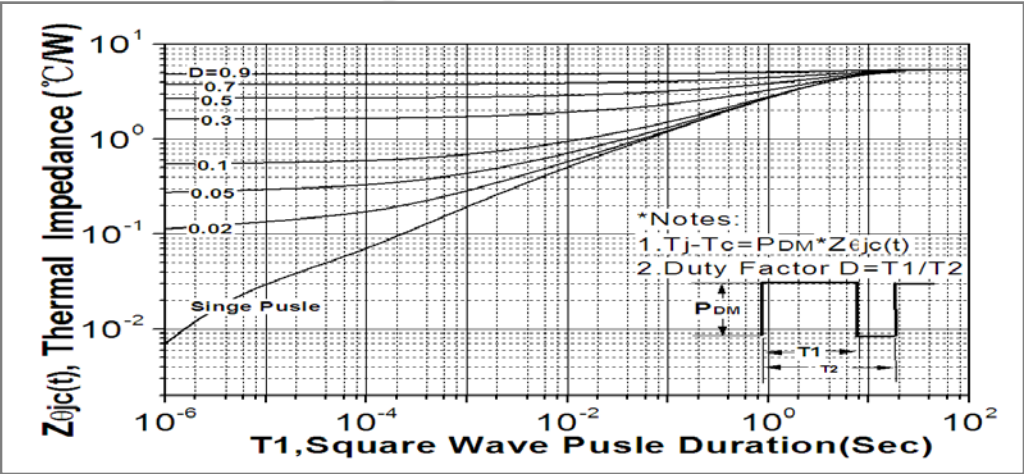


Fig. 11. Transient thermal response curve(TO-220)

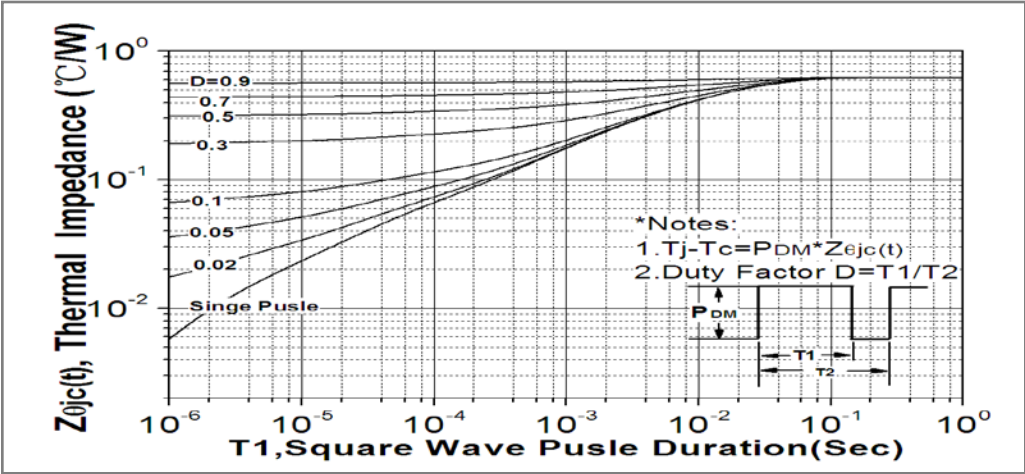


Fig. 12. Gate charge test circuit & waveform

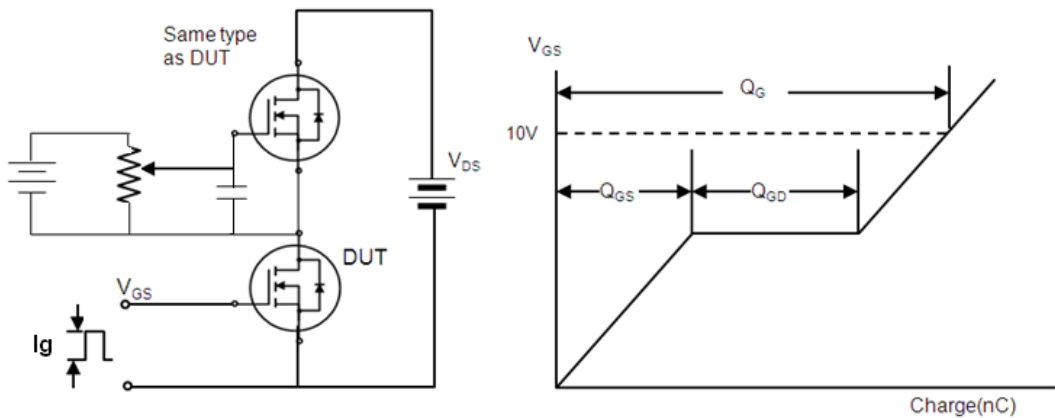


Fig. 13. Switching time test circuit & waveform

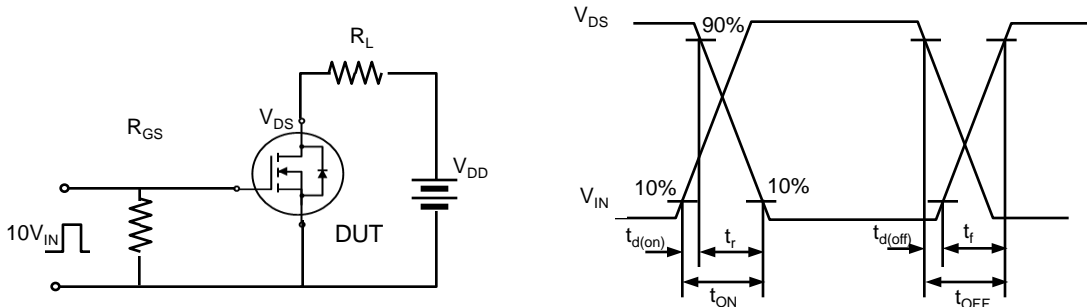


Fig. 14. Unclamped Inductive switching test circuit & waveform

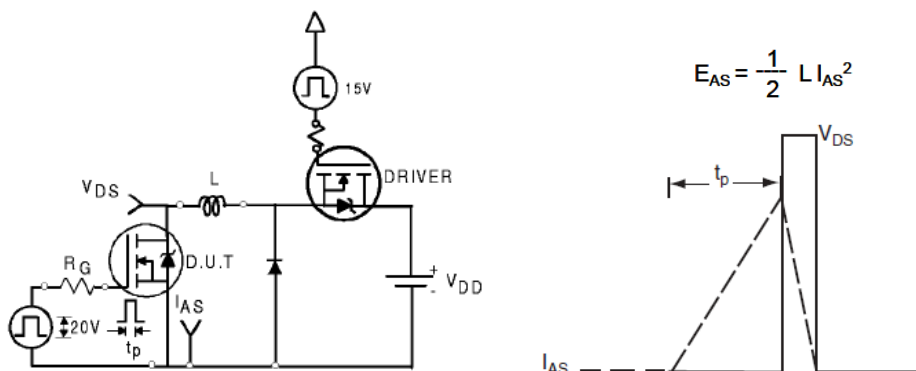
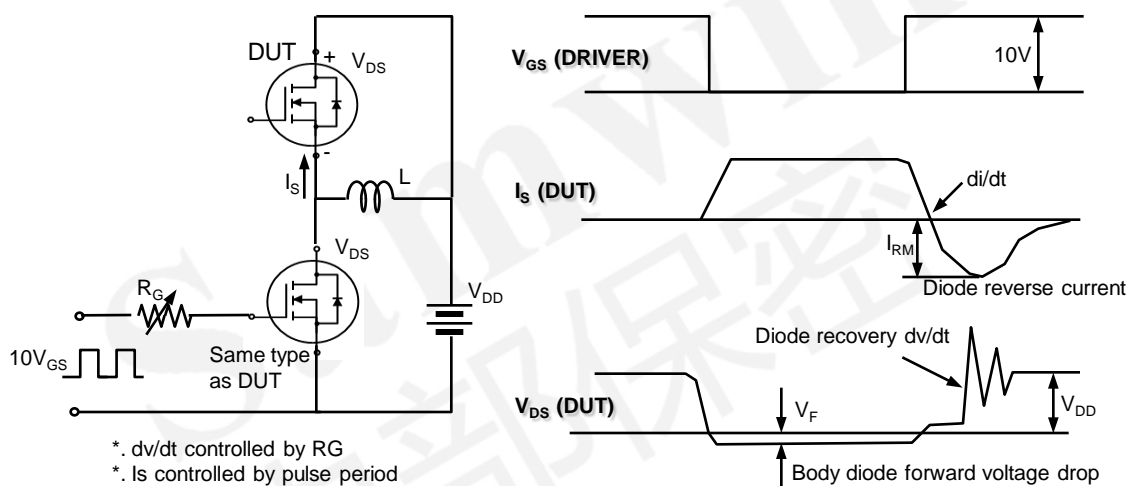



Fig. 15. Peak diode recovery dv/dt test circuit & waveform



DISCLAIMER

- * All the data & curve in this document was tested in XI'AN SEMIPOWER TESTING & APPLICATION CENTER.
- * This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.
- * Qualification standards can also be found on the Web site (<http://www.semipower.com.cn>) 
- * Suggestions for improvement are appreciated, Please send your suggestions to samwin@samwinsemi.com