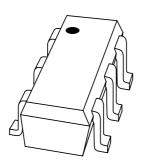
# **DISCRETE SEMICONDUCTORS**

# DATA SHEET



# BC846S NPN general purpose double transistor

Product specification Supersedes data of 1999 May 28 1999 Sep 01





# NPN general purpose double transistor

**BC846S** 

### **FEATURES**

- Two transistors in one package
- Reduces number of components and board space
- No mutual interference between the transistors.

### **APPLICATIONS**

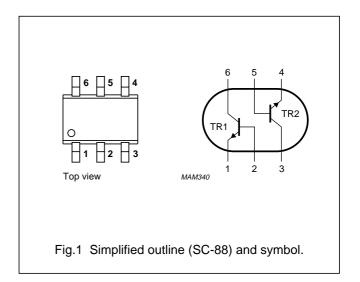
• General purpose switching and small signal amplification.

### **DESCRIPTION**

NPN double transistor in an SC-88 (SOT363) plastic six lead package.

### **PINNING**

PIN		DESCRIPTION
1, 4	emitter	TR1; TR2
2, 5	base	TR1; TR2
6, 3	collector	TR1; TR2



### **MARKING**

TYPE NUMBER	MARKING CODE
BC846S	4Ft

### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT	
Per transist	or		<u>'</u>			
V <sub>CBO</sub>	collector-base voltage	open emitter	_	80	V	
V <sub>CEO</sub>	collector-emitter voltage	open base	_	65	V	
V <sub>EBO</sub>	emitter-base voltage	open collector	_	6	V	
I <sub>C</sub>	collector current (DC)		_	100	mA	
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	200	mW	
T <sub>stg</sub>	storage temperature		-65	+150	°C	
Tj	junction temperature		_	150	°C	
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C	
Per device						
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C; note 1	_	300	mW	

### Note

1. Refer to SC-88 (SOT363) standard mounting conditions.

# NPN general purpose double transistor

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### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS		UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	416	K/W

### Note

1. Refer to SC-88 (SOT363) standard mounting conditions.

### **CHARACTERISTICS**

 $T_{amb}$  = 25 °C unless otherwise specified.

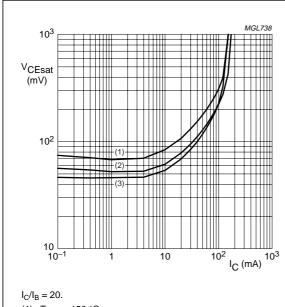
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
Per transist	or				•	
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V	-	-	15	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V; T <sub>j</sub> = 150 °C	_	_	5	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 2 mA; V <sub>CE</sub> = 5 V	110	_	_	
V <sub>CEsat</sub>	collector-emitter saturation	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA	_	_	100	mV
	voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA; note 1	_	_	300	mV
V <sub>BEsat</sub>	base-emitter saturation voltage	I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA	_	770	_	mV
C <sub>c</sub>	collector capacitance	$I_E = i_e = 0$ ; $V_{CB} = 10 \text{ V}$ ; $f = 1 \text{ MHz}$	_	_	1.5	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 5 V; f = 100 MHz	100	_	_	MHz

### Note

1. Pulse test:  $t_p \le 300~\mu s;~\delta \le 0.02.$ 

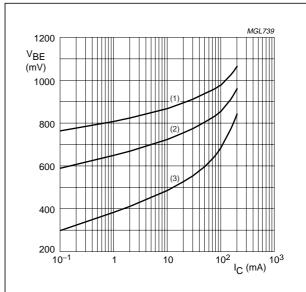
# NPN general purpose double transistor

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- (1)  $T_{amb} = 150 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = -55 \, ^{\circ}C$ .

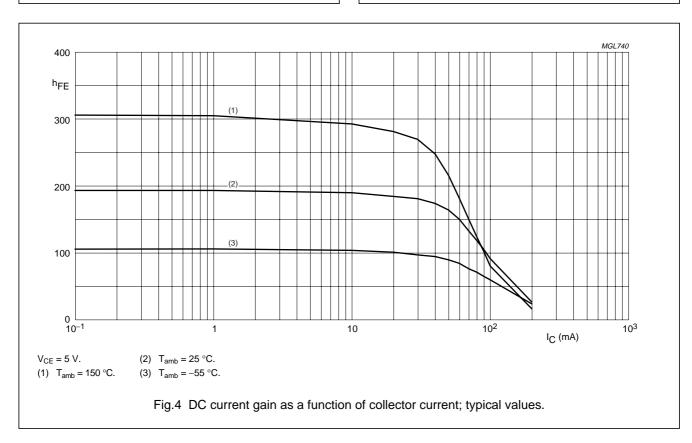
Fig.2 Collector-emitter saturation voltage as a function of collector current; typical values.



 $V_{CE} = 5 \text{ V}.$ 

- (1)  $T_{amb} = -55 \, ^{\circ}C$ .
- (2)  $T_{amb} = 25 \, ^{\circ}C$ .
- (3)  $T_{amb} = 150 \, ^{\circ}C$ .

Fig.3 Base-emitter voltage as a function of collector current; typical values.



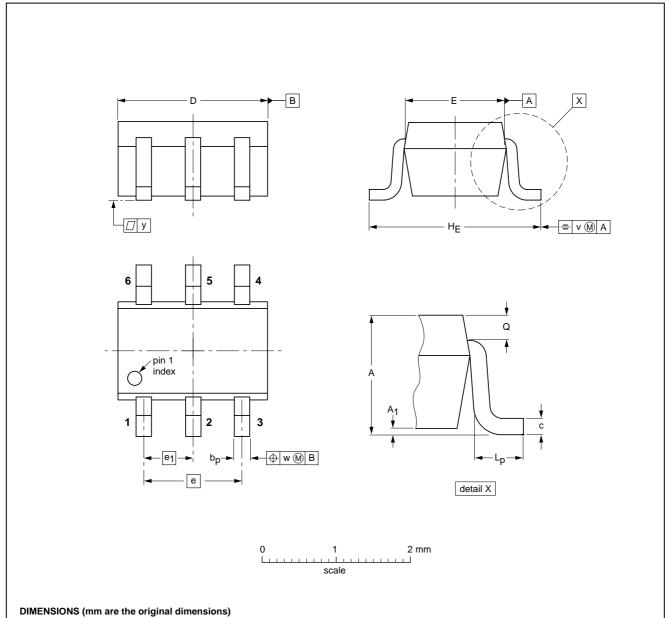
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### **PACKAGE OUTLINE**

### Plastic surface mounted package; 6 leads

**SOT363** 



UNIT	Α	A <sub>1</sub> max	bp	C	D	E	е	e <sub>1</sub>	HE	Lp	ď	٧	w	у	
mm	1.1 0.8	0.1	0.30 0.20	0.25 0.10	2.2 1.8	1.35 1.15	1.3	0.65	2.2 2.0	0.45 0.15	0.25 0.15	0.2	0.2	0.1	

OUTLINE		REFERENCES				ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT363			SC-88			97-02-28

1999 Sep 01 5

## NPN general purpose double transistor

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### **DEFINITIONS**

Data sheet status					
Objective specification	This data sheet contains target or goal specifications for product development.				
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.				
Product specification	This data sheet contains final product specifications.				
Limiting values					
more of the limiting values r of the device at these or at a	Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.				
Application information					
Where application information is given, it is advisory and does not form part of the specification.					

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