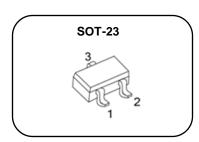


N-channel Enhanced mode SOT-23 MOSFET

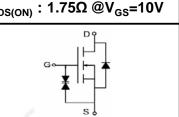
Features

- High Switching Speed
- Low R_{DS(ON)} (Typ 2.3Ω)@V_{GS}=5V
- Low R_{DS(ON)} (Typ 1.75Ω)@V_{GS}=10V
 Low Gate Charge (Typ 1.7nC)
- Application: Small Servo Motor Control, Switch



1. Gate 2. Source 3. Drain

BV_{DSS}: 60V : 0.3A $R_{DS(ON)}: 1.75\Omega \ @V_{GS}\text{=}10V$



General Description

This power MOSFET is produced with advanced technology of SAMWIN. This technology enable the power MOSFET to have better characteristics, including fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.





Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW E 2N7002	SW2N7002	SOT-23	REEL

Absolute maximum ratings

Symbol	Parameter	Value	Unit
V _{DSS}	Drain to source voltage	60	V
	Continuous drain current (@T _c =25°C)	0.3*	А
l _D	Continuous drain current (@T _C =100°C)	0.19*	А
I _{DM}	Drain current pulsed (note 1)	1.2	А
V _{GS}	Gate to source voltage	±20	V
P _D	Total power dissipation (@T _C =25°C)	0.35	W
T_{STG},T_{J}	Operating junction temperature & storage temperature	-55 ~ + 150	°C

^{*.} Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value	Unit
R _{thja}	Thermal resistance, Junction to ambient(note 2)	350	°C/W



Electrical characteristic ($T_C = 25^{\circ}C$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Off charac	teristics					
BV _{DSS}	Drain to source breakdown voltage	V _{GS} =0V, I _D =250uA	60			V
I _{DSS}	Drain to source leakage current	V _{DS} =60V, V _{GS} =0V			1	uA
		V _{DS} =48V, T _C =125°C			50	uA
	Gate to source leakage current, forward	V _{GS} =10V, V _{DS} =0V			500	nA
I _{GSS}		V _{GS} =20V, V _{DS} =0V	R	5)	10	uA
	Gate to source leakage current, reverse	V _{GS} =-10V, V _{DS} =0V			-500	nA
		V _{GS} =-20V, V _{DS} =0V			-10	uA
On charac	teristics (note 3)				•	
V _{GS(TH)}	Gate threshold voltage	$V_{DS}=V_{GS}$, $I_{D}=250$ uA	1.0		2.5	V
_	Drain to source on state resistance	$V_{GS}=5V, I_{D}=0.4A$		2.3	3	Ω
R _{DS(ON)}		V_{GS} =10V, I_{D} = 0.5A		1.75	2	Ω
G _{fs}	Forward transconductance	$V_{DS} = 10 \text{ V}, I_{D} = 0.2 \text{ A}$		0.3		S
Dynamic c	haracteristics(note 4)					
C _{iss}	Input capacitance			36		
C _{oss}	Output capacitance	V _{GS} =0V, V _{DS} =25V, f=1MHz		18		pF
C _{rss}	Reverse transfer capacitance			3		
t _{d(on)}	Turn on delay time	V_{DS} =30V, V_{GS} =10V , I_{D} =0.2A R_{G} =10 Ω		13		- ns
t _r	Rising time			25		
t _{d(off)}	Turn off delay time			46		
t _f	Fall time			29		
Q_g	Total gate charge	V _{DS} =10V, V _{GS} =4.5V, I _D =0.3A		1.7		nC

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Тур.	Max.	Unit
Is	Continuous source current	Integral reverse p-n Junction			0.2	Α
I _{SM}	Pulsed source current	diode in the MOSFET			0.8	Α
V _{SD}	Diode forward voltage drop.	I _S =0.2A, V _{GS} =0V			1.4	V

- 1. Repeatitive rating : pulse width limited by junction temperature.
- 2. Surface Mounted on FR4 Board, $t \le 2\%$.
- 3. Pulse Test : Pulse Width ≤ 300us, duty cycle ≤ 2%
- 4. Guaranteed by design, not subject to production.



Fig. 1. On-state characteristics

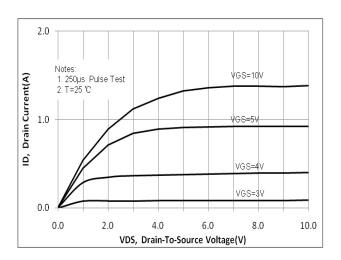


Fig. 3. Gate charge characteristics

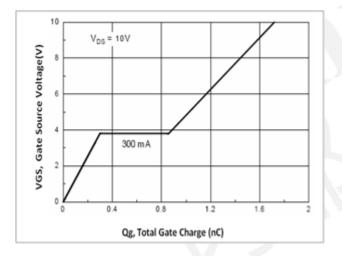


Fig. 5. Capacitance Characteristics

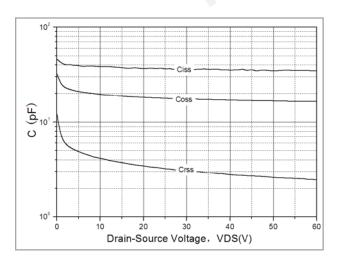


Fig. 2. On-resistance variation vs. drain current and gate voltage

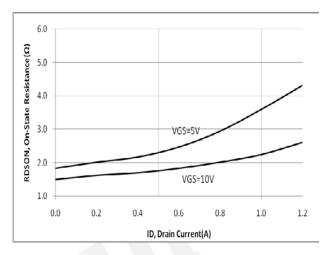


Fig. 4. Maximum safe operating area

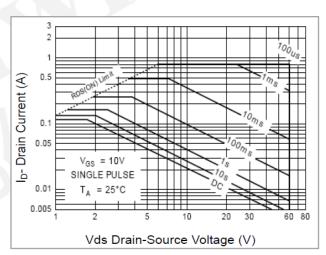




Fig. 6. Transient thermal response curve

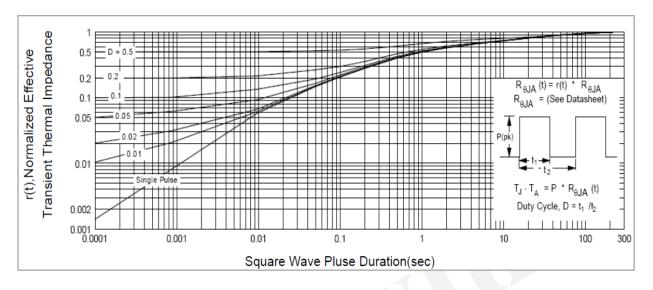


Fig. 7. Gate charge test circuit & waveform

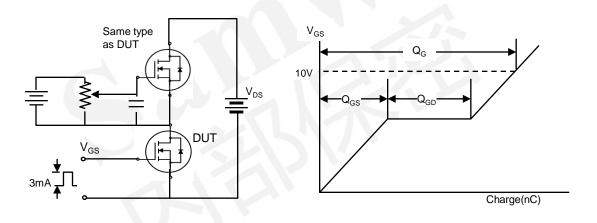
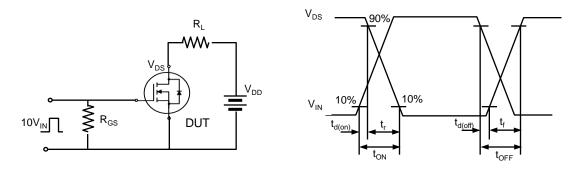


Fig. 8. Switching time test circuit & waveform







DISCLAIMER

- * All the data & curve in this document was tested in XI'AN SEMIPOWER TESTING & APPLICATION CENTER.
- * This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.
- * Qualification standards can also be found on the Web site (http://www.semipower.com.cn)
- * Suggestions for improvement are appreciated, Please send your suggestions to samwin@samwinsemi.com