

ECH8601

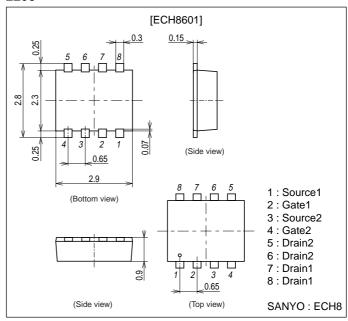
LIB Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · 2.5V drive.

Package Dimensions

unit : mm 2206



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		20	V
Gate-to-Source Voltage	VGSS		±10	V
Drain Current (DC)	ID		7	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	40	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board (900mm ² X0.8mm)1unit	1.4	W
Total Dissipation	PT	Mounted on a ceramic board (900mm ² X0.8mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	20			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =20V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	VGS=±8V, VDS=0			±10	μΑ

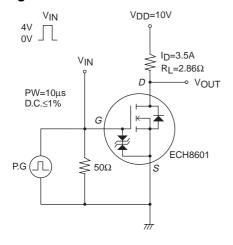
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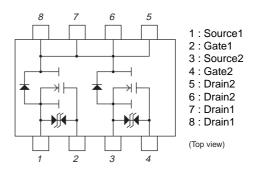
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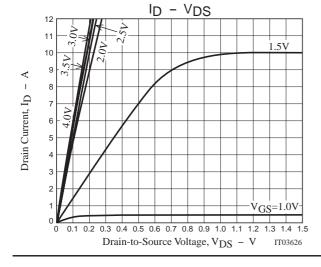
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Uill
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	0.5		1.3	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3.5A	7.7	11		S
Static Drain-to-Source On-State Resistance	RDS(on)1	ID=4A, VGS=4.5V		17	23	mΩ
	R _{DS} (on)2	I _D =4A, V _{GS} =4.0V		18	24	mΩ
	R _{DS} (on)3	I _D =2A, V _{GS} =2.5V		24	35	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		800		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		350		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		170		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit		15		ns
Rise Time	t _r	See specified Test Circuit		100		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit		61		ns
Fall Time	tf	See specified Test Circuit		90		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		23		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		1.3		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =3.5A		3.4		nC
Diode Forward Voltage	V _{SD}	I _S =7A, V _{GS} =0		0.83	1.2	V

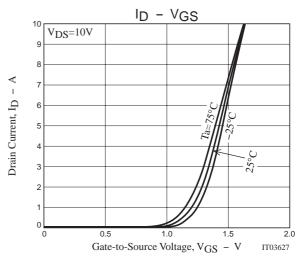
Switching Time Test Circuit

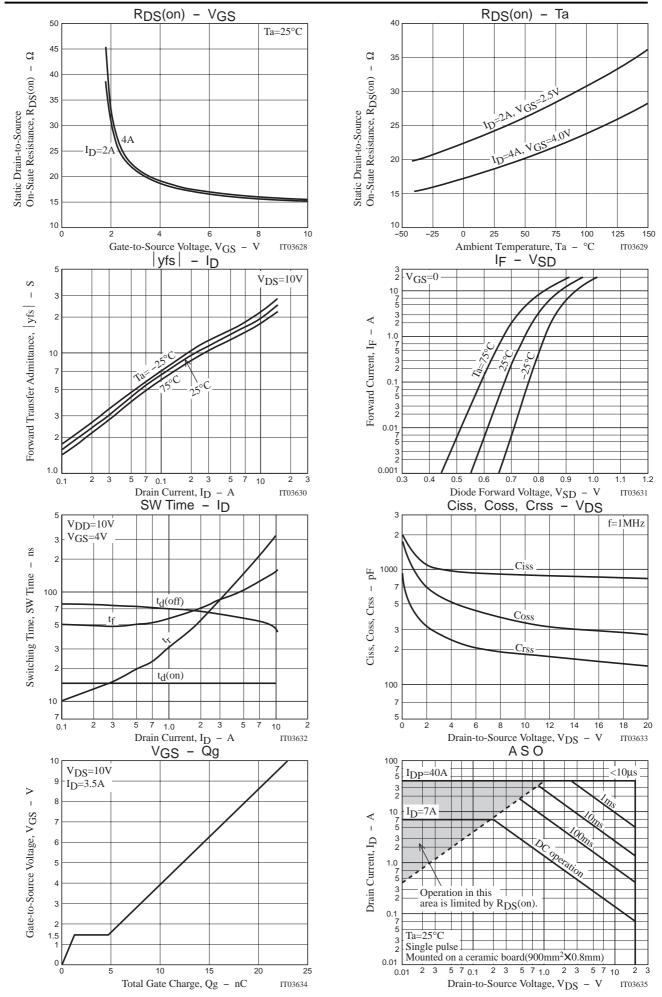


Electrical Connection

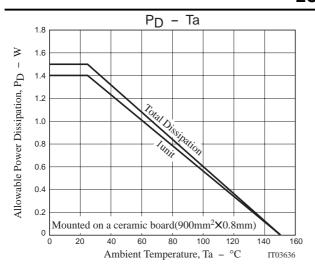








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