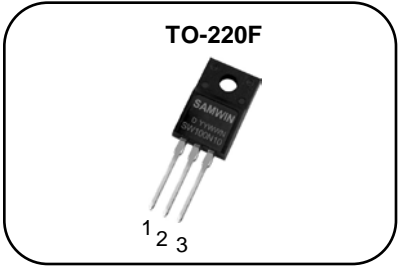


N-channel Enhancement mode TO-220F MOSFET

Features

- High ruggedness
- $R_{DS(ON)}$ (Typ 9.5mΩ) @ $V_{GS}=10V$
- Gate Charge (Typ 107nC)
- Improved dv/dt Capability
- 100% Avalanche Tested
- Application: DC-DC , E-Bike



1. Gate 2. Drain 3. Source

BV_{DSS} : 100V
 I_D : 100A
 $R_{DS(ON)}$: 9.5mΩ

General Description

This power MOSFET is produced with advanced technology of SAMWIN.
This technology enable power MOSFET to have better characteristics, such as fast switching time, low on resistance, low gate charge and especially excellent avalanche characteristics.



Order Codes

Item	Sales Type	Marking	Package	Packaging
1	SW F 100N10D	SW100N10D	TO-220F	TUBE

Absolute maximum ratings

Symbol	Parameter	Value	Unit
V_{DSS}	Drain to Source Voltage	100	V
I_D	Continuous Drain Current (@ $T_C=25^{\circ}C$)	100*	A
	Continuous Drain Current (@ $T_C=100^{\circ}C$)	63*	A
I_{DM}	Drain current pulsed (note 1)	400	A
V_{GS}	Gate to Source Voltage	± 20	V
E_{AS}	Single pulsed Avalanche Energy (note 2)	910	mJ
E_{AR}	Repetitive Avalanche Energy (note 1)	140	mJ
dv/dt	Peak diode Recovery dv/dt (note 3)	5	V/ns
P_D	Total power dissipation (@ $T_C=25^{\circ}C$)	39	W
	Derating Factor above 25°C	0.3	W/°C
T_{STG}, T_J	Operating Junction Temperature & Storage Temperature	-55 ~ + 150	°C
T_L	Maximum Lead Temperature for soldering purpose, 1/8 from Case for 5 seconds.	300	°C

*. Drain current is limited by junction temperature.

Thermal characteristics

Symbol	Parameter	Value	Unit
R_{thjc}	Thermal resistance, Junction to case	3.2	°C/W
R_{thcs}	Thermal resistance, Case to Sink	0.5	°C/W
R_{thja}	Thermal resistance, Junction to ambient	50	°C/W

Electrical characteristic ($T_C = 25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
Off characteristics						
BV_{DSS}	Drain to source breakdown voltage	$V_{GS}=0V, I_D=250\mu A$	100			V
$\Delta BV_{DSS} / \Delta T_J$	Breakdown voltage temperature coefficient	$I_D=250\mu A$, referenced to 25°C		0.1		$V/^{\circ}\text{C}$
I_{DSS}	Drain to source leakage current	$V_{DS}=100V, V_{GS}=0V$			1	μA
		$V_{DS}=80V, T_C=125^{\circ}\text{C}$			50	μA
I_{GSS}	Gate to source leakage current, forward	$V_{GS}=20V, V_{DS}=0V$			100	nA
	Gate to source leakage current, reverse	$V_{GS}=-20V, V_{DS}=0V$			-100	nA
On characteristics						
$V_{GS(TH)}$	Gate threshold voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	2		4	V
$R_{DS(ON)}$	Drain to source on state resistance	$V_{GS}=10V, I_D=50A$		9.5	11.5	$m\Omega$
G_{fs}	Forward Transconductance	$V_{DS}=10V, I_D=50A$		92		S
Dynamic characteristics						
C_{iss}	Input capacitance	$V_{GS}=0V, V_{DS}=25V, f=1\text{MHz}$		6440		pF
C_{oss}	Output capacitance			542		
C_{rss}	Reverse transfer capacitance			302		
$t_{d(on)}$	Turn on delay time	$V_{DS}=50V, I_D=30A, R_G=25\Omega$ (note 4 , 5)		67		nS
t_r	Rising time			112		
$t_{d(off)}$	Turn off delay time			248		
t_f	Fall time			118		
Q_g	Total gate charge	$V_{DS}=80V, V_{GS}=10V, I_D=30A$ (note 4 , 5)		107		nC
Q_{gs}	Gate-source charge			24		
Q_{gd}	Gate-drain charge			34		

Source to drain diode ratings characteristics

Symbol	Parameter	Test conditions	Min.	Typ.	Max.	Unit
I_S	Continuous source current	Integral reverse p-n Junction diode in the MOSFET			100	A
I_{SM}	Pulsed source current				400	A
V_{SD}	Diode forward voltage drop.	$I_S=100A, V_{GS}=0V$			1.4	V
T_{rr}	Reverse recovery time	$I_S=30A, V_{GS}=0V,$		57		nS
Q_{rr}	Breakdown voltage charge	$di_f/dt=100A/us$		149		nC

※. Notes

1. Repeattive rating : pulse width limited by junction temperature.
2. $L=2mH, I_{AS}=30A, V_{DD}=50V, R_G=25\Omega$, Starting $T_J=25^{\circ}\text{C}$
3. $I_{SD} \leq 30A, di/dt = 100A/us, V_{DD} \leq BV_{DSS}$, Starting $T_J=25^{\circ}\text{C}$
4. Pulse Test : Pulse Width $\leq 300us$, duty cycle $\leq 2\%$
5. Essentially independent of operating temperature.

Fig. 1. On-state characteristics

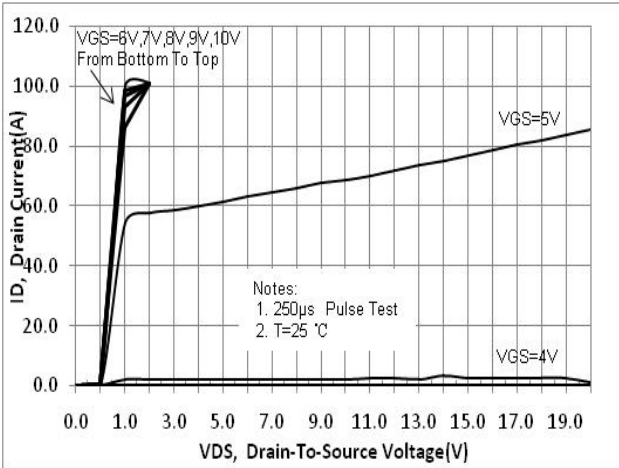


Fig. 2. On-resistance variation vs. drain current and gate voltage

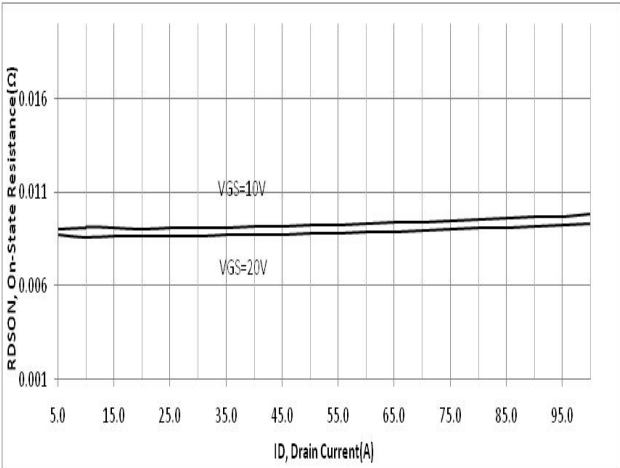


Fig. 3. Gate charge characteristics

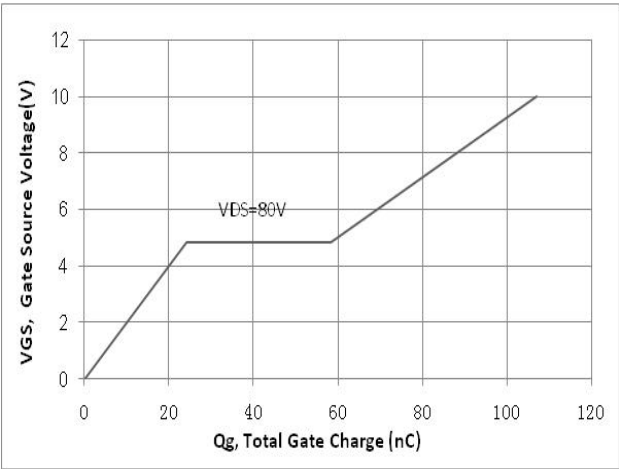


Fig. 4. On state current vs. diode forward voltage

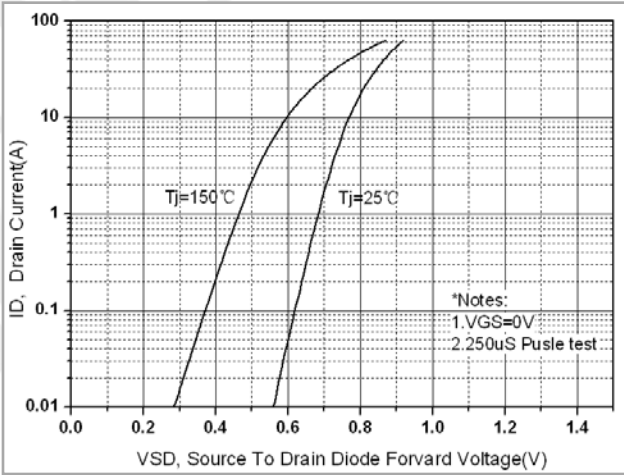


Fig 5. Breakdown Voltage Variation vs. Junction Temperature

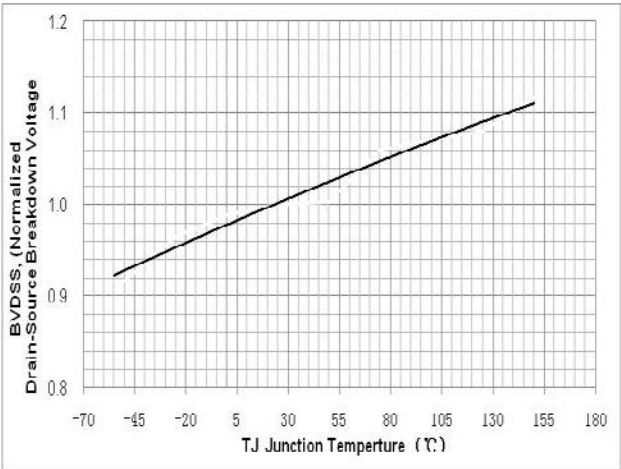


Fig. 6. On resistance variation vs. junction temperature



Fig. 7. Maximum safe operating area

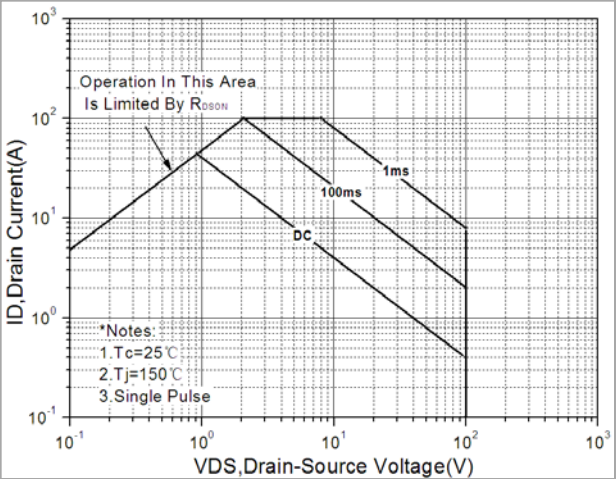


Fig. 8. Transient thermal response curve

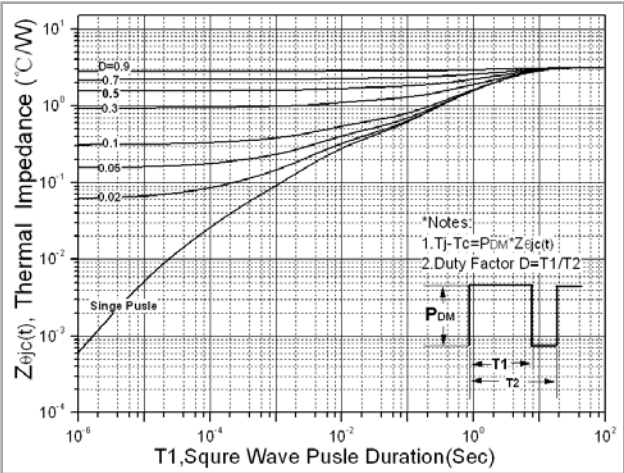


Fig. 9. Capacitance Characteristics

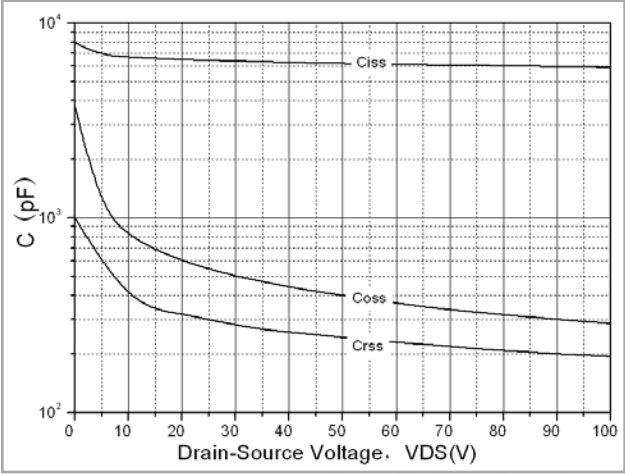


Fig. 10. Gate charge test circuit & waveform

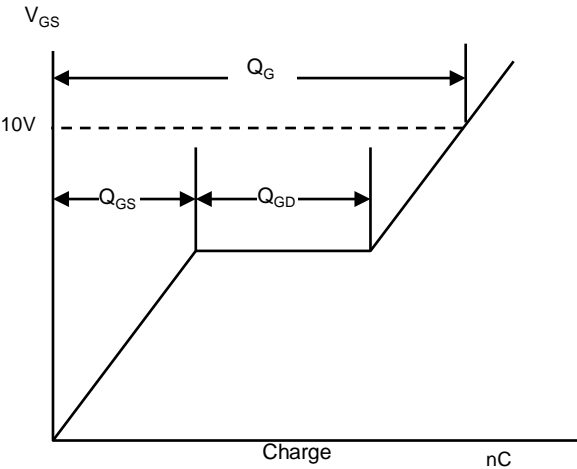
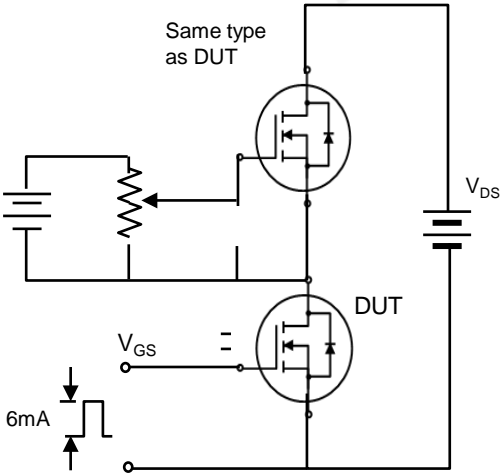


Fig. 11. Switching time test circuit & waveform

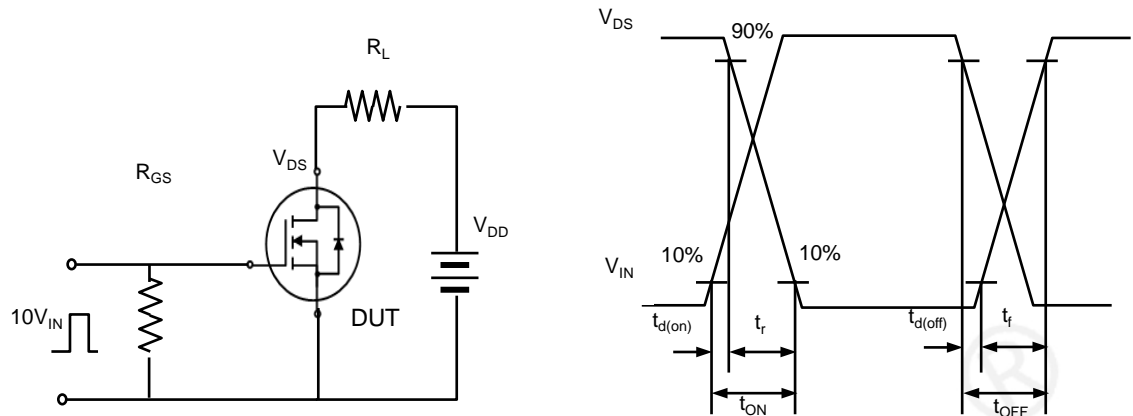


Fig. 12. Unclamped Inductive switching test circuit & waveform

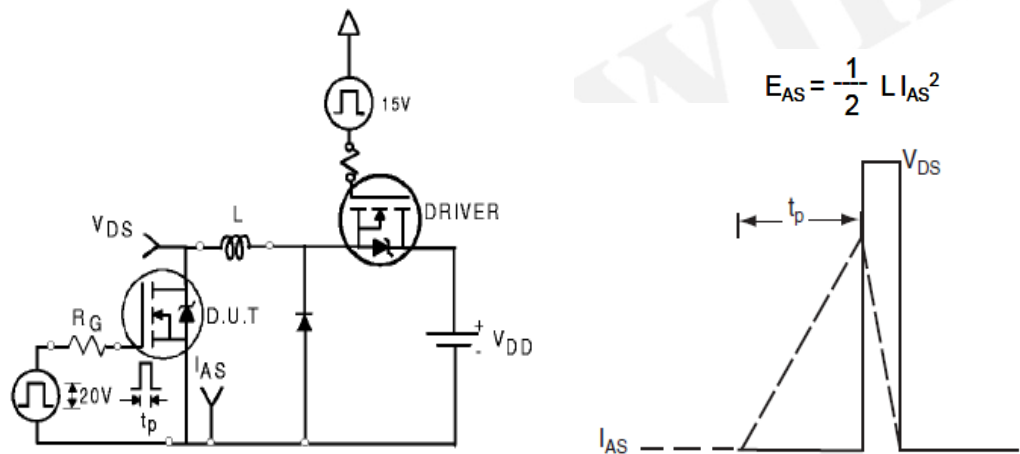
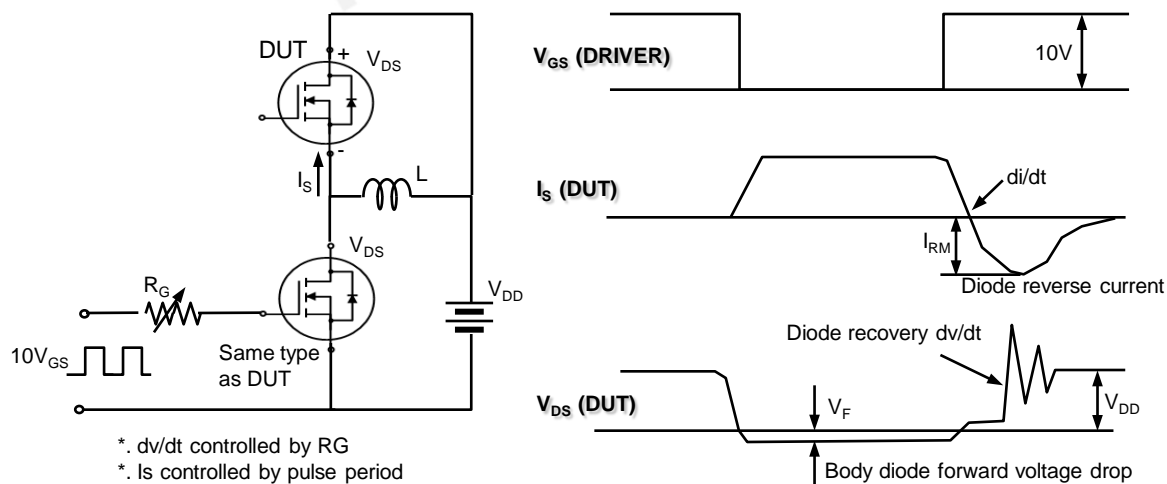



Fig. 13. Peak diode recovery dv/dt test circuit & waveform



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DISCLAIRATION:

- * All the data&curve within this document was tested in XI'AN SEMIPOWER TESTING&APPLICATION CENTER.
- * This product has passed the PCT,TC,HTRB,HTGB,HAST,PC and Solderdunk reliability testing.
- * Qualification Standards can also be found on the Web site (<http://www.semipower.com.cn>) 
- * Any advice, please send your proposal to samwin@samwinsemi.com