Aror University of Art, Architecture, Design & Heritage Sukkur

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Assignment 01   Date: Jan 18, 2013
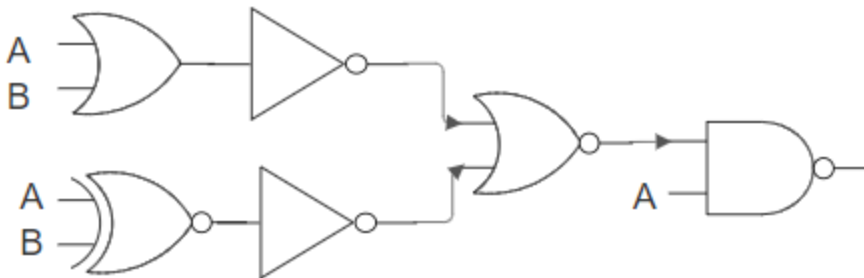
Subject: Digital Logic Design (CS103) (Theory)   Mark: 02

Instructor: Abdul Ghafoor   **Due Date: Jan 24, 2024**

Understand the following logic gate circuit:

Task 01: Find the follow truth table of logic circuit: (1 mark)

Input		Output
A	B	
0	1	
1	0	
0	0	
0	0	

Task 02: Find the logic equation (Boolean expression) of the circuit (1 mark)

Instruction:

- Complete both tasks on paper using a pen or pencil.
- Scan the solution paper and upload them to Google Classroom within the specified time.
- In case of any technical difficulties preventing you from joining the Google Classroom, submit the assignment as a hard copy before the deadline. Please address any issues related to Google Classroom promptly, as future assignments will only be accepted through this platform.