

# TEST SET

1

*All the tests are done with the system having following processor:*

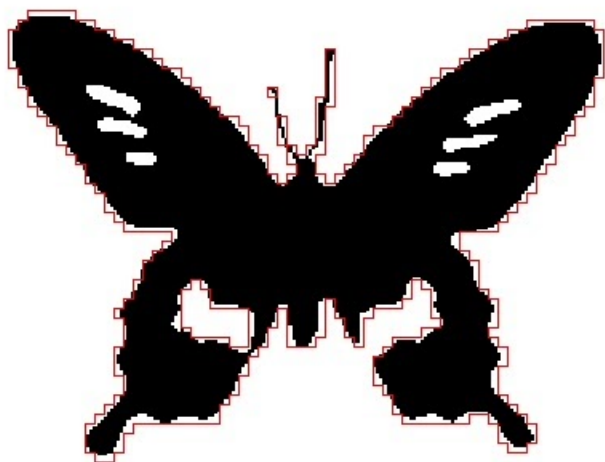
**Processor:** Intel(R) Core(TM) i5-8265U CPU @ 1.60GHz, 1800 Mhz, 4 Core(s), 8 Logical Processor(s)



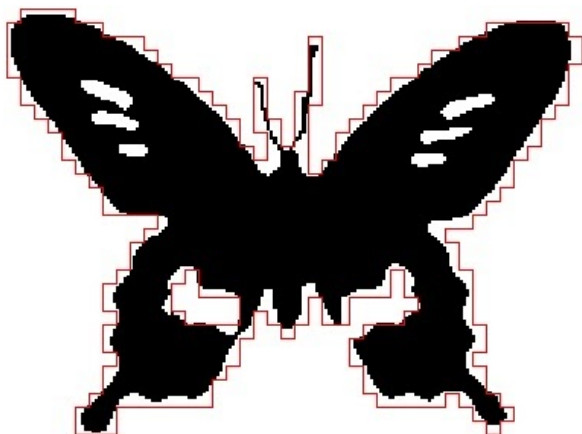
**Resolution:** 410x314

Outer Isothetic Cover (g=6,t=42ms )

Inner Isothetic Cover (g=6,t=44ms)



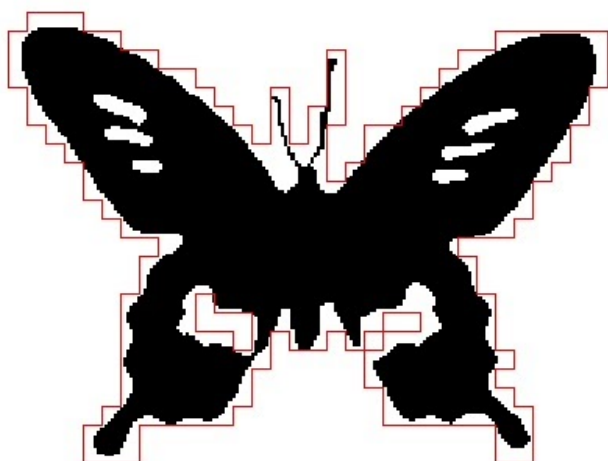
Outer Isothetic Cover( $g=9$ ,  $t=32\text{ms}$ )



Inner Isothetic Cover( $g=9$ ,  $t=31\text{ms}$ )



Outer Isothetic cover (  $g=12$ ,  $t=28\text{ms}$  )



Inner Isothetic Cover( $g=12$ ,  $t=27\text{ms}$  )



2

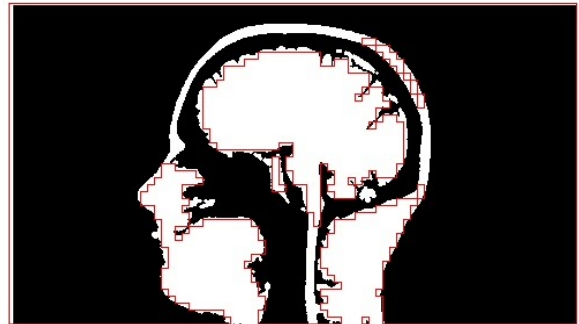


Resolution: 561x340

Outer Isothetic Cover (g=6, t=63ms)



Inner Isothetic Cover(g=6, t=57ms)



Outer Isothetic Cover (g=9, t=52ms)

Inner Isothetic Cover(g=9, t=46ms)



Outer Isothetic Cover ( $g=12$ ,  $t=43\text{ms}$ )



Inner Isothetic Cover( $g=12$ ,  $t=39\text{ms}$ )





**Resolution: 1116 x 424**

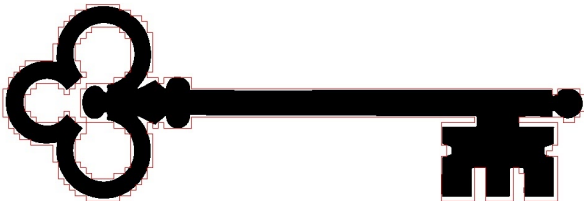
Outer Isothetic Cover (g=6, t=136ms)



Inner Isothetic Cover (g=6, t=122ms)



Outer Isothetic Cover (g=9, t=99ms)



Inner Isothetic Cover (g=9, t=88ms)

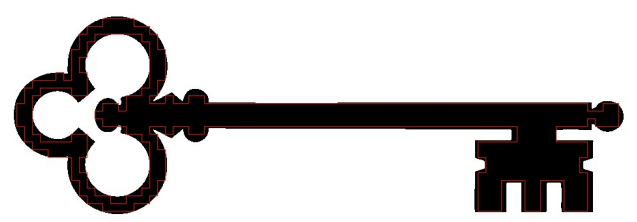
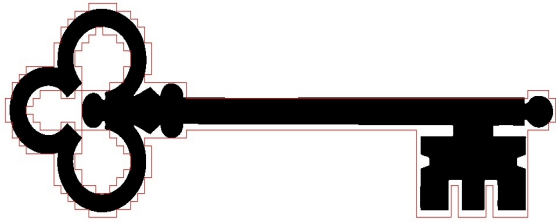


Outer Isothetic Cover (g=12, t=86ms)



Inner Isothetic Cover (g=12, t=78ms)







Resolution: 578x514

Outer Isothetic Cover (g=6, t=182ms)



Inner Isothetic cover(g=6, t=195ms)



Outer Isothetic Cover (g=9, t=121ms)



Inner Isothetic Cover (g=9, t=115ms)





Outer Isothetic Cover (g=12, t=100ms)



Inner Isothetic Cover (g=12, t=110ms)







Resolution: 1520 x 1446

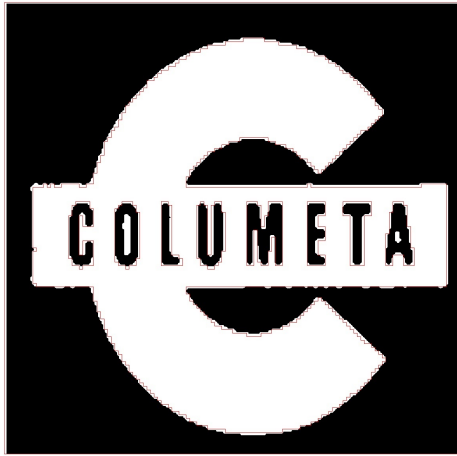
Outer Isothetic Cover(g=6, t=1059ms)



Inner Isothetic Cover(g=6, t=965ms)



Outer Isothetic Cover(g=9, t=677ms)



Inner Isothetic Cover(g=9, t=625ms)



Outer Isothetic Cover(g=12, t=519ms)



Inner Isothetic Cover(g=12, t=505ms)

