

## **BRAC UNIVERSITY**

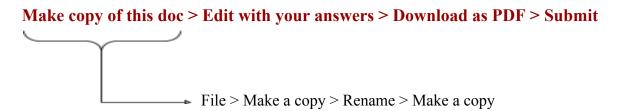
CSE250I

Dept. of Computer Science and Engineering

Circuits and Electronics Laboratory

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## Simulation Labtest CSE250 Fall'23

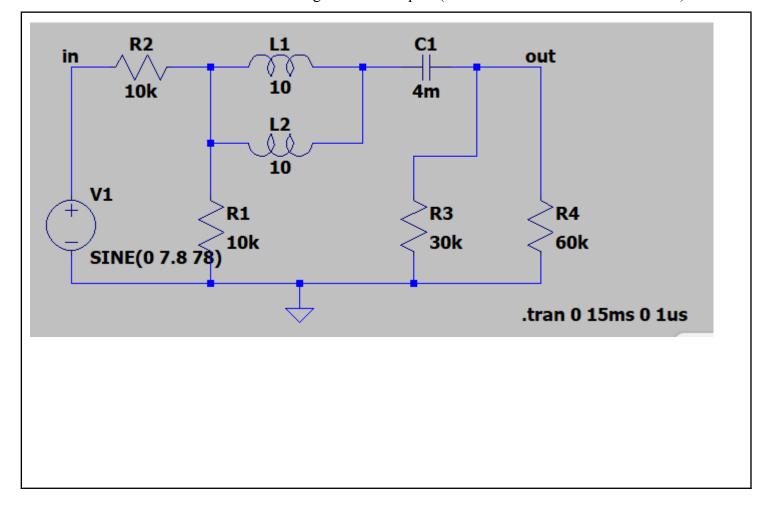


## **Answers**

A. Phase difference between the indicated voltage and current

Quantity	Value	
θ	0.046 degree	

B. Screenshot of schematic diagram from LTspice (simulation command must be visible)



C. Screenshot of the plot of the capacitor voltage and current as a function of time from LTspice

