

07d0512060c21006d009b711520033f5

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NOTES:
1.HSF Property:Comply iSupplier system HSF property attribute up-to-date value.

CALTECH 11.6"
OXFORD 14"
(BSW)
CS2 BUILD
2015.5.15

21-OCT-2002		
DATE	CHANGE NO.	REV

INVENTEC			
MODEL,PROJECT,FUNCTION			
DESIGN/DRAWER	XXX	DATE	21-OCT-2002
CHECK			
APPROVAL			
TITLE NAME	CALTECH OXFORD	SIZE	A3
CODE	CS	DOC NUMBER	1310xxxxx-0-0
PCB PIN	XXXXXXXX	PCB VER	AX1
		SHEET	1 of 1
		REV	X01

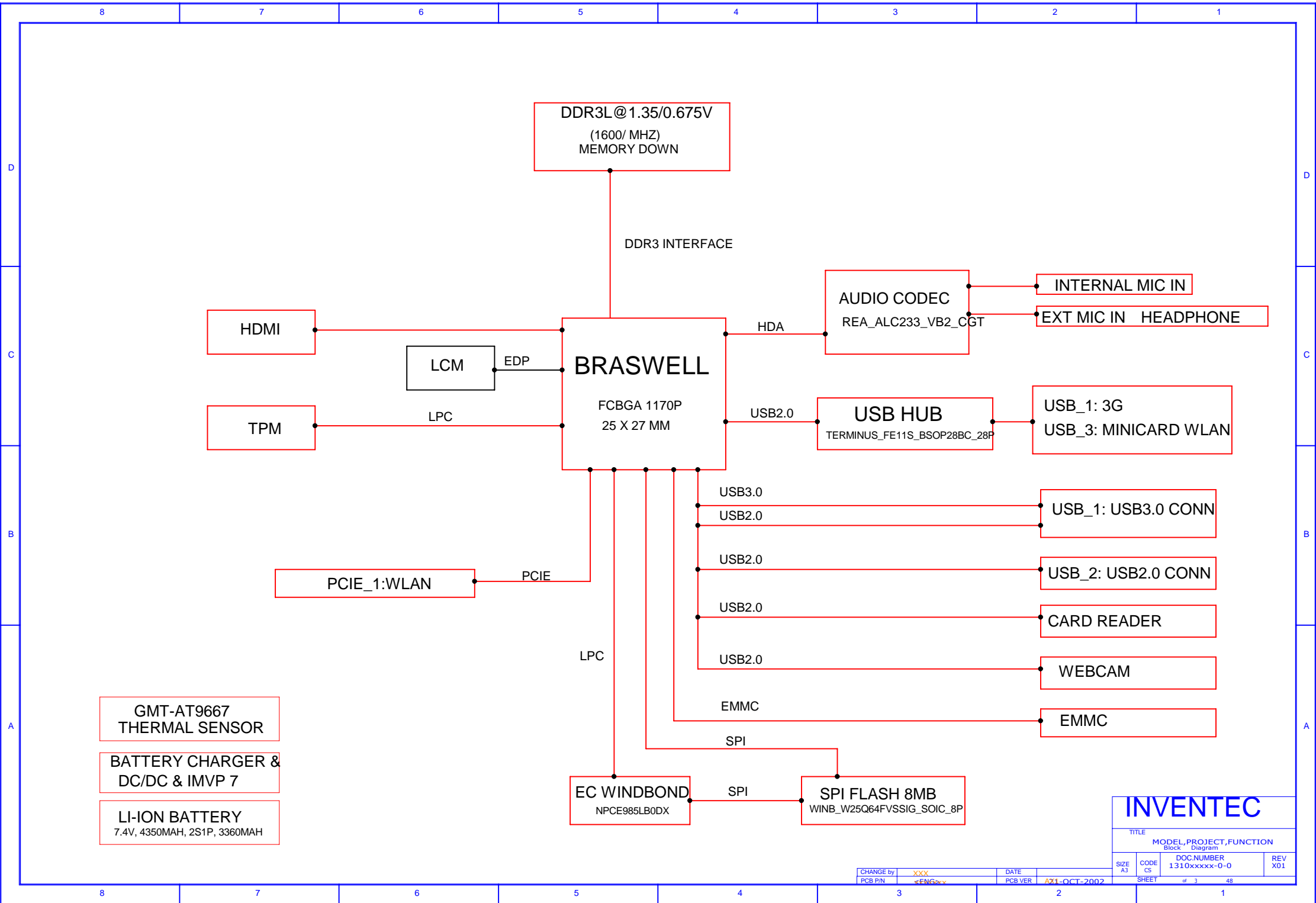
TABLE OF CONTENTS

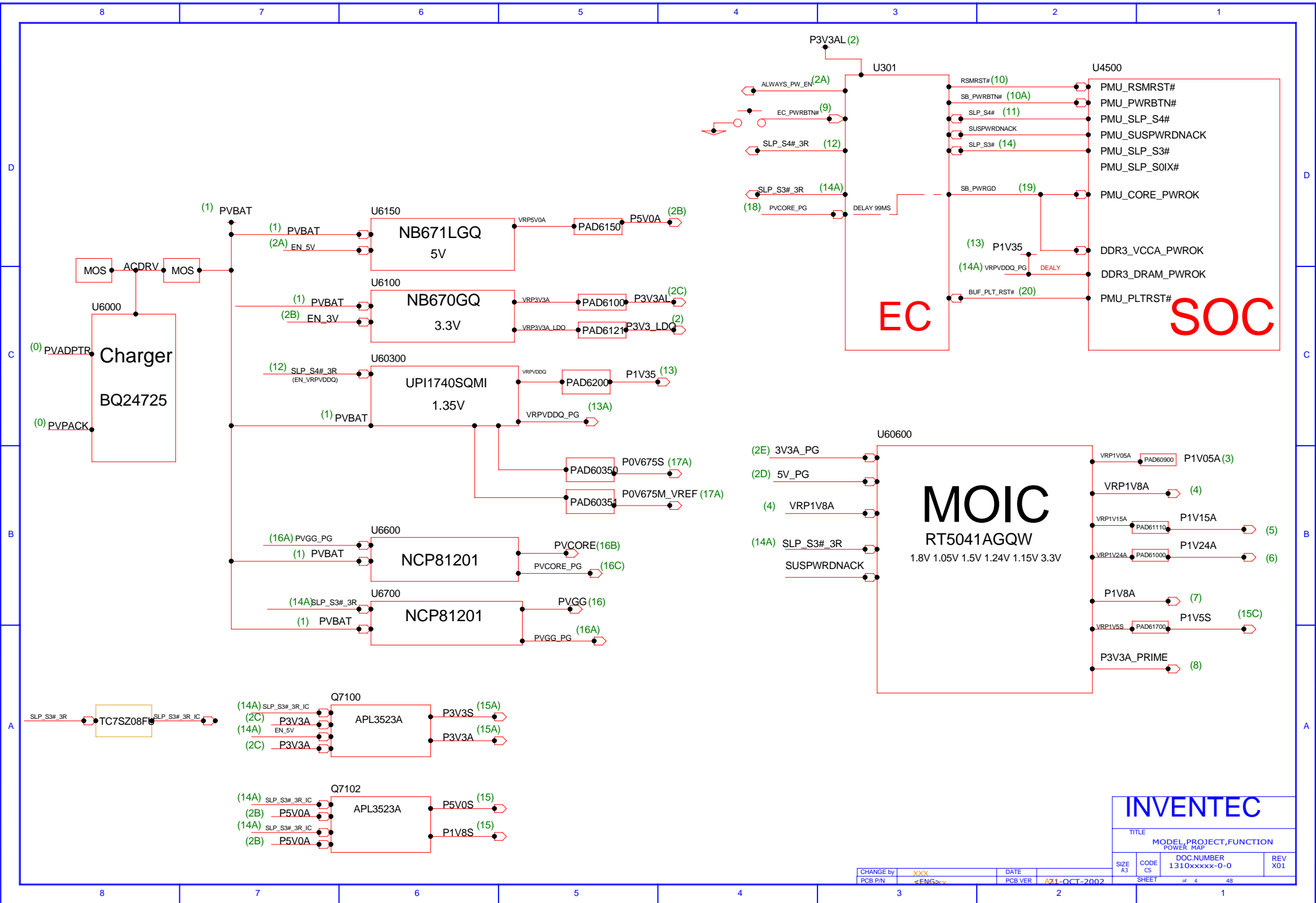
PAGE	PAGE
1. COVER PAGE	27. EDP
2. INDEX	28. HDMI
3. BLOCK DIAGRAM	29~30.DDR3L
4. POWER MAP	31. THERMAL&DDR RESISTER
5. POWER CHARGER	32~42. CHIPSET
6. POWER +V3.3A/+5A	43.LEVEL SHIFT
7. POWER +V1.35S	44. USB2.0 HUB
8. POWER VCORE	45. EMI
9. POWER VCORE/MOS	46. USB2.0 DB
10. POWER PVGG	47. APP LED DB(NO COMPONENT)
11. POWER PVGG/MOS	48. PWRBUTTON DB
12. MOIC(1V8.1V05.1V5.1V24.1V15)	
13. ENABLE PIN	
14. LOAD SWITCH	
15. PCB SCREW	
16. KB&TOUCH PAD CONN	
17. EC	
18. AUDIO CODEC	
19. SPEAKER/HP JACK/MIC JACK	
20. CARDREADER	
21. NGFF WLAN/DEBUG CARD	
22. 3G(NO COMPONENT)	
23. USB2.0	
24. USB3.0	
25. EMMC	
26. POWER BOARD / TPM	

INVENTECTITLE
MODEL,PROJECT,FUNCTION
CATELOG

SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
SHEET		2 of 48	

CHANGE by PCB P/N	XXX xxxxxxx	DATE PCB VER	21-OCT-2002 AX1
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INVENTEC

TITLE
MODEL PROJECT,FUNCTION
POWER MAP

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX PCB P/N DATE PCB VER A71-OCT-2002

SHEET of 4 48

D

C

B

A

D

C

B

A

PVADPTR DON'T PUT ANY CAPACITY

DON'T PUT ANY CAPACITOR

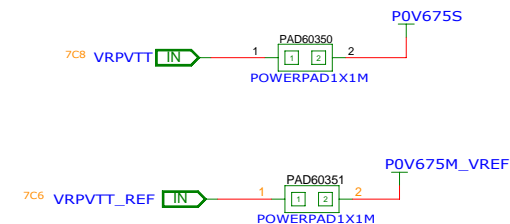
DON'T PUT ANY CAPACITOR

INVENTEC

CHANGE by	XXX	DATE	
PCB P/N	<ENG>	PCB VER	A71-OCT-2002

TITLE			
MODEL PROJECT,FUNCTION			
CHARGER			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		of 5	48

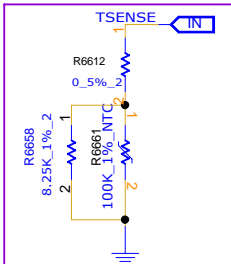




STATE	S3	S5	VTT	VTTREF
Normal	Hi	Hi	VTTREF	VDDQSNS/2
Standby	Lo	Hi	OFF (High-Z)	VDDQSNS/2
Shutdown	Lo	Lo	0V (Discharge)	0V (Discharge)

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	
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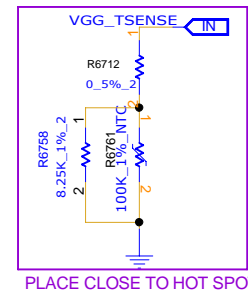
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PCB P/N	XXXXXXX	PCB VER	AX1



SHEET 8 of 48

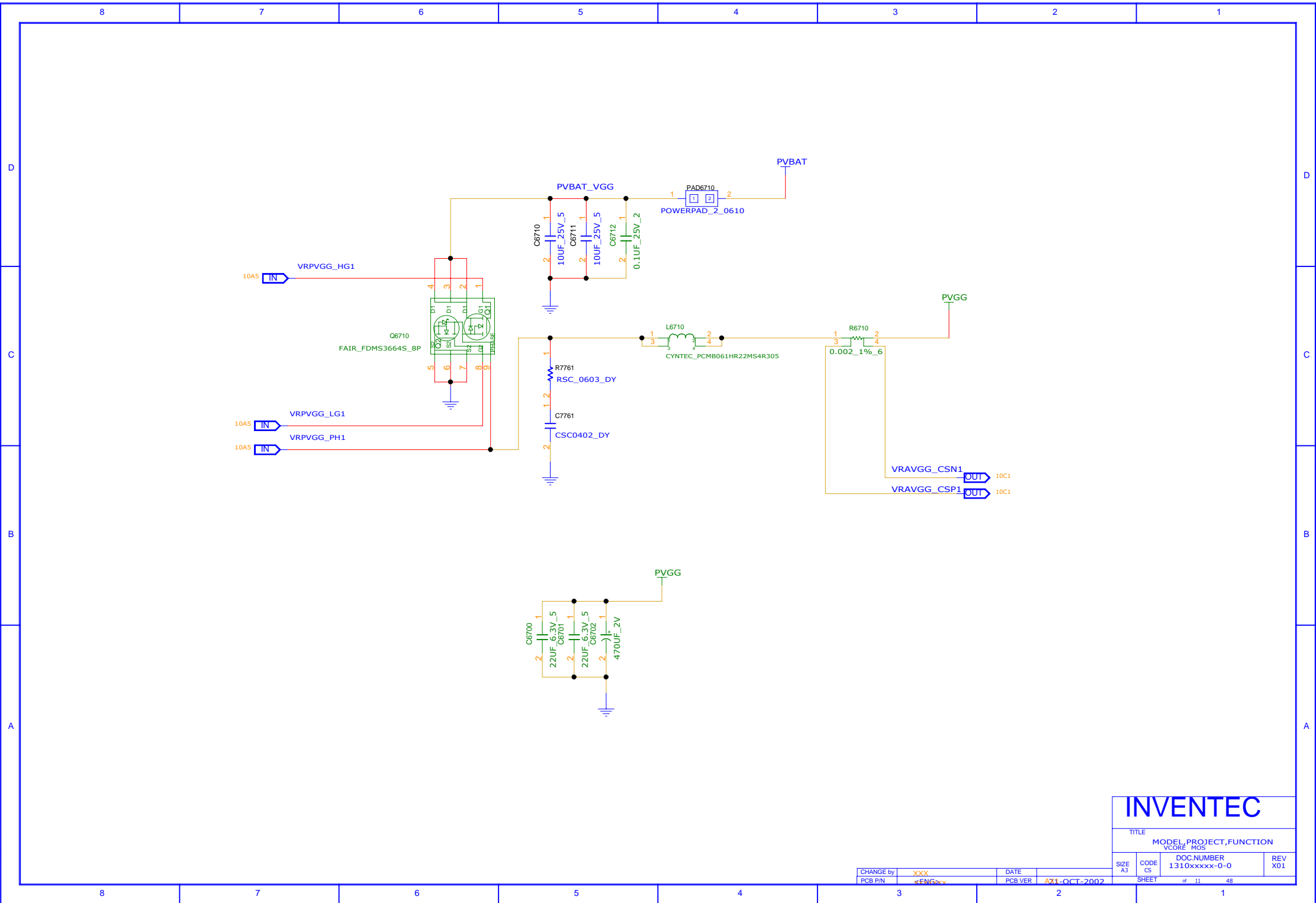


CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



A3	CS	1510XXXXX-0-0	A
SHEET		10 of 48	

DATE	21-OCT-2002
PCB VER	AX1



INVENTEC

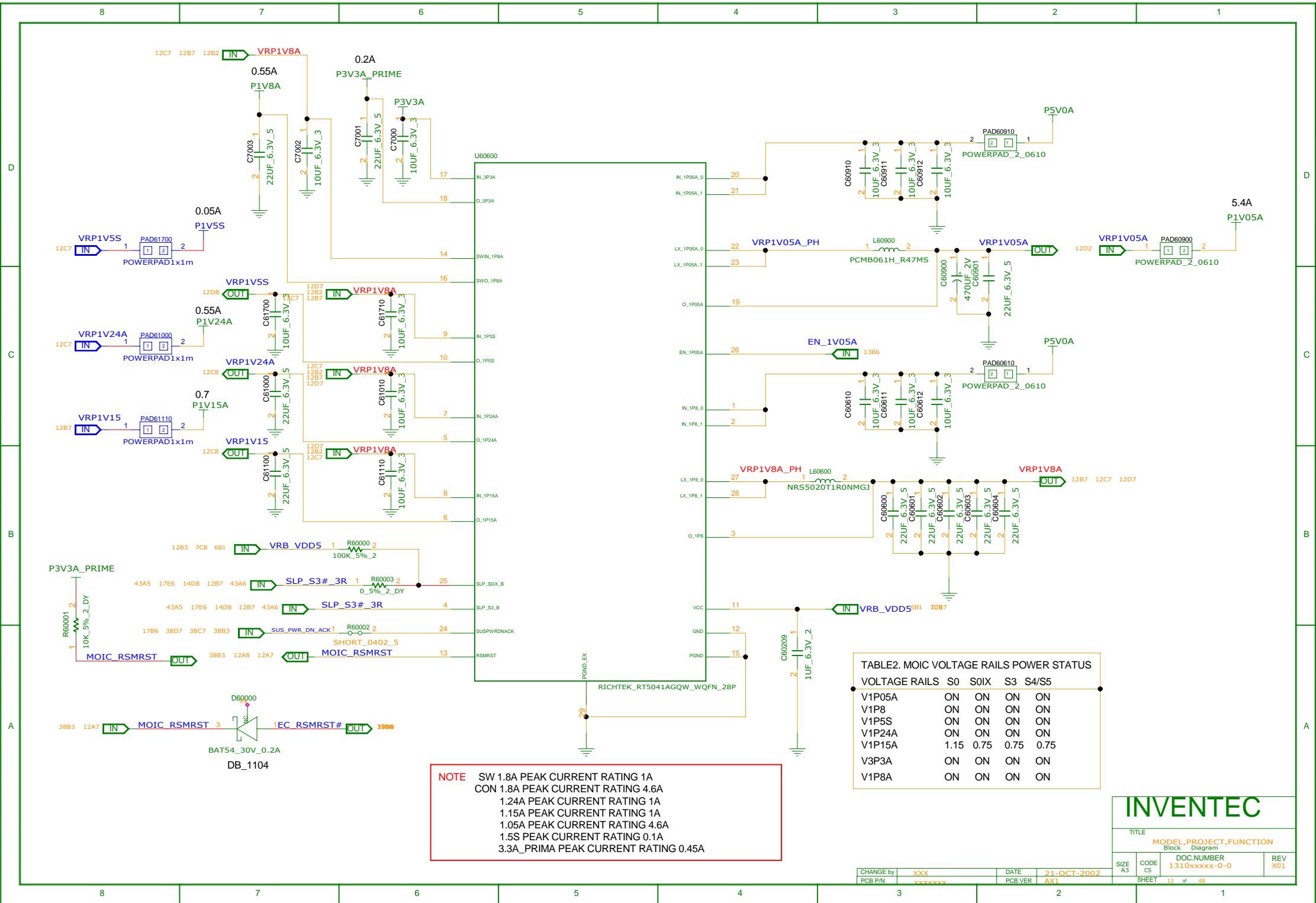
TITLE MODEL PROJECT,FUNCTION

VCORE M05

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE PCB VER A71-OCT-2002

SHEET of 11 48



INVENTEC

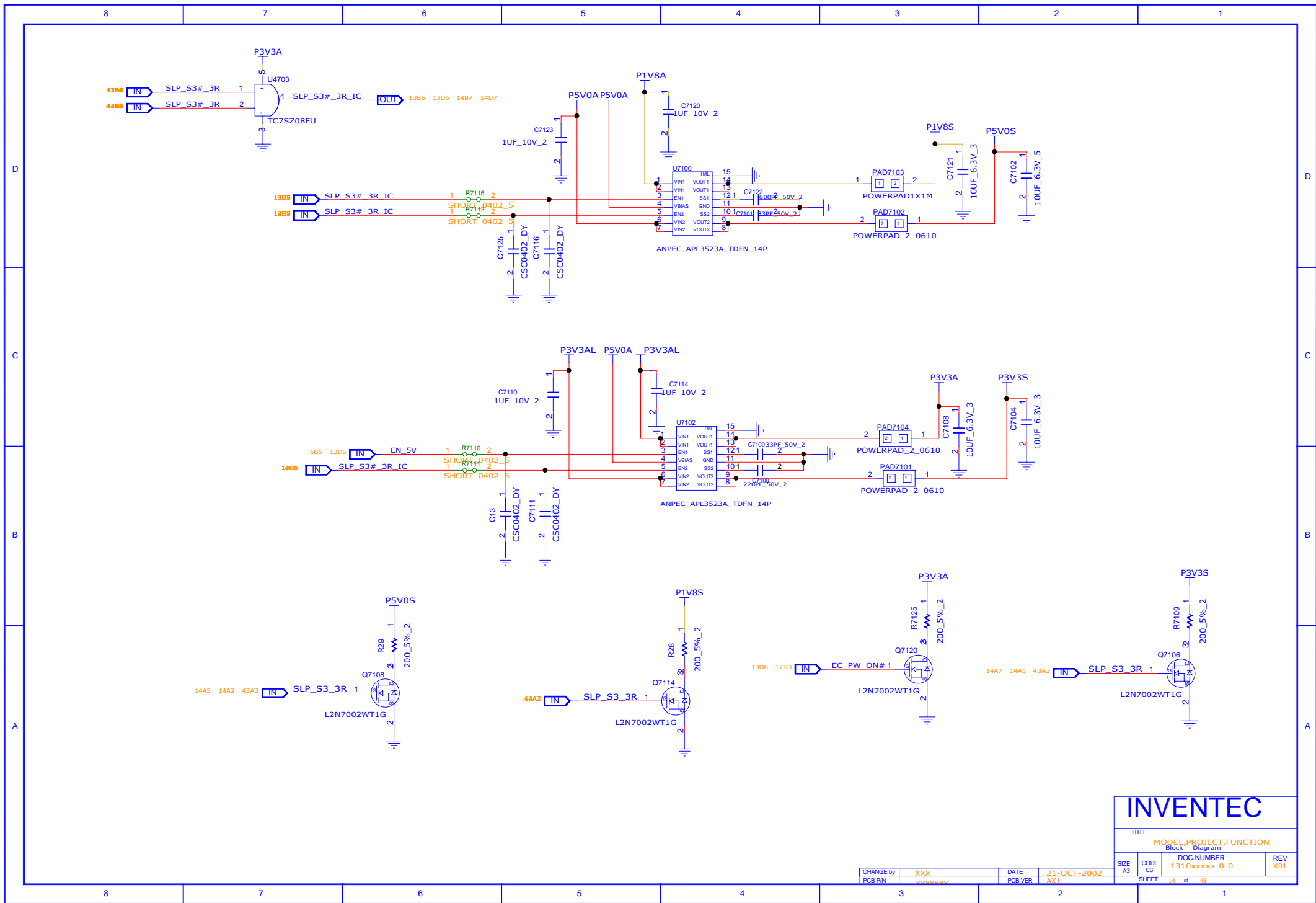
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MODEL,PROJECT,FUNCTION			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET	12 of 48		

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



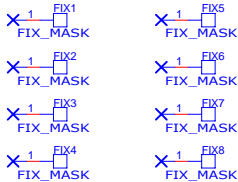
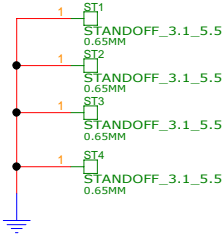
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SHEET		13 of 48	

SHEET 13 of 48

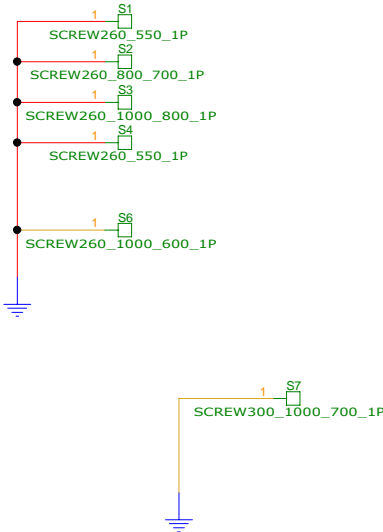


REFERENCE 0~49(PCB SCREW)

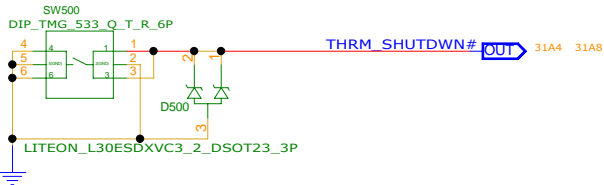
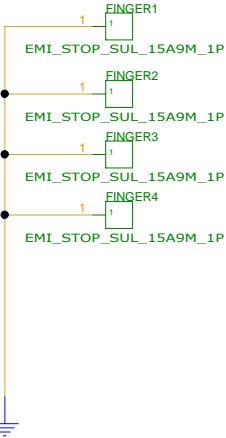
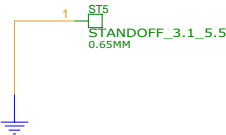
CPU



PCB



3G



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

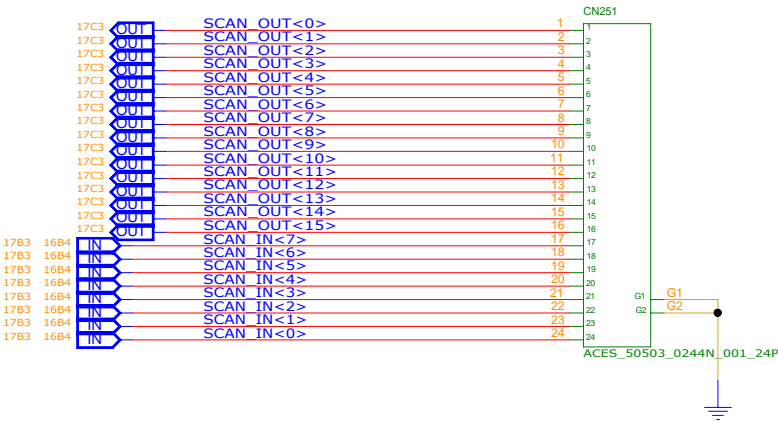
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CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

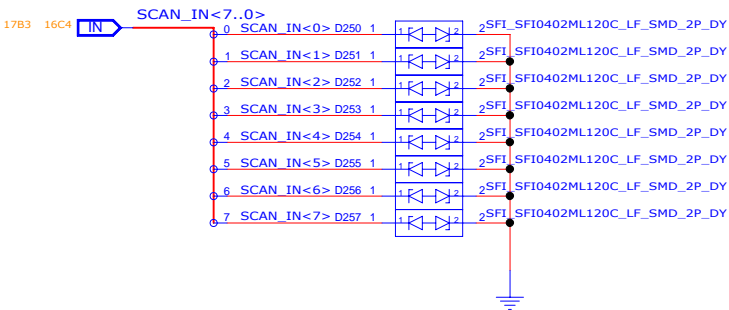
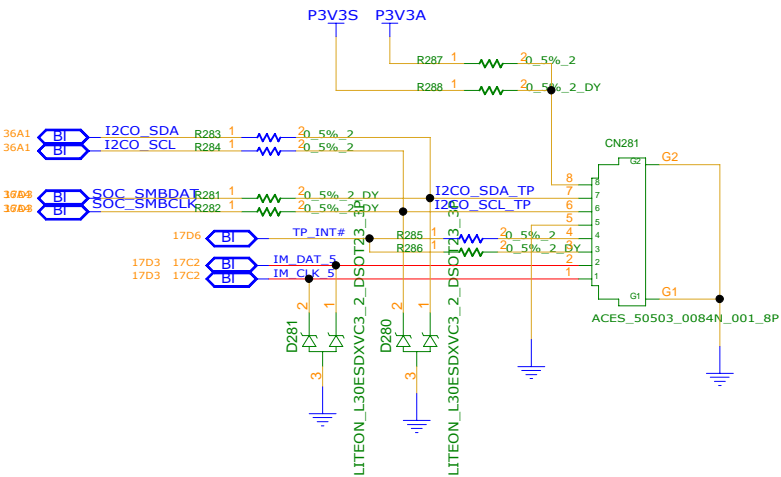
SHEET 15 of 48

REFERENCE 250~299(KB/TP CONN)

KEYBOARD CONN



TOUCHPAD CONN (CLICK PAD)



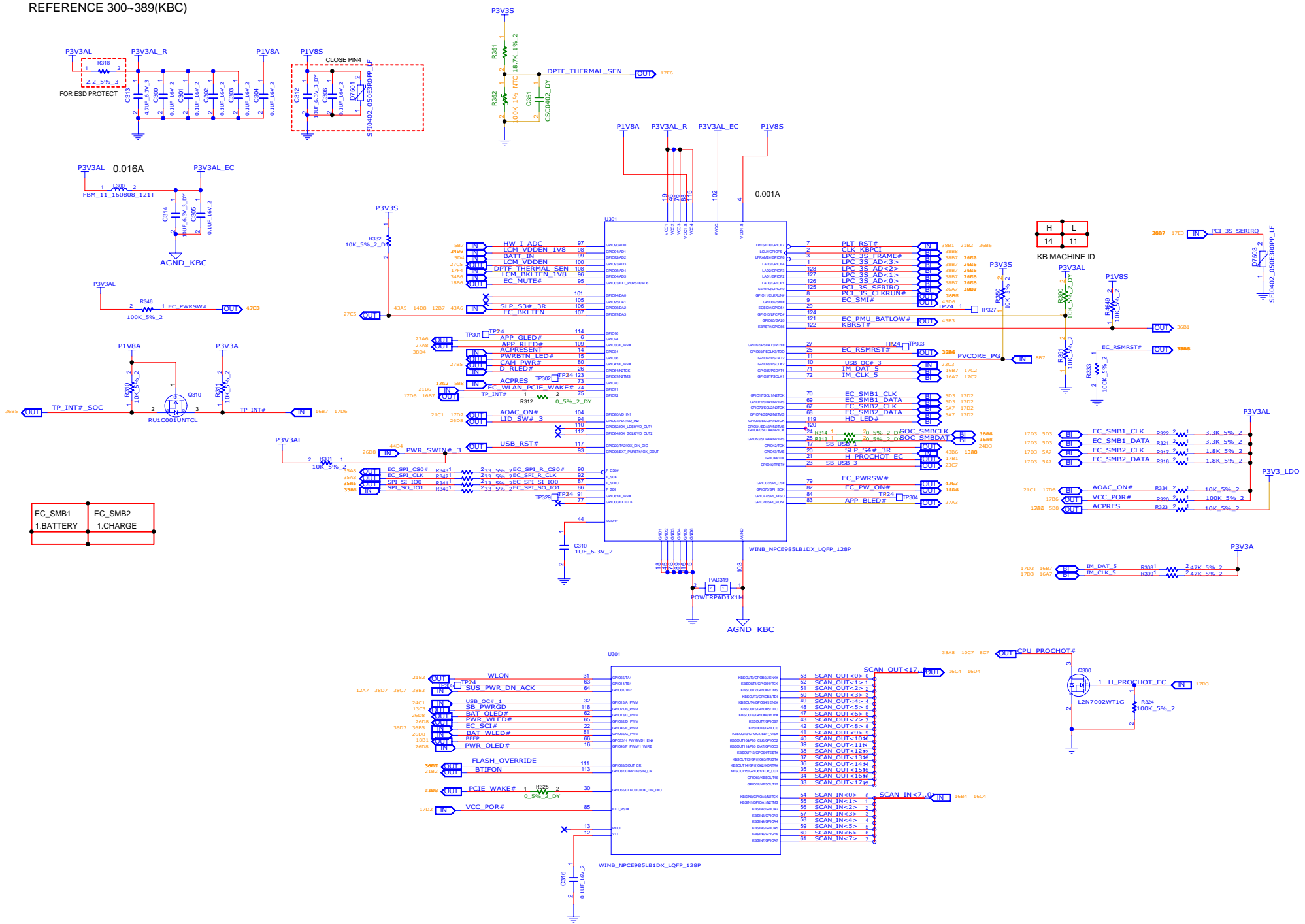
INVENTEC

TITLE
MODEL,PROJECT,FUNCTION

SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

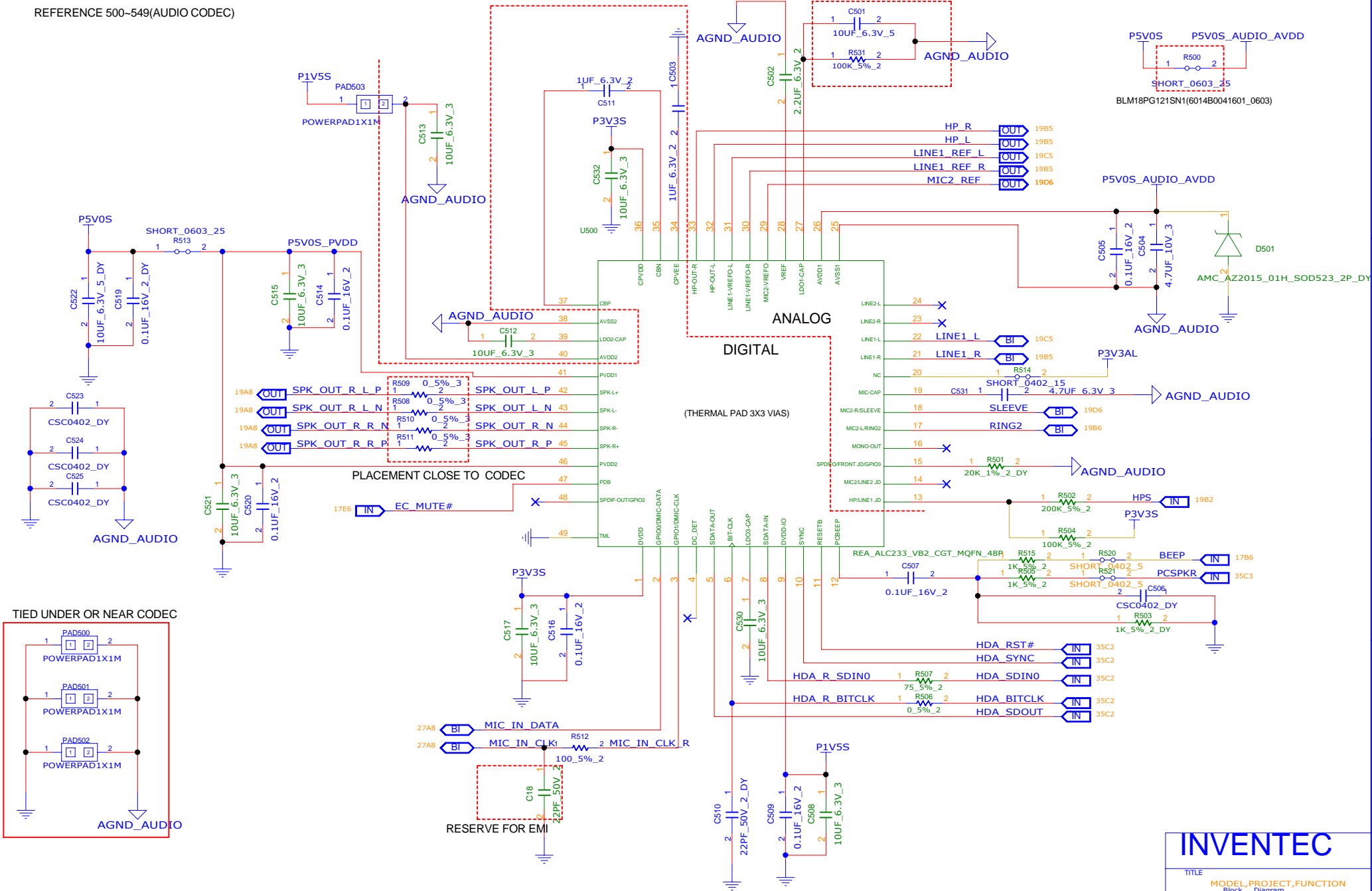
REFERENCE 300~389(KBC)



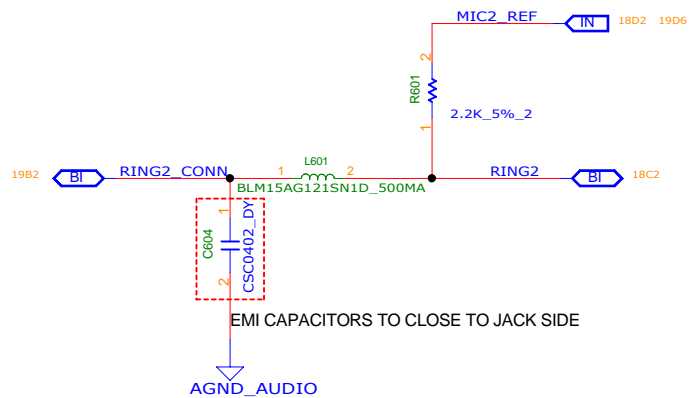
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
EC			
SIZE	CODE	DOC NUMBER	REV
C	CS	1310xxxxxx-0-0	X01
SHEET		of 17	48

CHANGE by	XJENG>	DATE	21-OCT-2002	SIZE A3	CODE CS	1310xxxxx-0-0	X01
PCB P/N	xxxxxxx	PCB VER	AXXER>		SHEET	18 of 48	

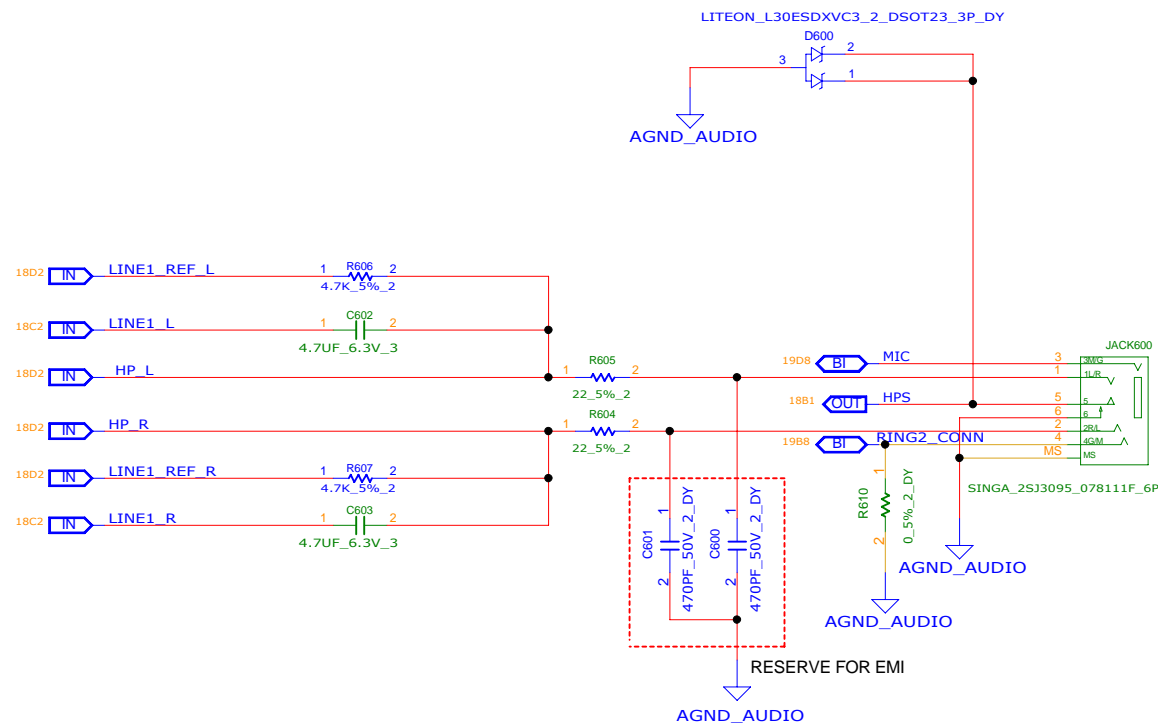


AUDIO JACKS



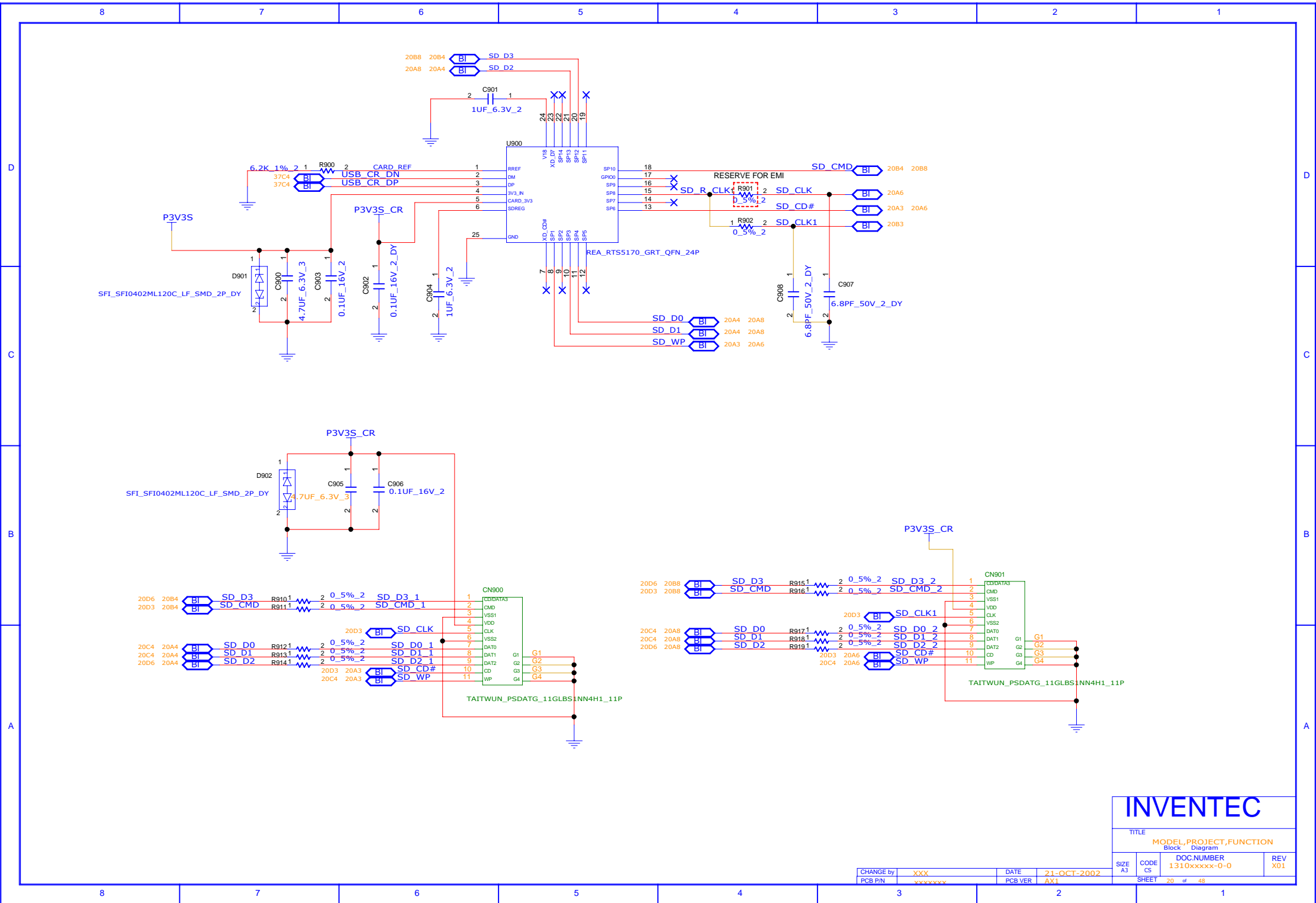
INTERNAL SPEAKERS

RESERVE FOR EMI, PLACEMENT NEAR CONNECTOR



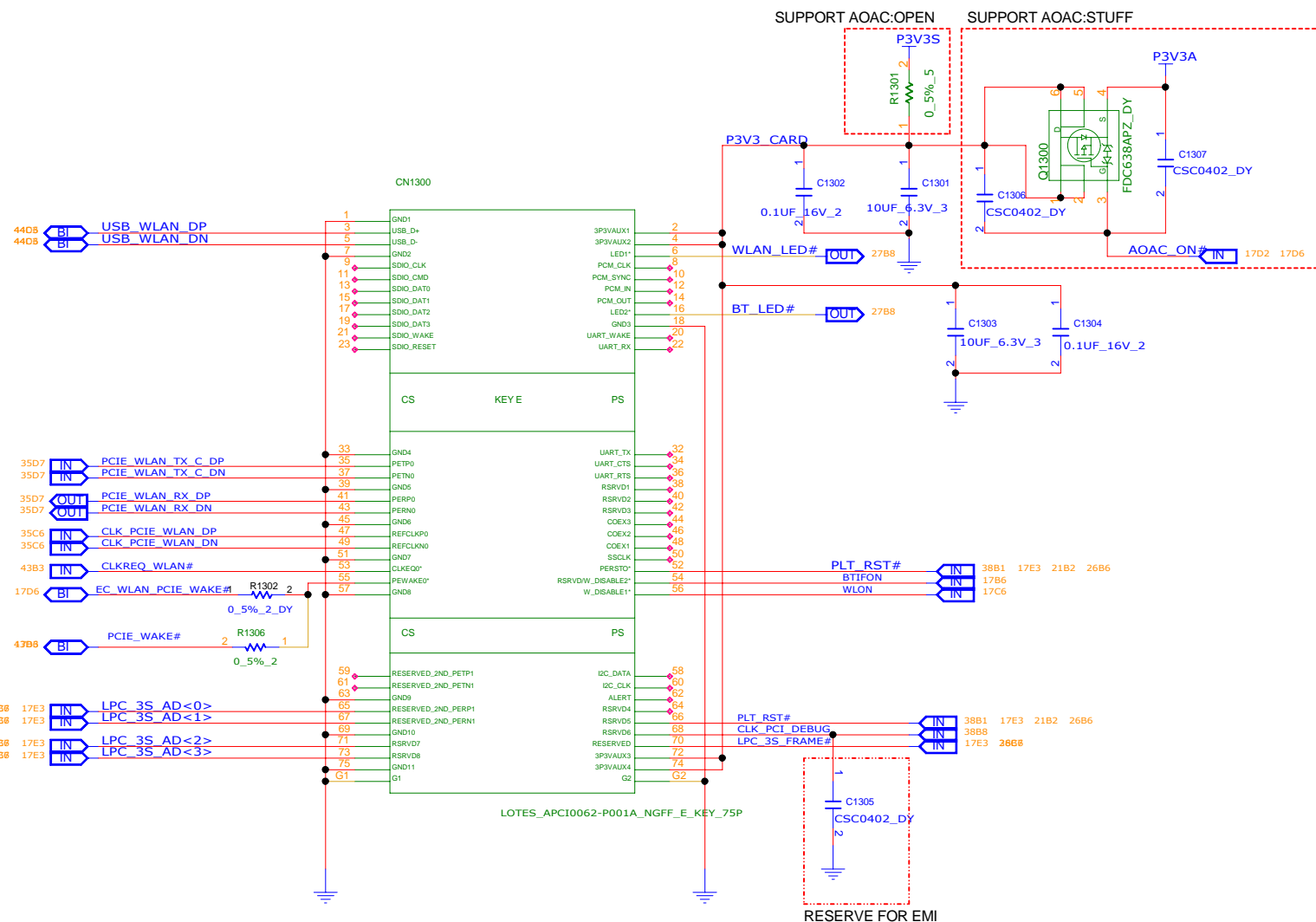
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TITLE			
MODEL,PROJECT,FUNCTION Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxxx-0-0	REV X01
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PCB P/N	xxxxxxx	PCB VER	AX1			SHEET	19	of 48	



WIFI AND DEBUG CARD CO-LAYOUT CONNECTOR

REFERENCE:MD0-134(MVDAN)



INVENTEC				
TITLE				
MODEL,PROJECT,FUNCTION				
Block Diagram				
SIZE	CODE	DOC NUMBER	REV	
A3	CS	1310xxxx-0-0	X01	
SHEET 21 of 48				

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

3G

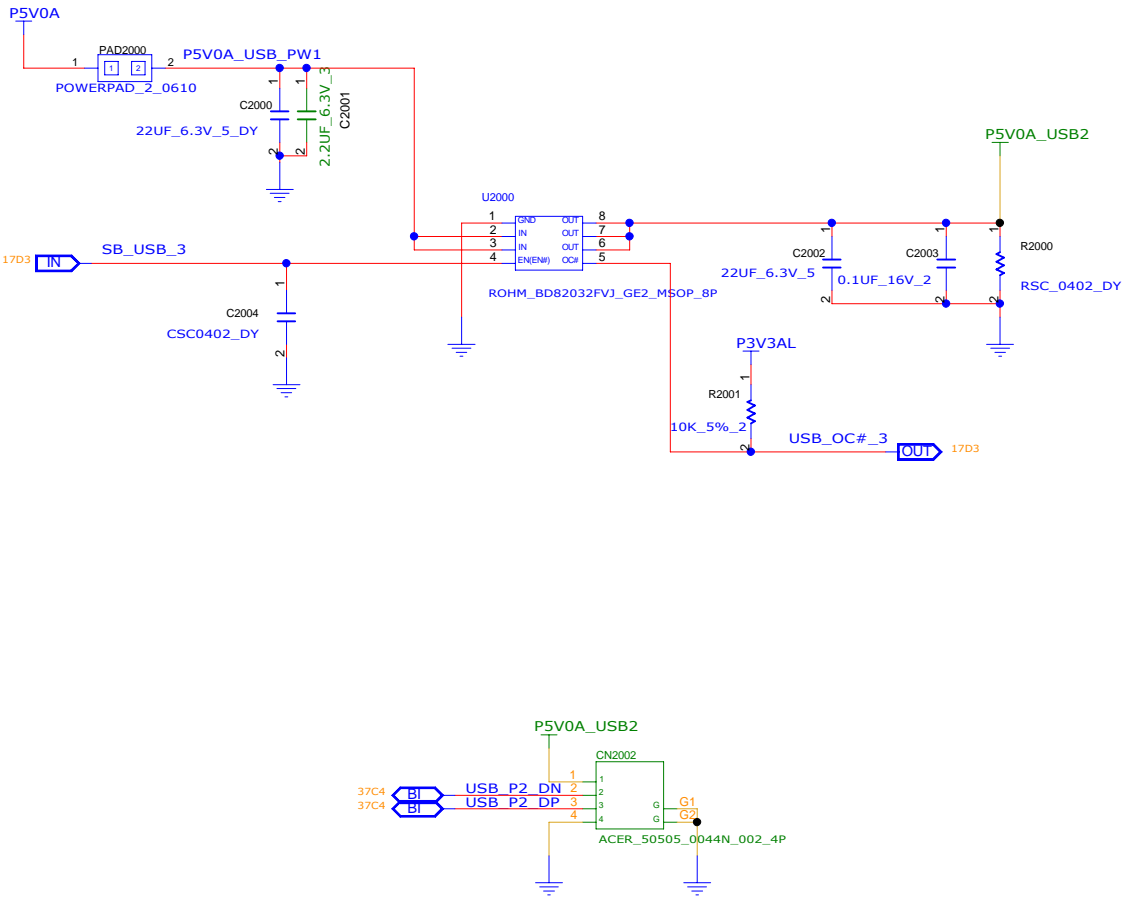
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TITLE	MODEL,PROJECT,FUNCTION
	Block Diagram

SIZE A3	CODE CS	DOC.NUMBER 1310xxxxx-0-0	REV X01
SHEET		22 of 48	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

REFERENCE 2000~2099(USB)



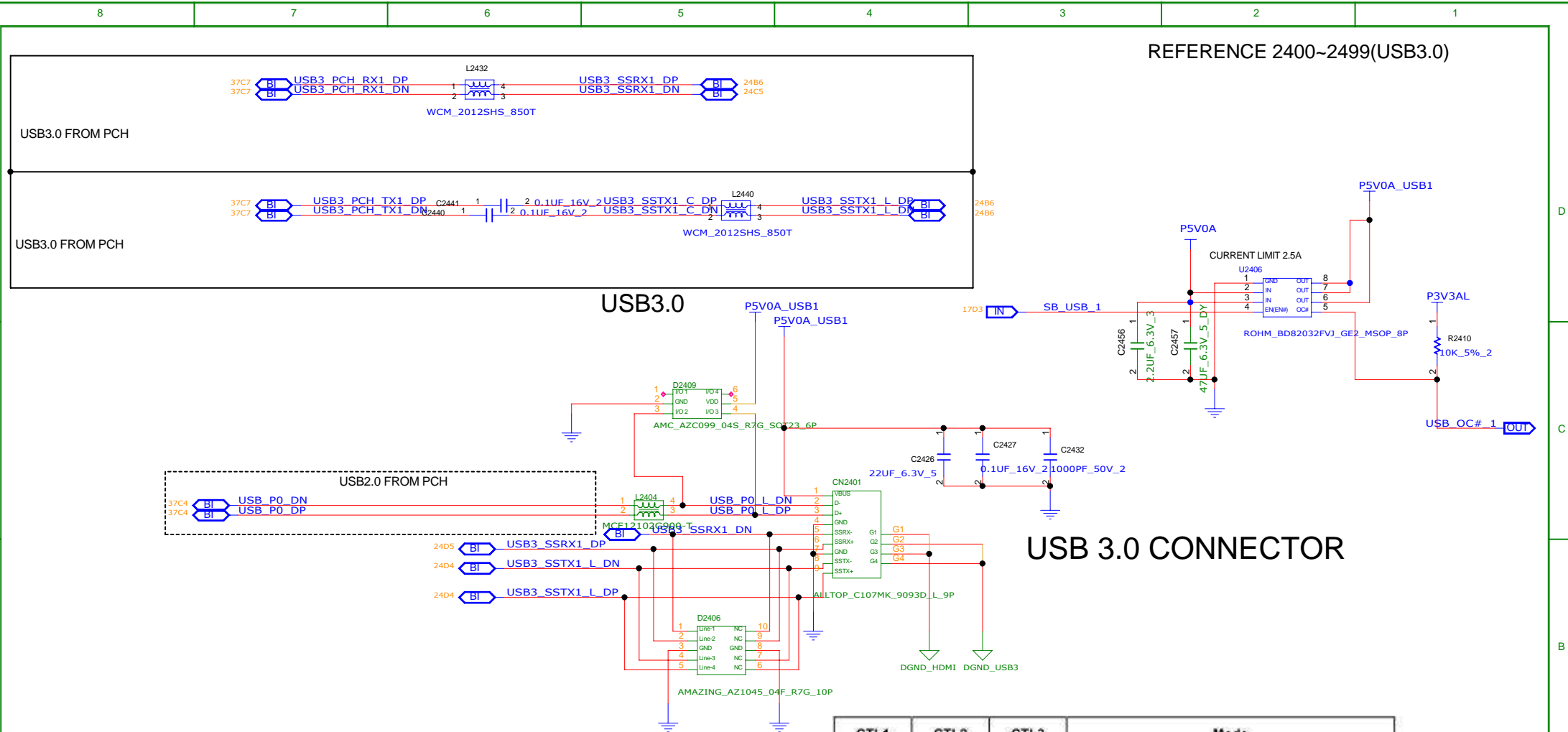
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TITLE
MODEL, PROJECT, FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

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CTL1	CTL2	CTL3	Mode
0	0	X	Dedicated Charging Port, Auto-detect
0	1	X	Dedicated Charging Port, BC Specification 1,1 Only
1	0	X	Dedicated Charging Port, Apple Only
1	1	0	Standard Downstream Port, USB 2.0 Mode
1	1	1	Charging Downstream Port, BC Specification 1.1

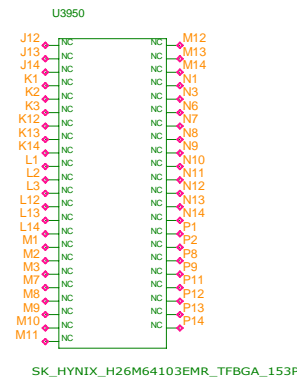
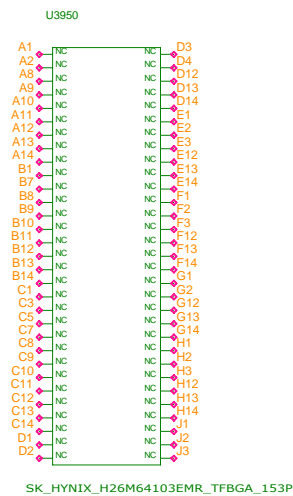
Table 3 – TPS2541 Control Truth Table

INVENTEC

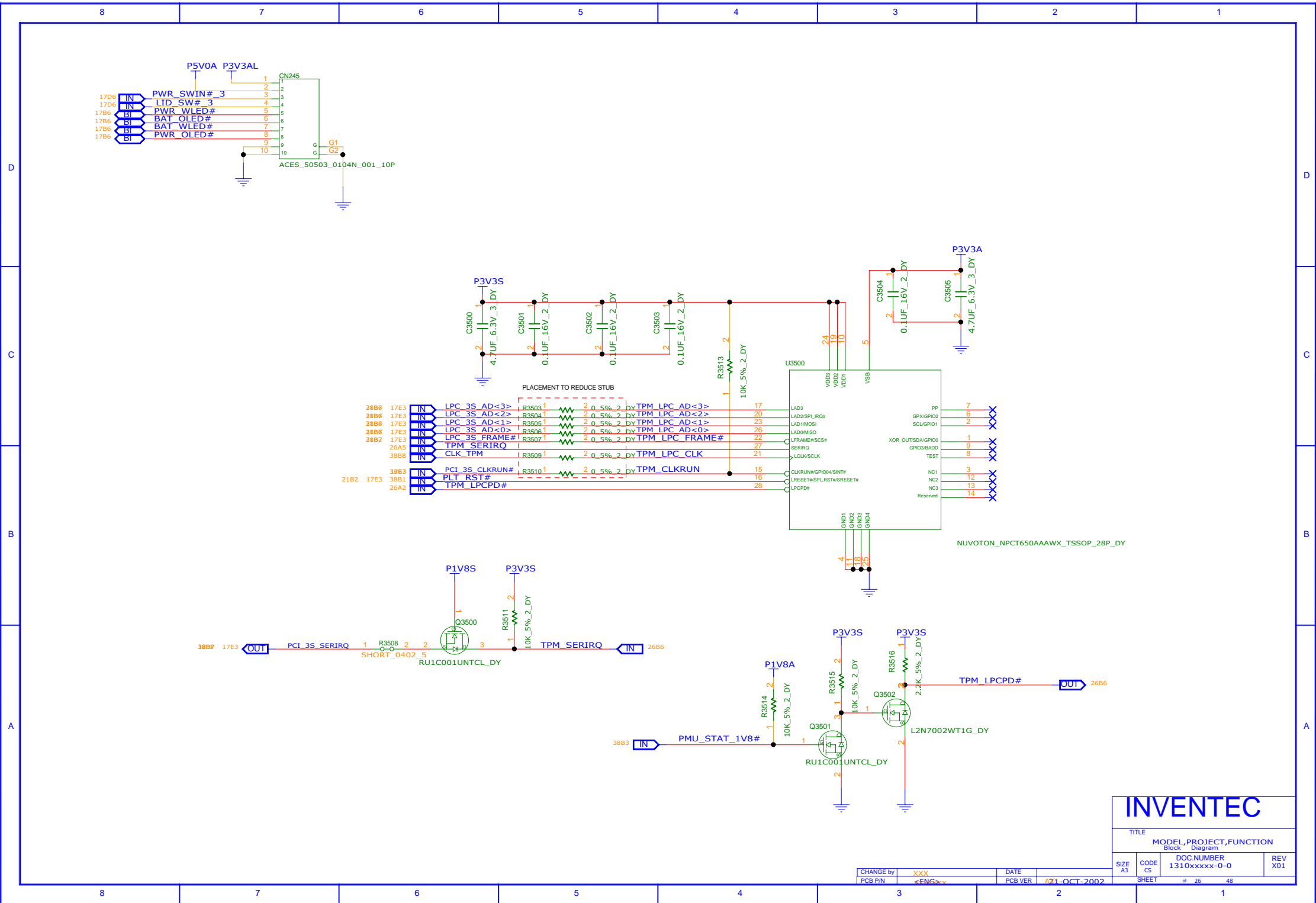
TITLE
MODEL,PROJECT,FUNCTION
Block DiagramSIZE C CODE CS DOC NUMBER
1310xxxxx-0-0

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CHANGE by XXX
PCB P/N xxxxxx
DATE 21-OCT-2002
PCB VER AX1



CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	XXXXXXXX	PCB VER	AX1



INVENTEC

TITLE MODEL,PROJECT,FUNCTION
Block Diagram

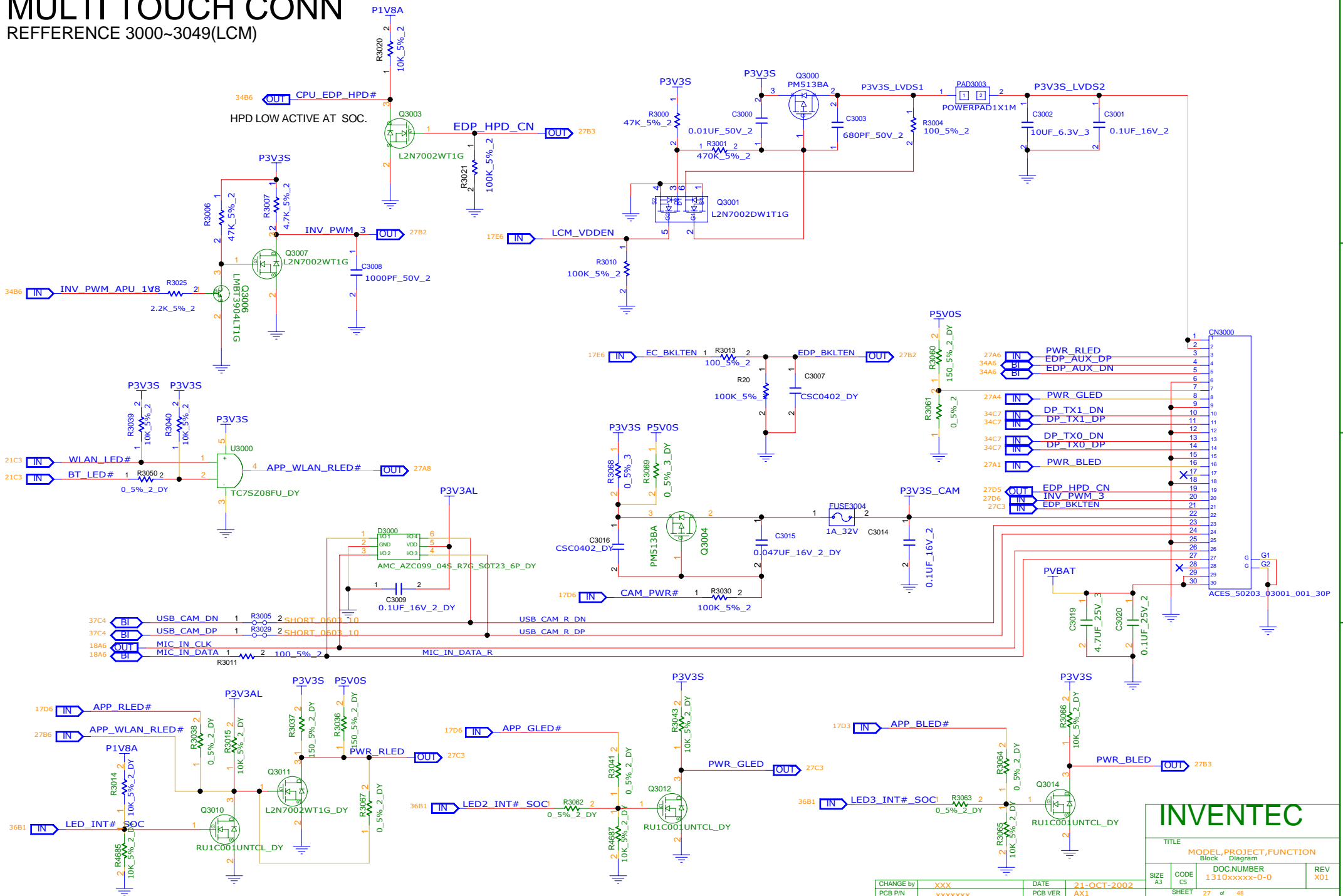
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01

CHANGE by	DATE
PCB P/N	PCB VER

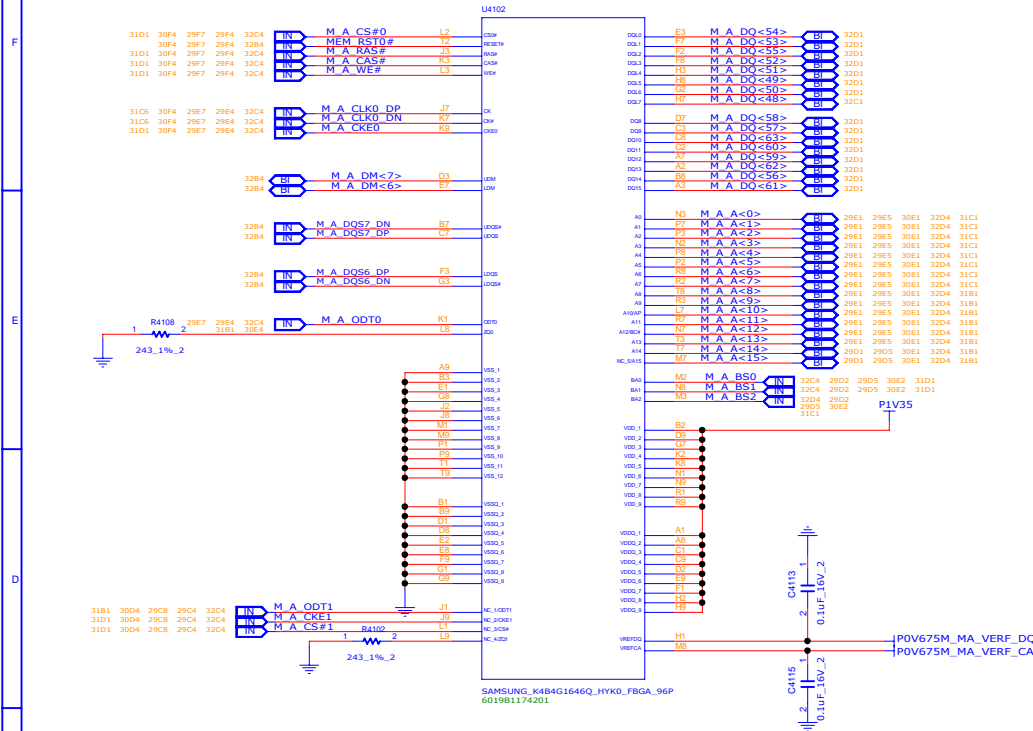
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MULTI TOUCH CONN

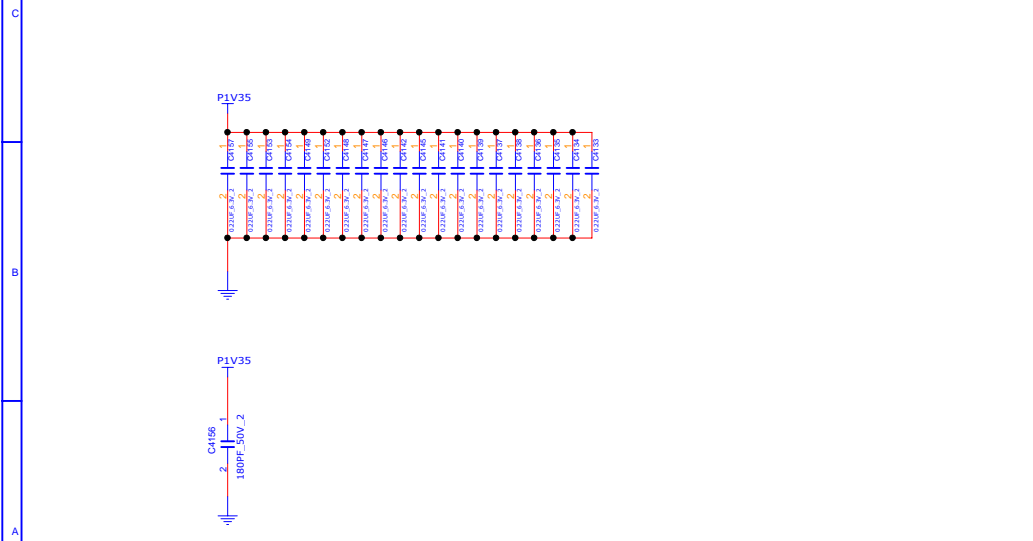
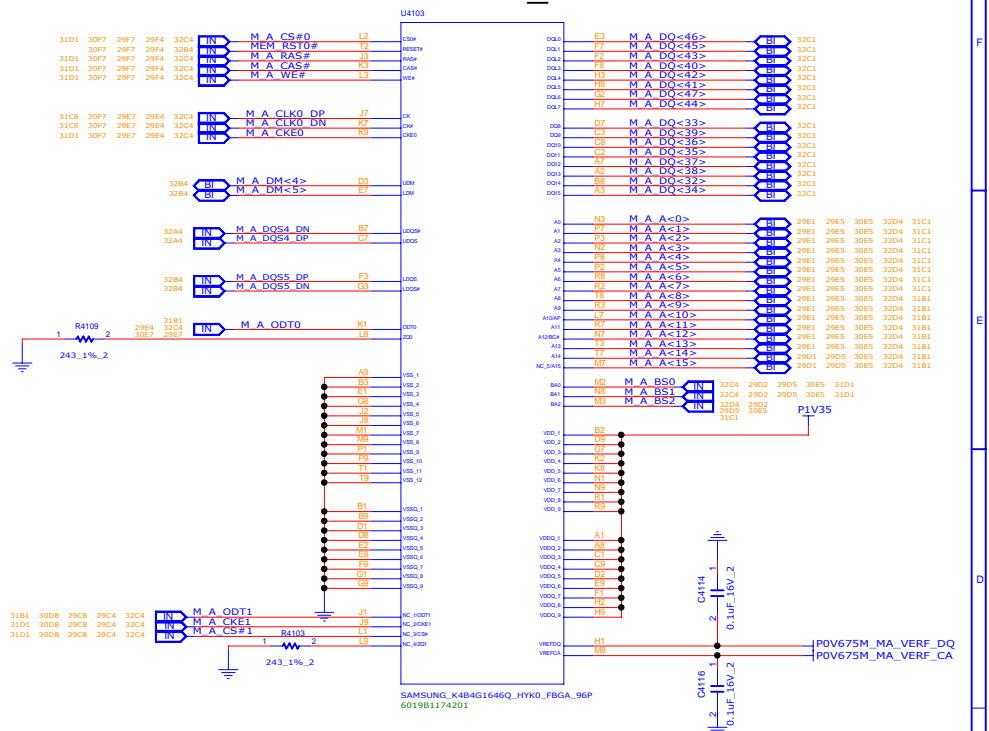
REFERENCE 3000~3049(LCM)



DDR3L_2



DDR3L_3



D

C

B

A

D

C

B

A

8

7

6

5

4

3

2

1

8

7

6

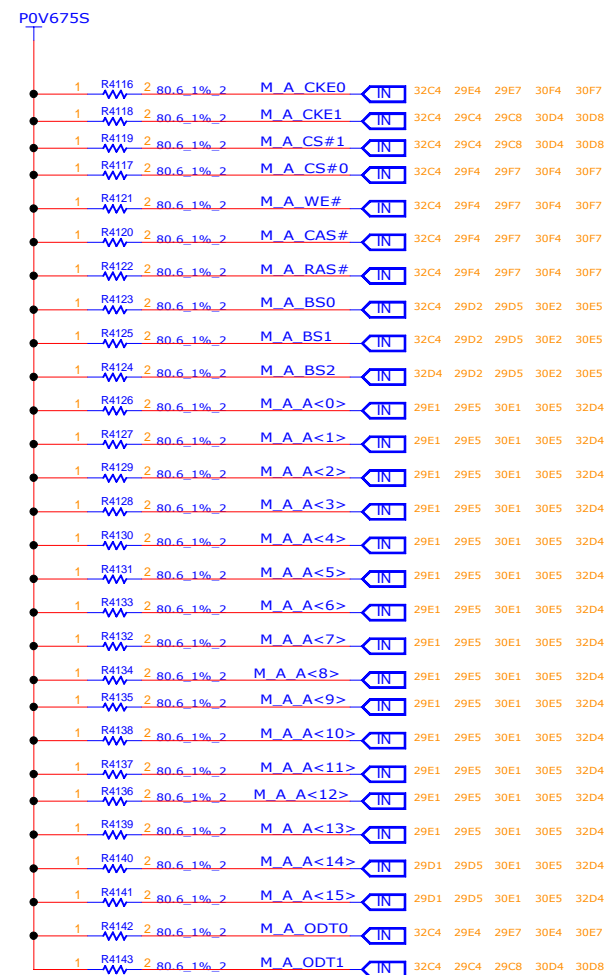
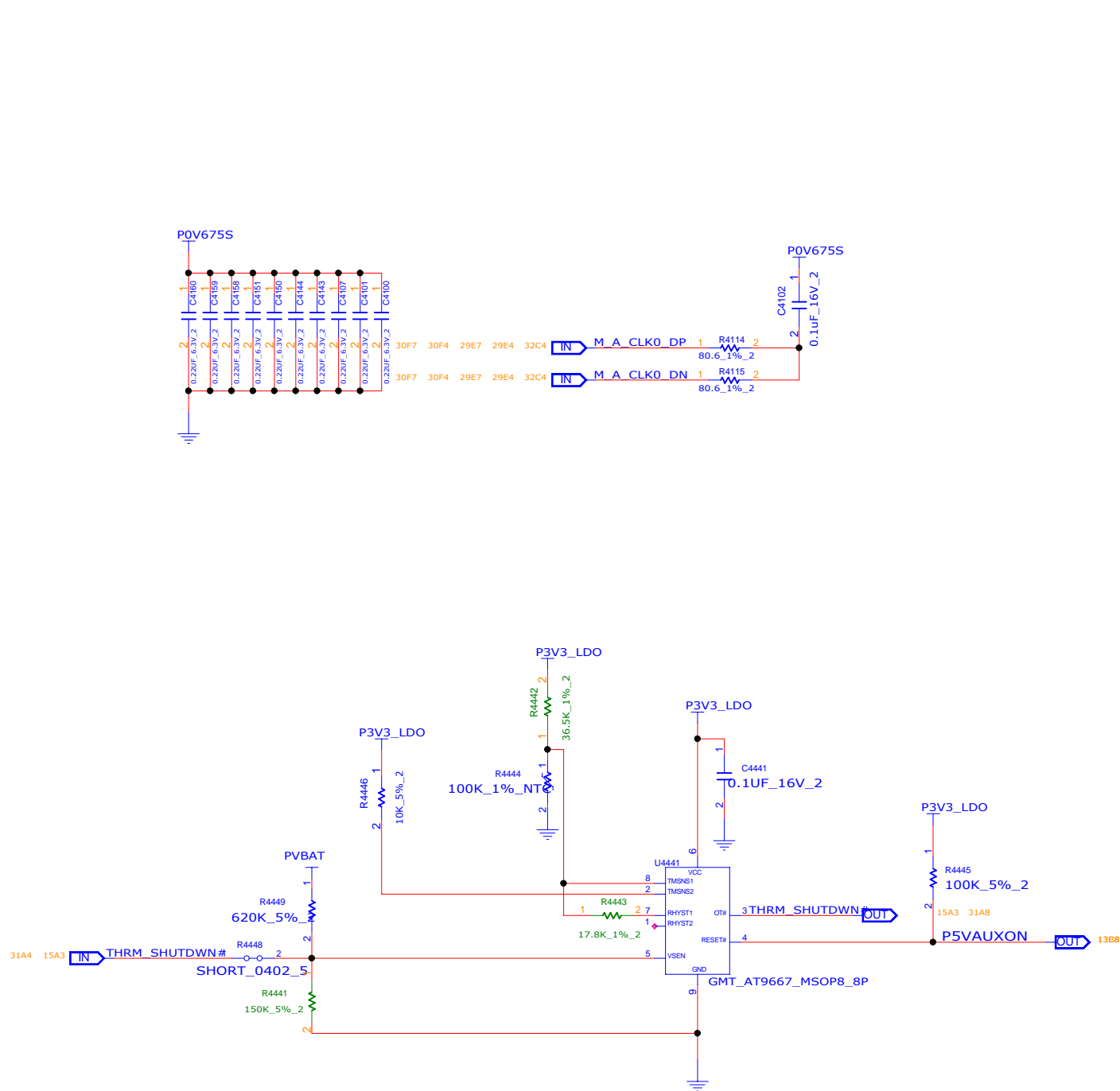
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4

3

2

1



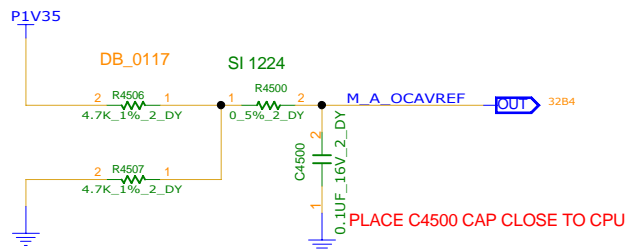
INVENTEC

TITLE
MODEL, PROJECT, FUNCTION
Block DiagramSIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01
SHEET of 31 48CHANGE by XXXXX> DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AWER>

D

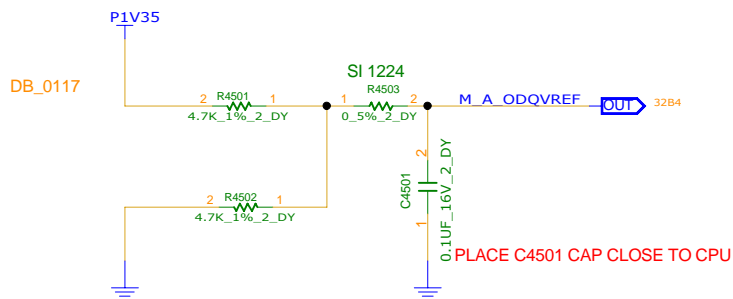
NOTE

PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES



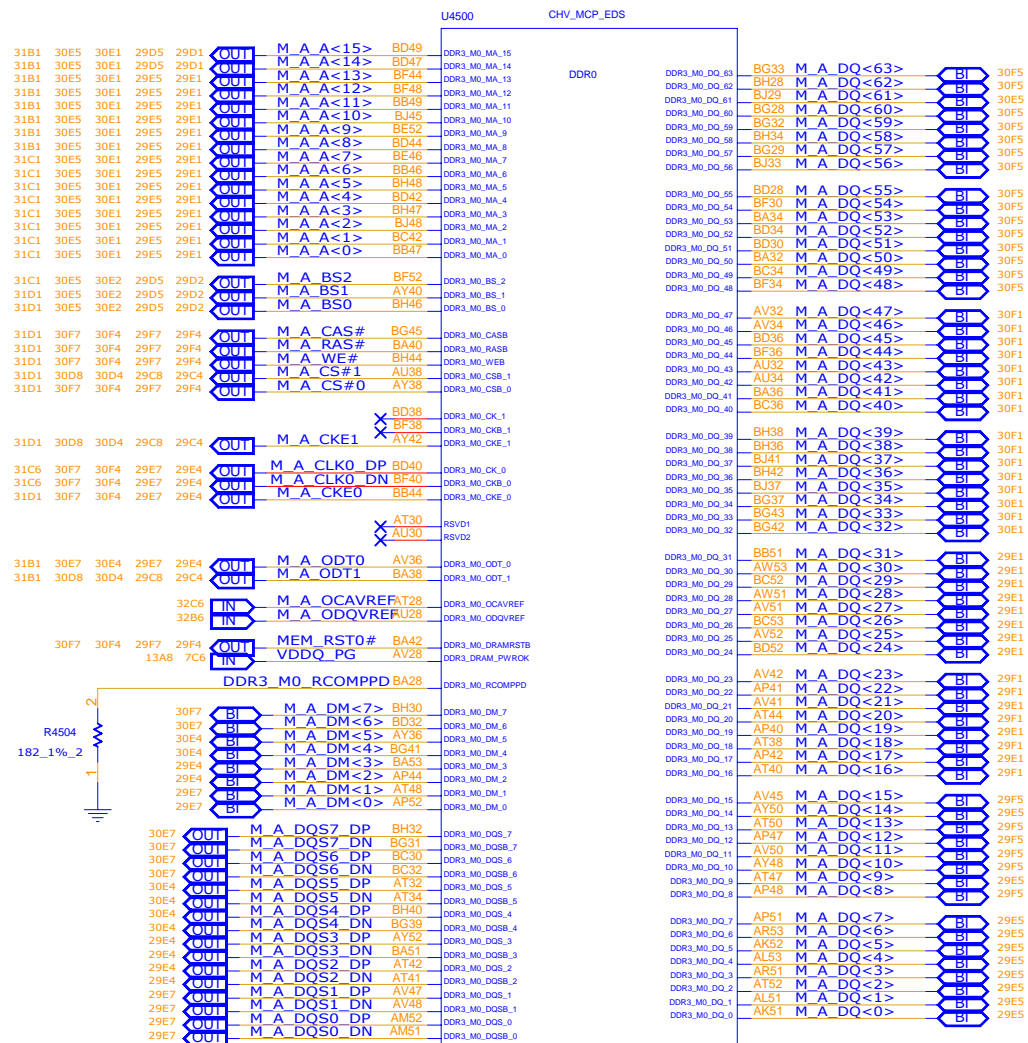
NOTE

PLACE TWO 4.7K RESISTORS CLOSE TO CPU PINS ON M_VREF
ROUTE THE VREF POWER SIGNALS WITH THICK TRACES



B

A



CPU_540276_BGA_1170P

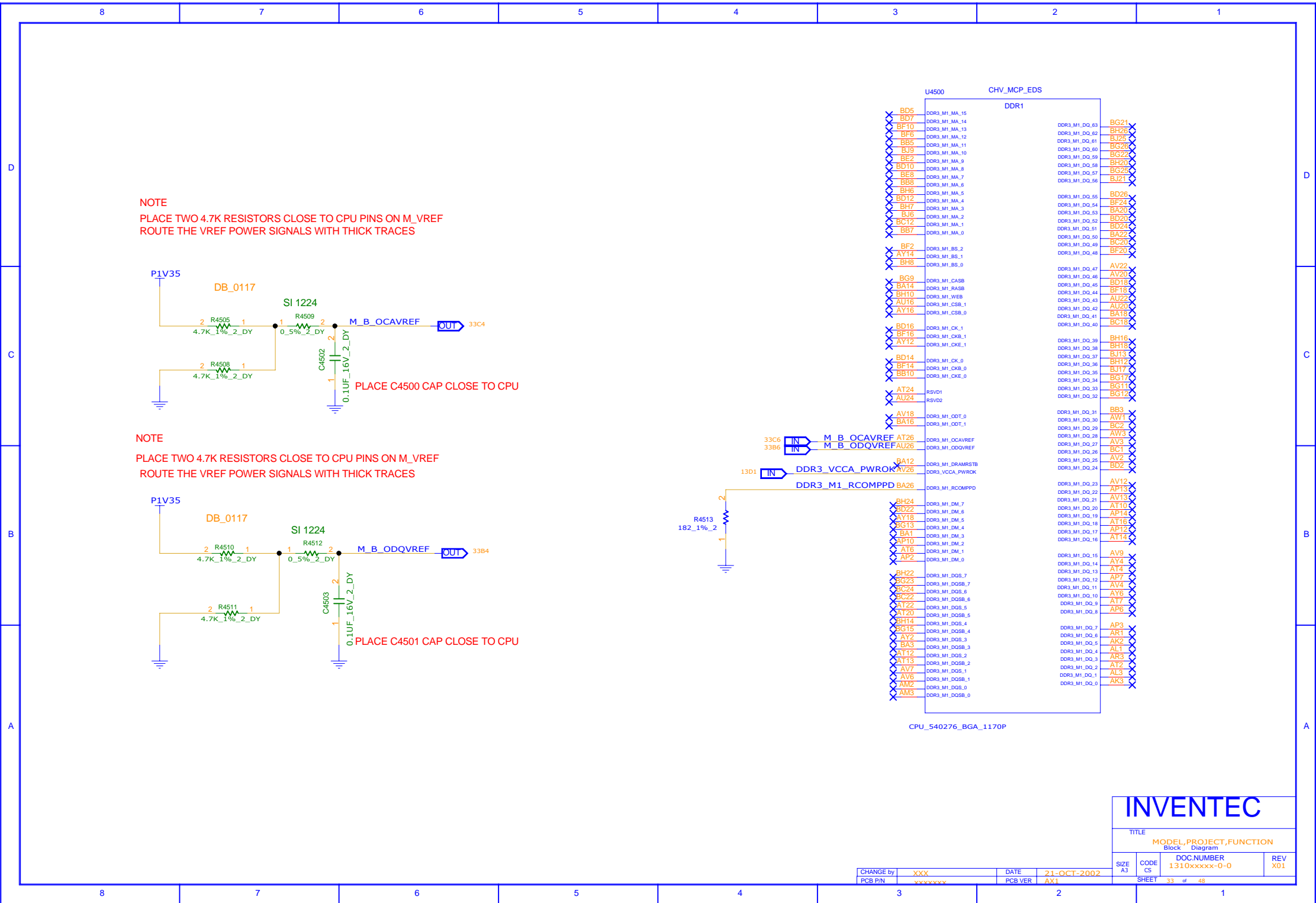
TMP131223010

INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3	CODE CS	DOCNUMBER 1310xxxxx-0-0	REV X01
SHEET	32	of 48	

CHANGE by PCB P/N	DATE PCB VER	21-OCT-2002 AX1
XXX xxxxxxx		



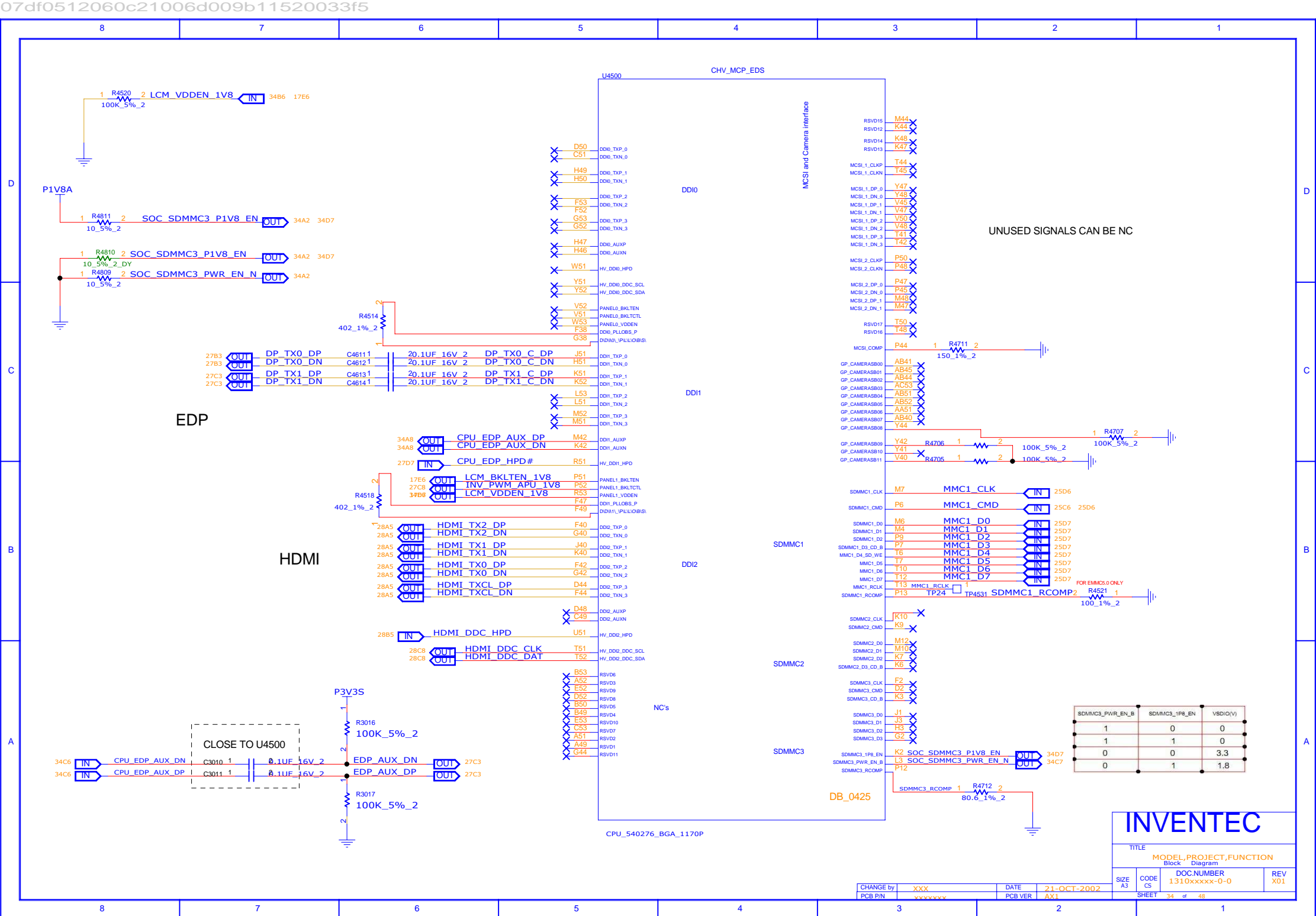
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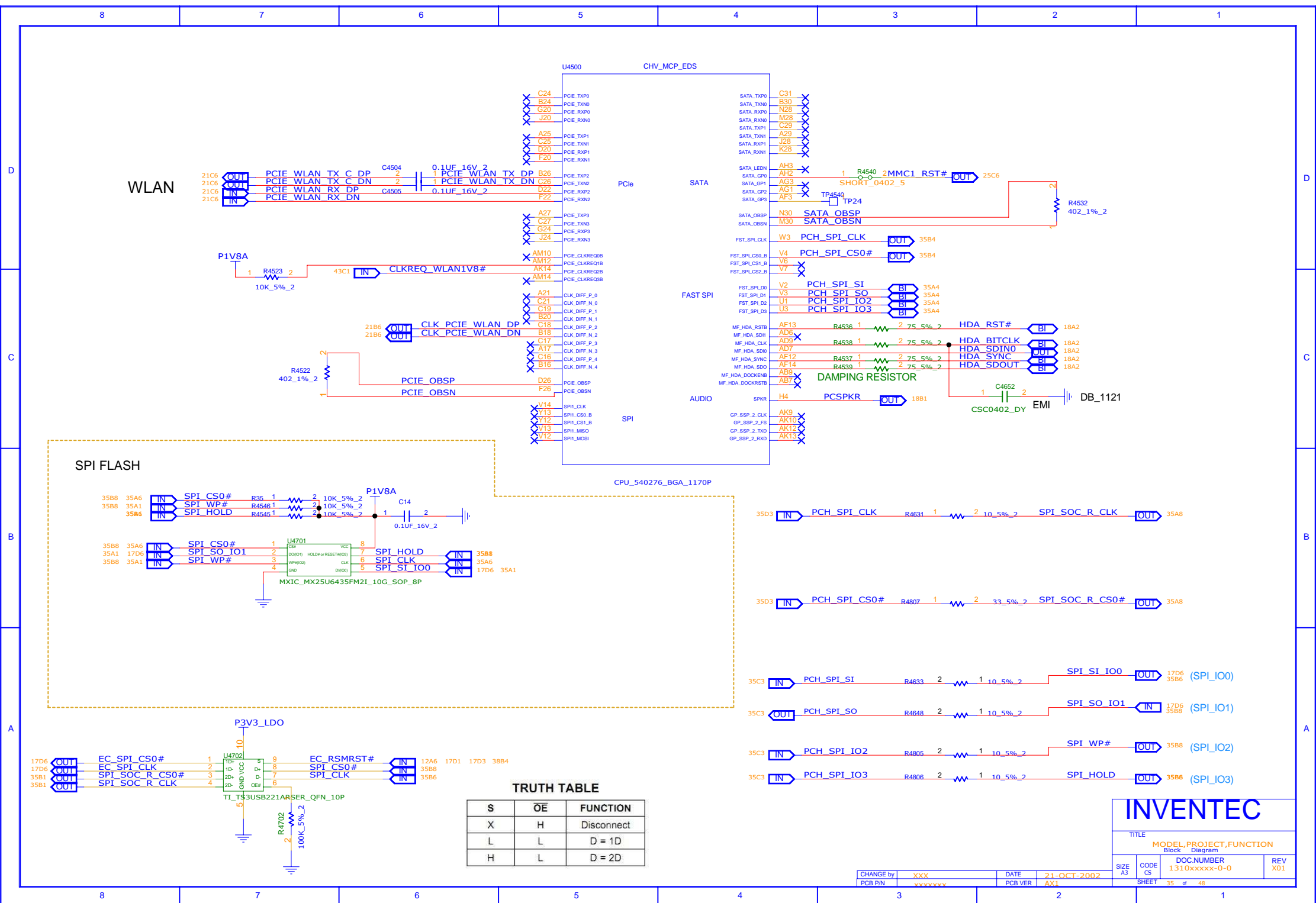
TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE A3 CODE CS DOC NUMBER 1310xxxxx-0-0 REV X01

CHANGE by XXX DATE 21-OCT-2002
PCB P/N xxxxxxxx PCB VER AX1

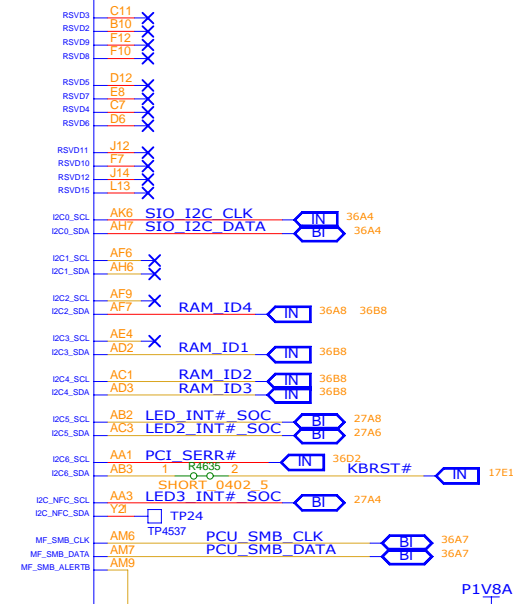
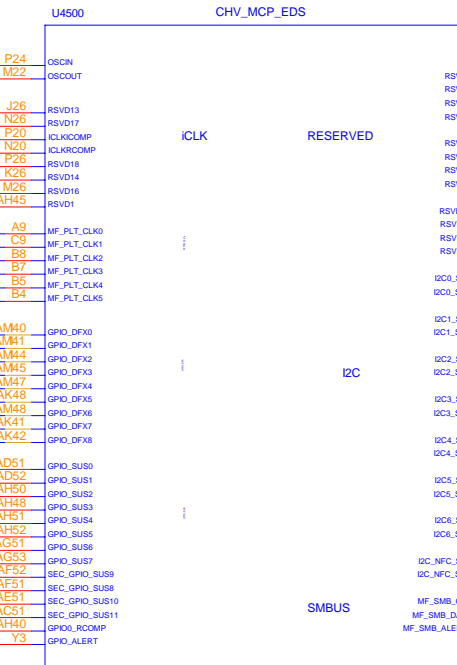
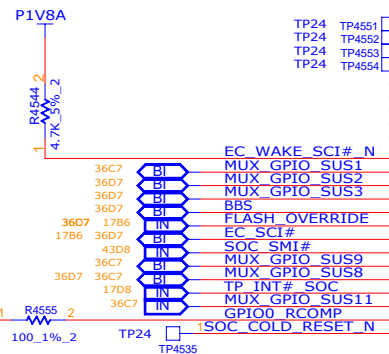
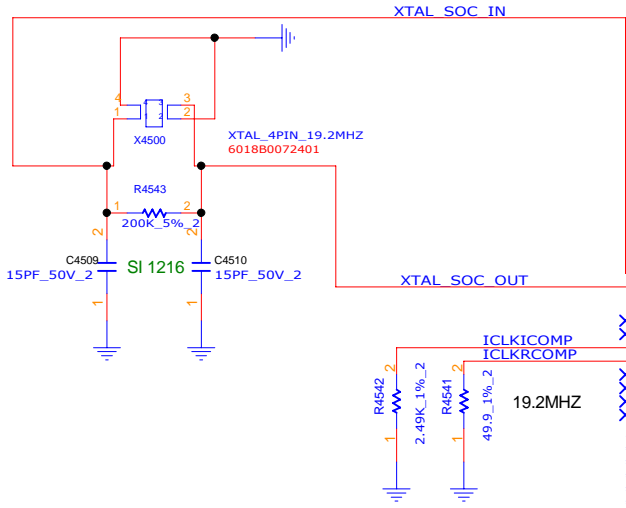
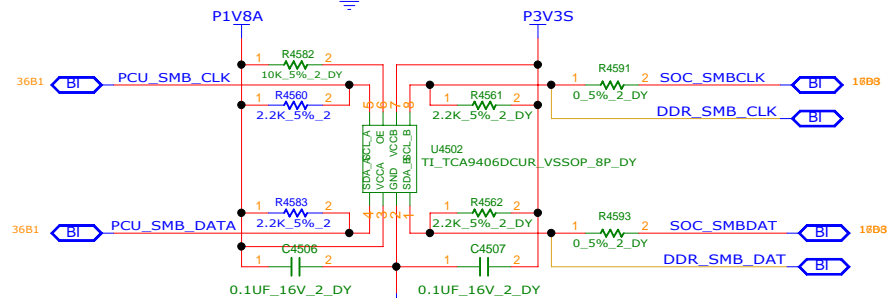
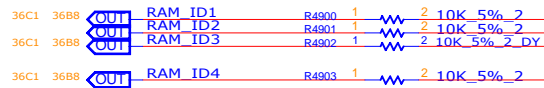
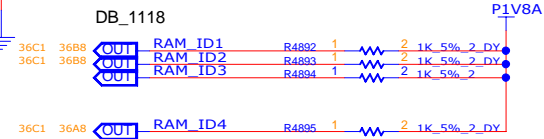
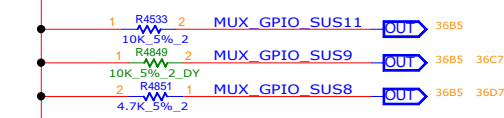
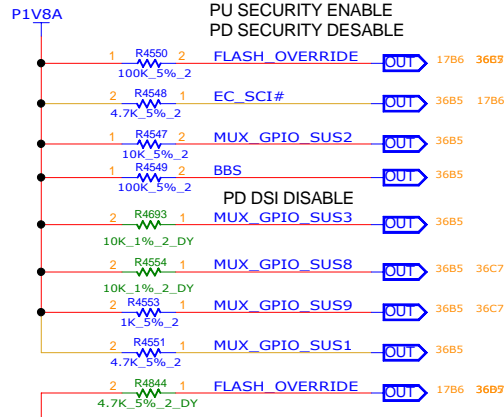
SHEET 33 of 48



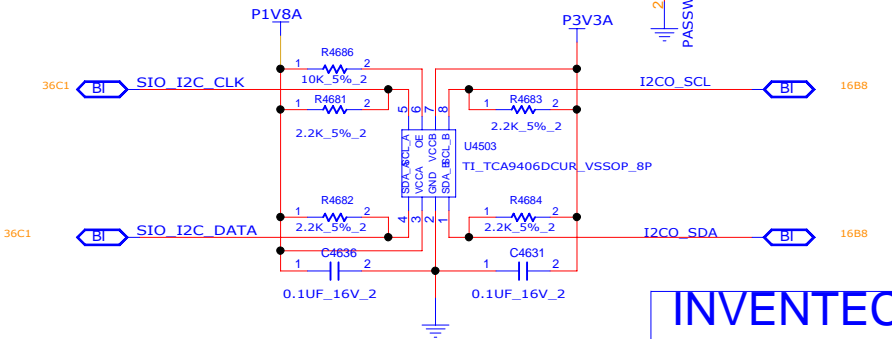


STRAP PIN

PLEASE REFERENCE TO CRB PAGE23

PU SECURITY ENABLE
PD SECURITY DESABLE

CPU_540276_BGA_1170P



INVENTEC

TITLE

MODEL,PROJECT,FUNCTION

DOC NUMBER

1310xxxxx-0-0

REV X01

CHANGE by XXX
PCB P/N <ENG>

DATE

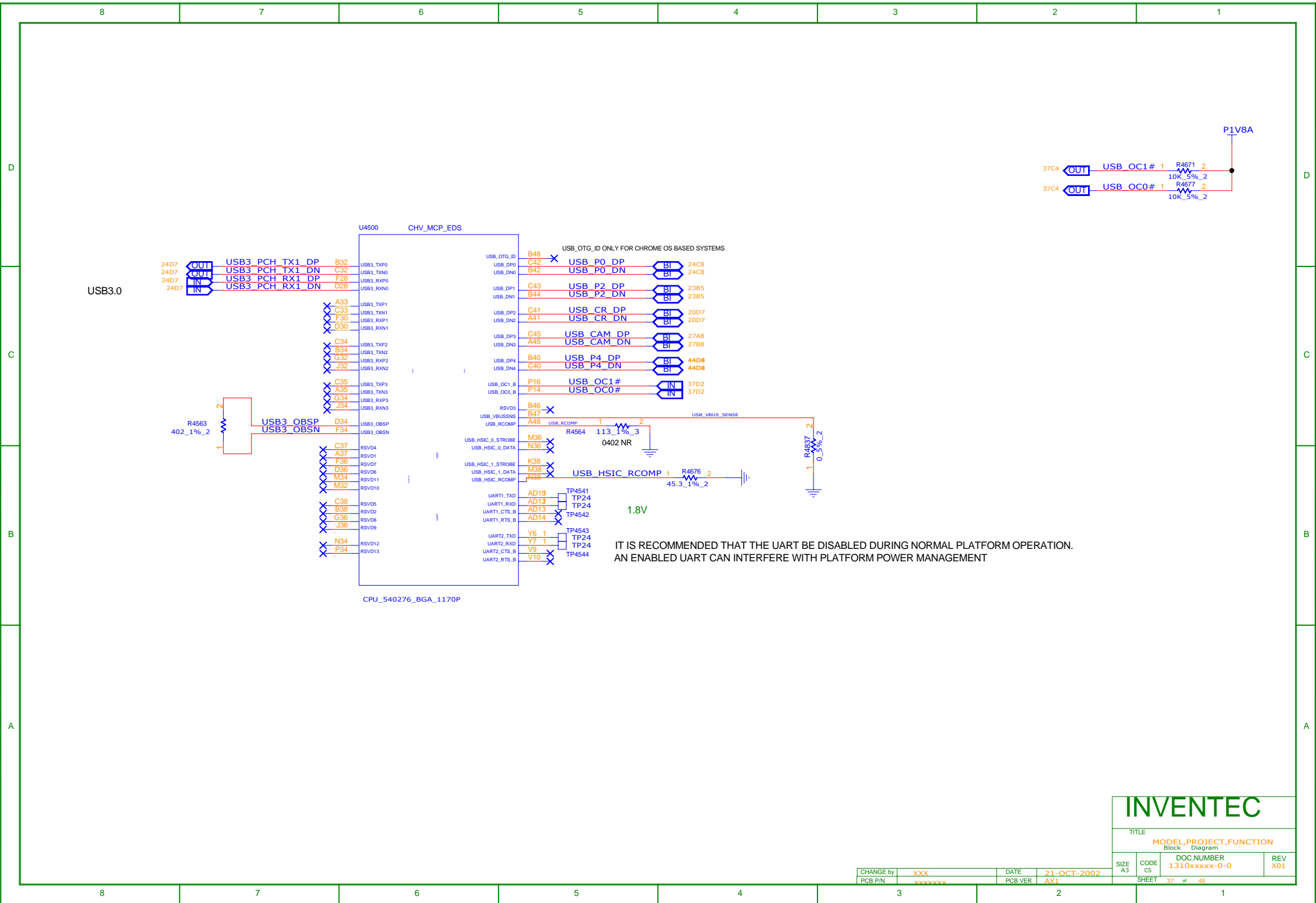
PCB VER

A71-OCT-2002

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INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE A3	CODE CS	DOC NUMBER 1310xxxxx-0-0	REV X01
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CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

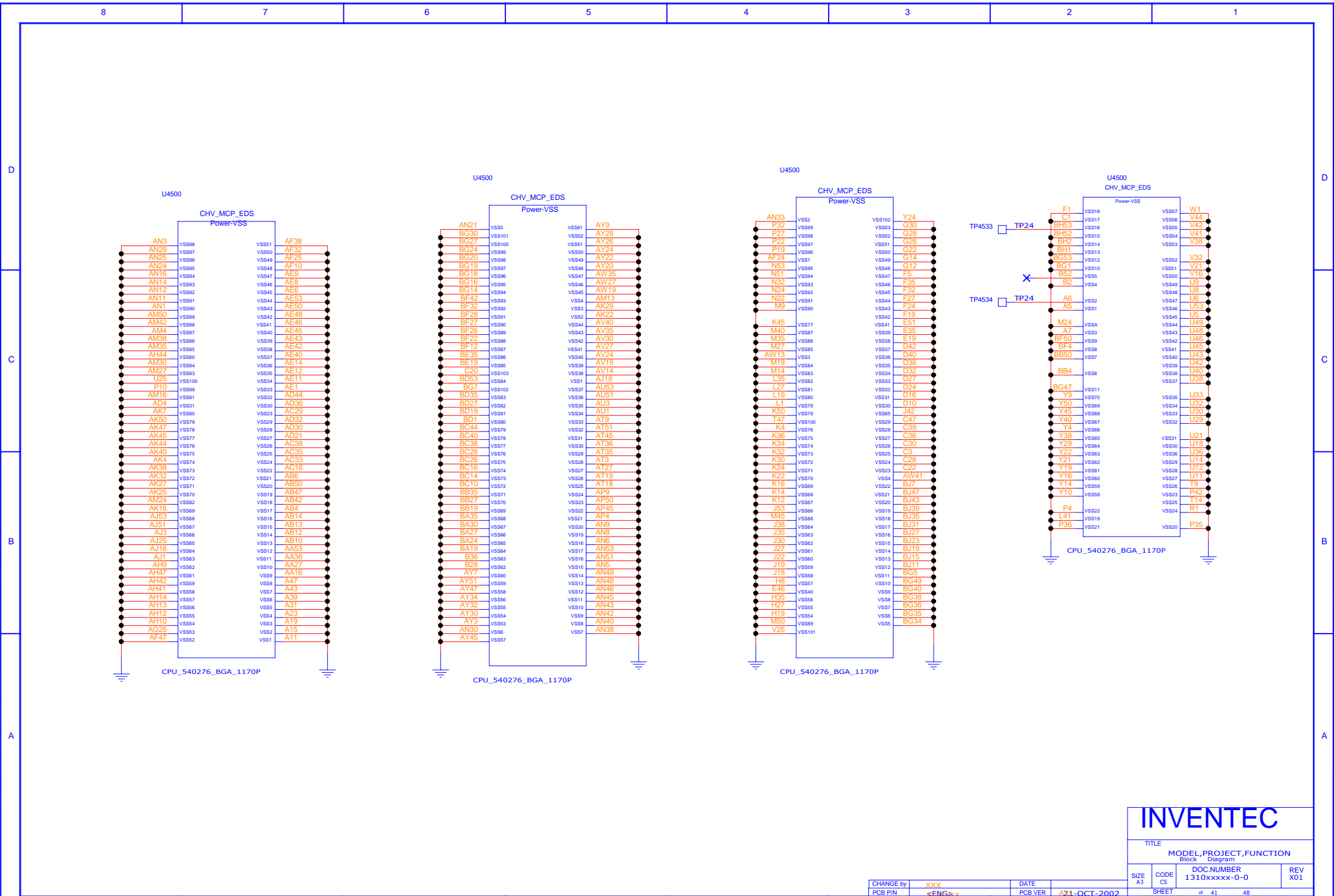


CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1

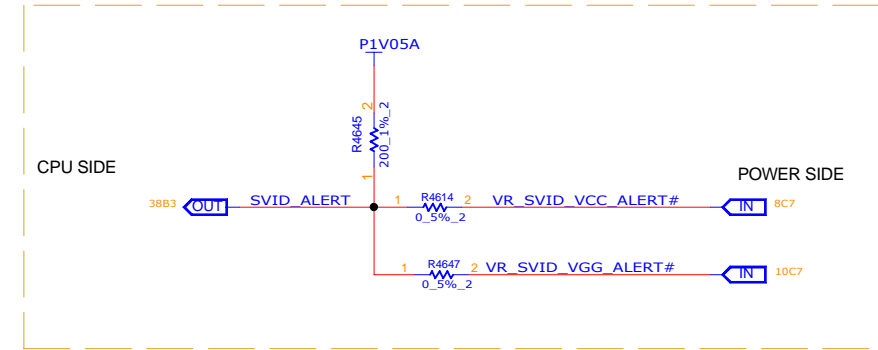
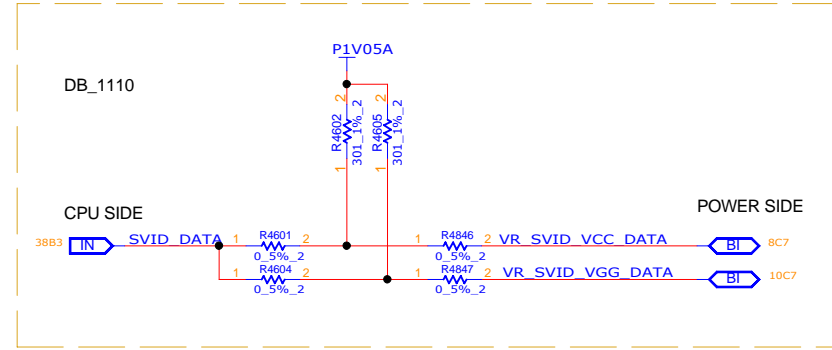
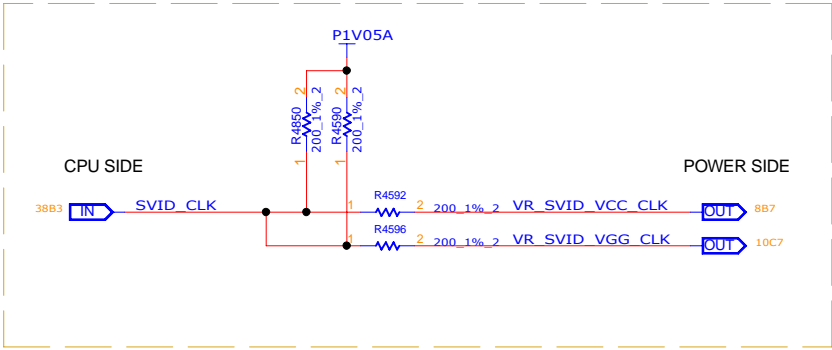




CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



SVID



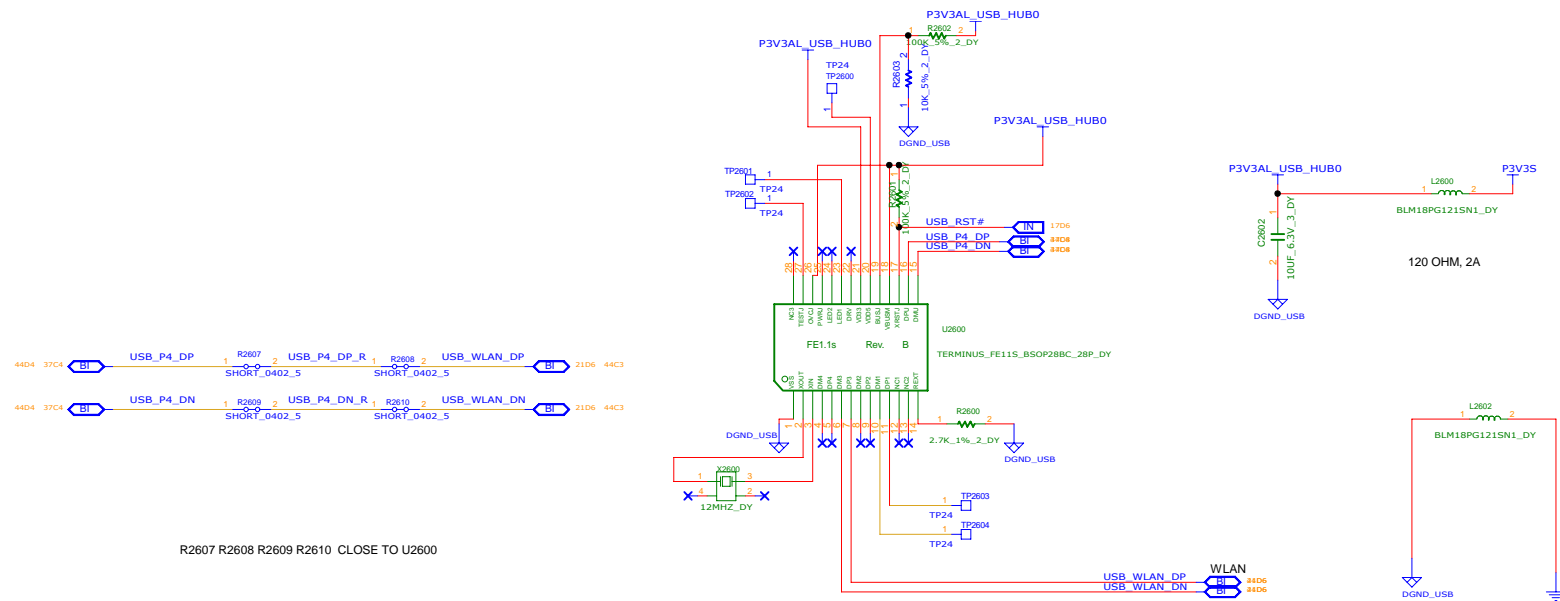
INVENTEC

TITLE			
MODEL,PROJECT,FUNCTION			
Block Diagram			
SIZE	CODE	DOC NUMBER	REV
A3	CS	1310xxxxx-0-0	X01
SHEET		42 of 48	

CHANGE by	XXX	DATE	21-OCT-2002
PCB P/N	xxxxxxx	PCB VER	AX1



USB2.0 HUB

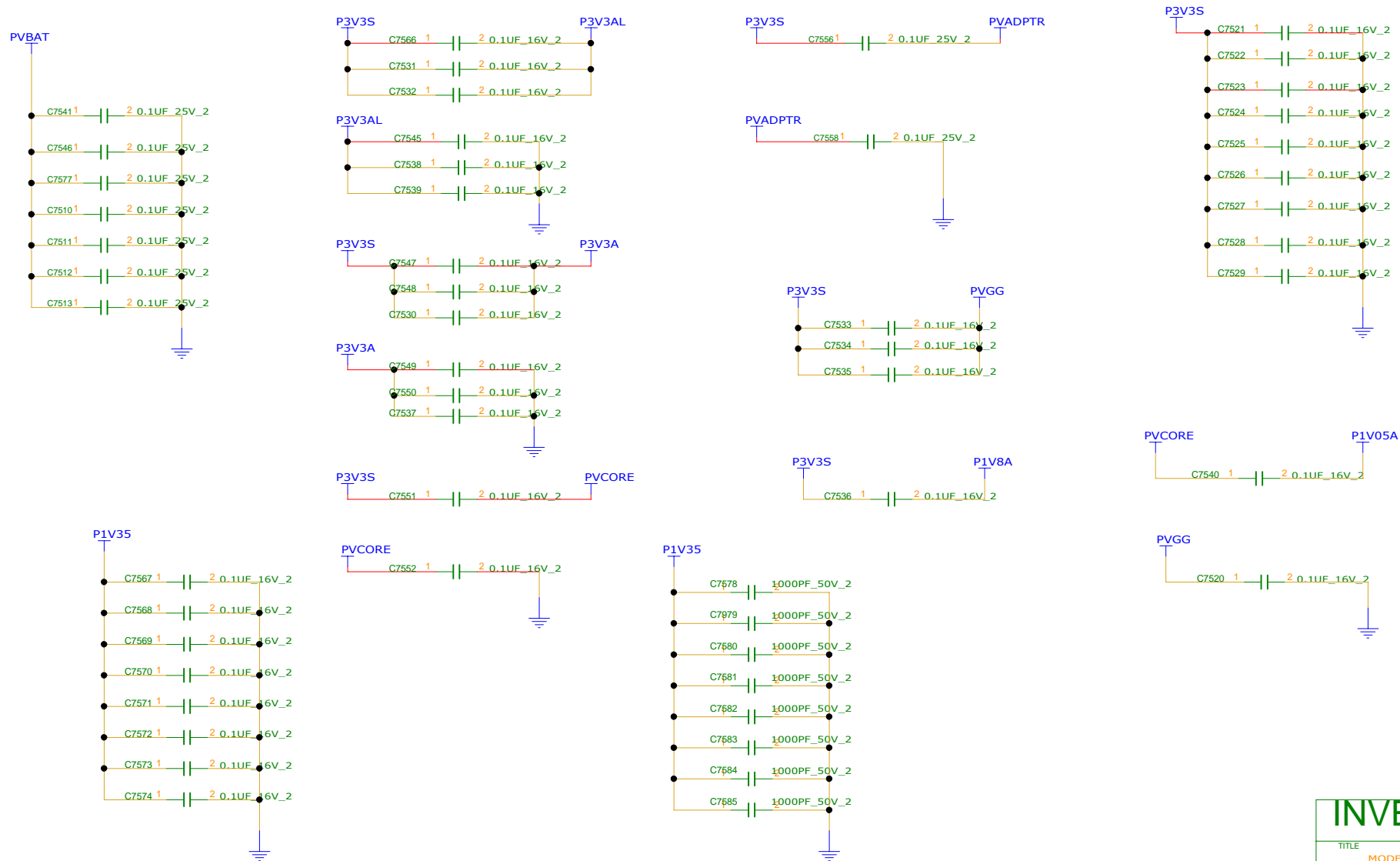


INVENTEC

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MODEL,PROJECT,FUNCTION			
Block Diagram			
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CHANGES	DATE	PCB VER
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EMI



INVENTEC

TITLE
MODEL,PROJECT,FUNCTION
Block Diagram

SIZE
A3

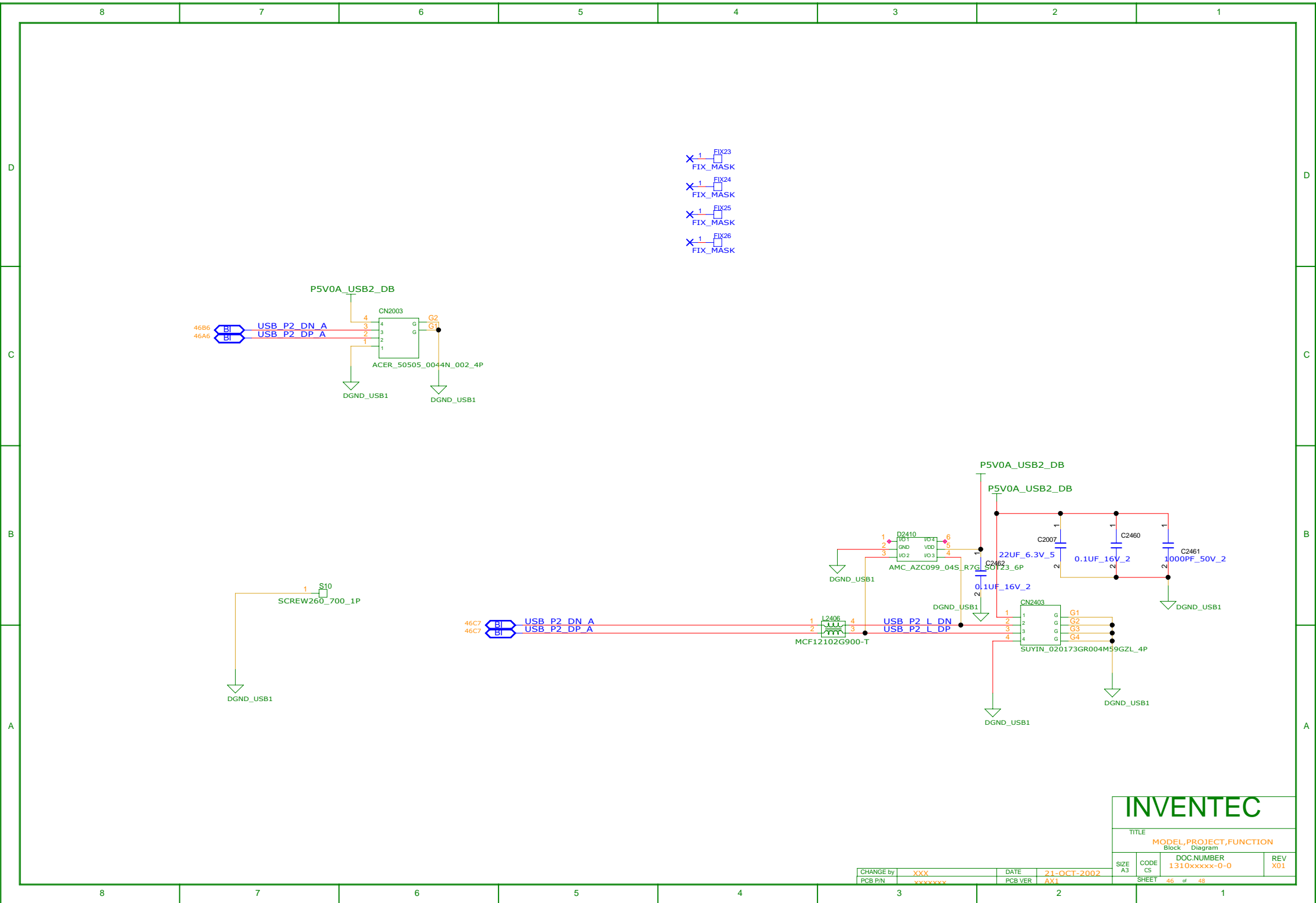
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SHEET
45 of 48

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PCB P/N	xxxxxxx	PCB VER	AX1



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Block Diagram							
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