Team name:	Pakistan-1								
Design:	Spatial SD ADC								
Area estimate (mm^2):	<2								
Caravel or Caravan?	Caravan								
Total pins	21								
(counts number of "x" below)									
Pin #	**Description**	**I/O/Bidir/Sup**	**Pin Labels for Sub-CKT**	**Pin Name**	**User area supply**	**GPIO used as analog**	**GPIO used as digtal**	**Caravan analog**	**Caravan clamp**
1	Analog VSS	Sup	VDD	vssa1	x				
2	Analog VDD	Sup	VSS	vccd1	x				
3	Input bias current	I	IBIAS	io_analog (9)		x			
4	External Select signal	I	SEL	io_in (14)			x		
5	Test point	O	QOUT1	io_analog (5)		x			
6	Test point	O	VCOMP	io_out (13)			x		
7	Test point	O	VINTEG	io_analog (1)		x			
8	Ref. Voltage for DAC	I	VREFN	io_analog (6)		x			
9	Ref. Voltage for DAC	I	VREFP	io_analog (0)		x			
10	Ref. Voltage for ADC	I	VREF	io_analog (7)		x			
11	Input signal 1	I	SD_IN_1	io_analog (4)		x			
12	Input signal 2	I	SD_IN_2	io_analog (2)		x			
13	Input signal 3	I	SD_IN_3	io_analog(3)		x			
14	Input signal 4	I	SD_IN_4	io_analog(10)		x			
15	Input for VCO	I	VC	io_analog(8)		x			
16	Clock	I	CLK_IN	io_in (19)			x		
17	Test point	O	VCO_CLK	io_out (17)			x		
18	Digital Out 1	O	SD_DOUT_1	io_out (15)			x		
19	Digital Out 2	O	SD_DOUT_2	io_out (18)			x		
20	Digital Out 3	O	SD_DOUT_3	io_out (16)			x		
21	Digital Out 4	O	SD_DOUT_4	io_out (12)			x		