

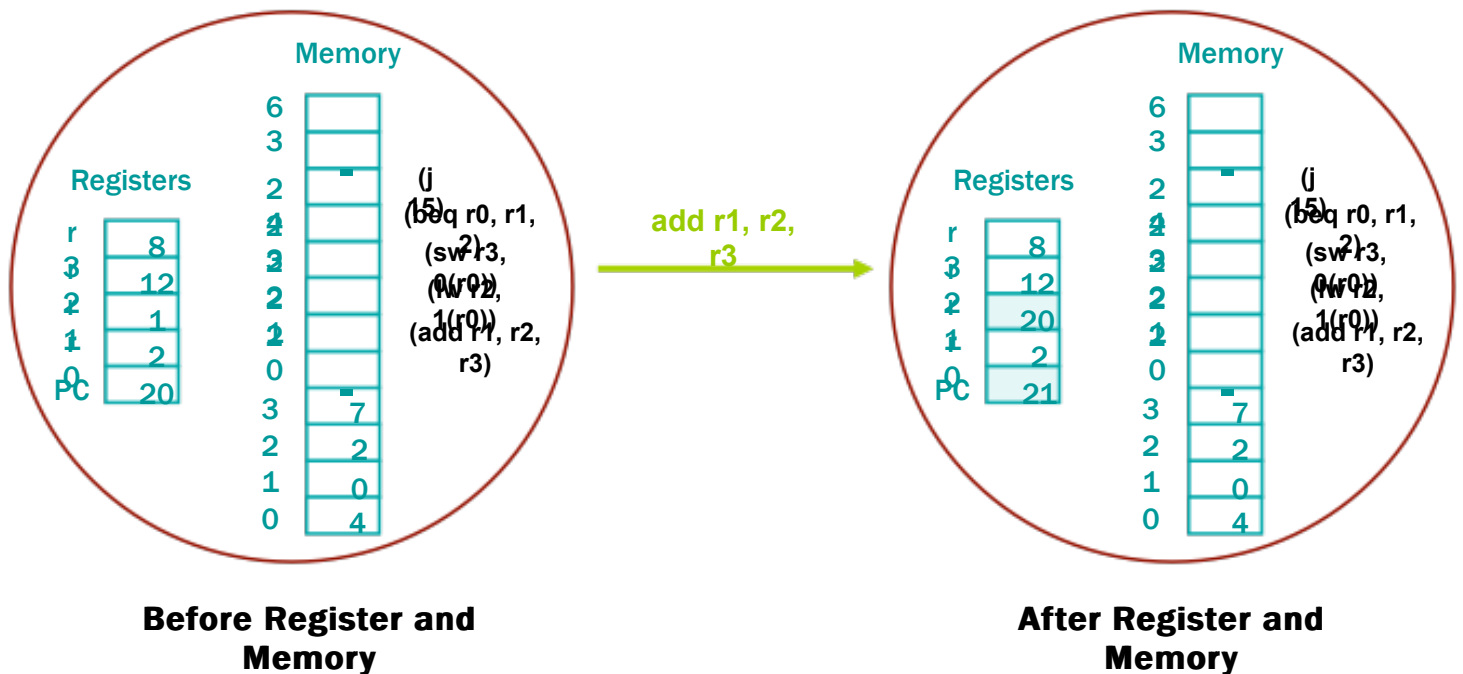
# ISA

CSE141L  
Fall 2016

# Instruction Set Architecture as an ADT

## Assumptions

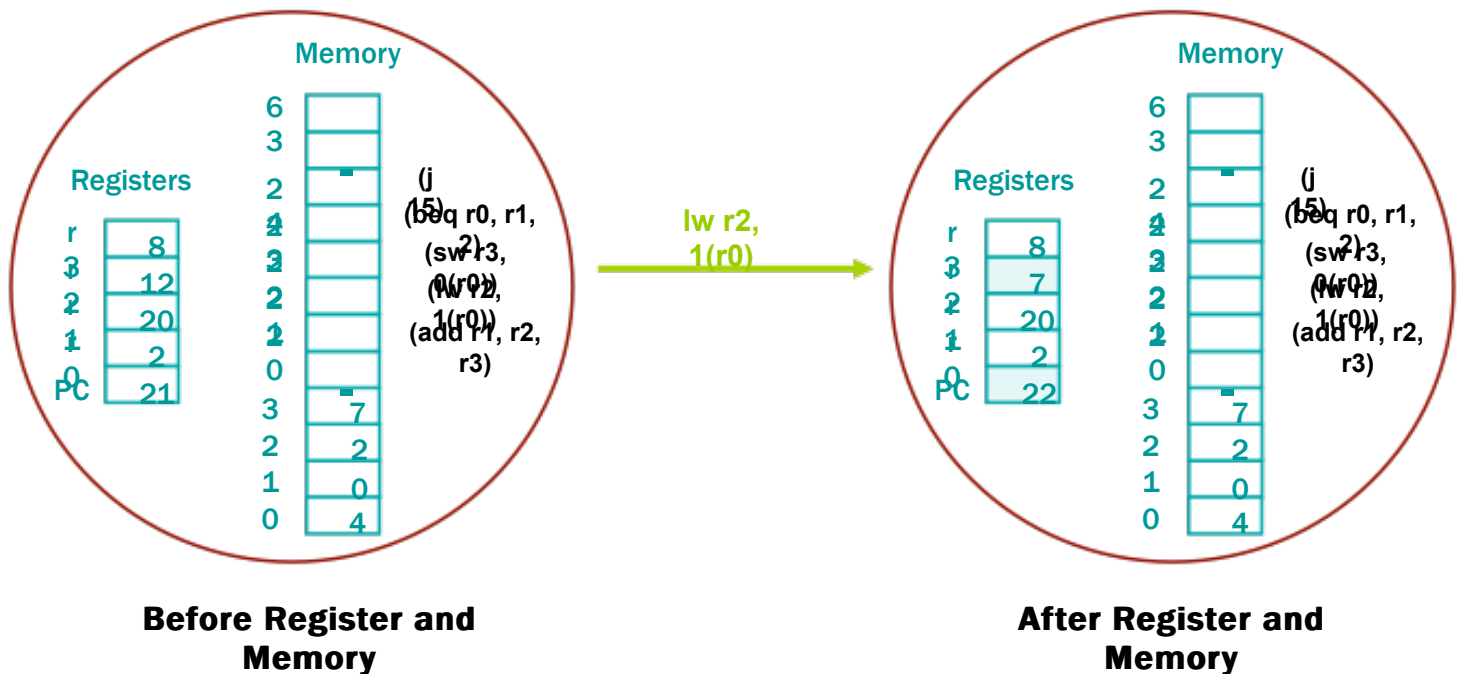
- 8 bit ISA
- # of registers = 4 + PC (Program Counter)
- Memory size = 64B



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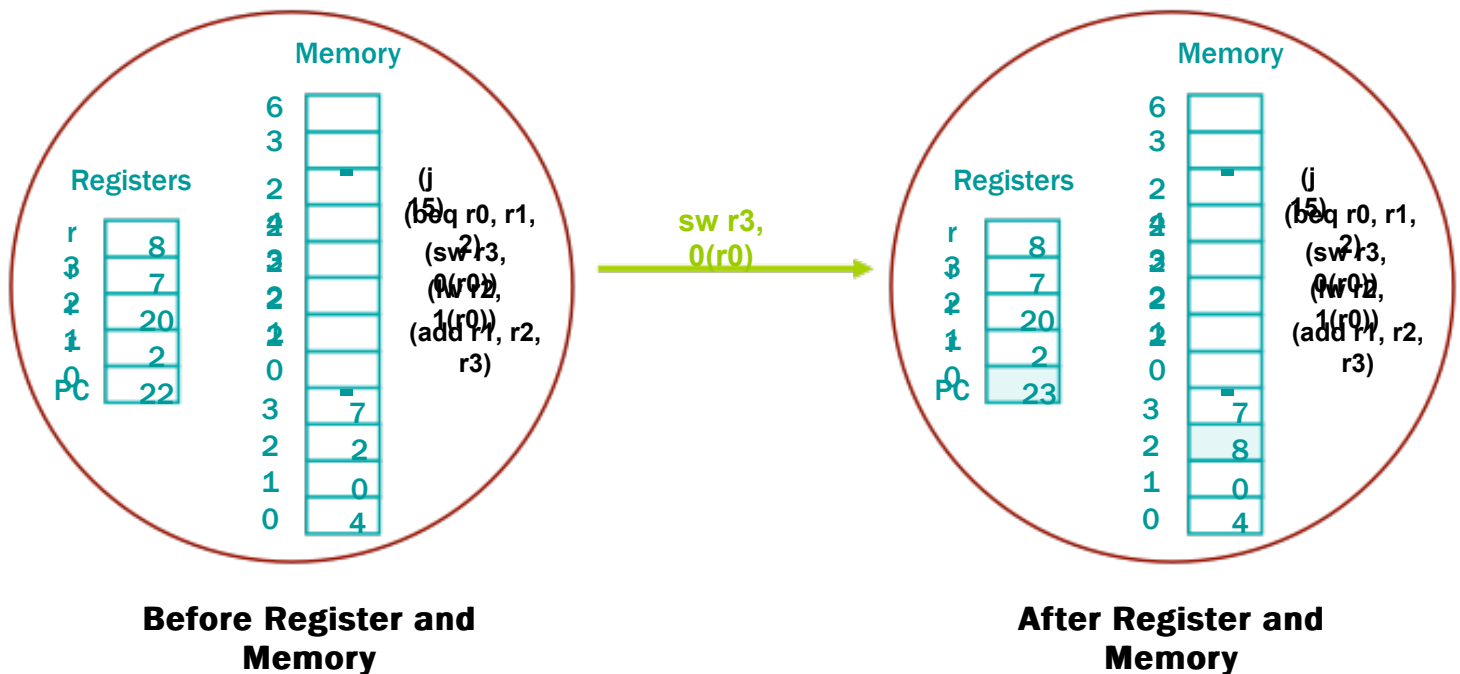
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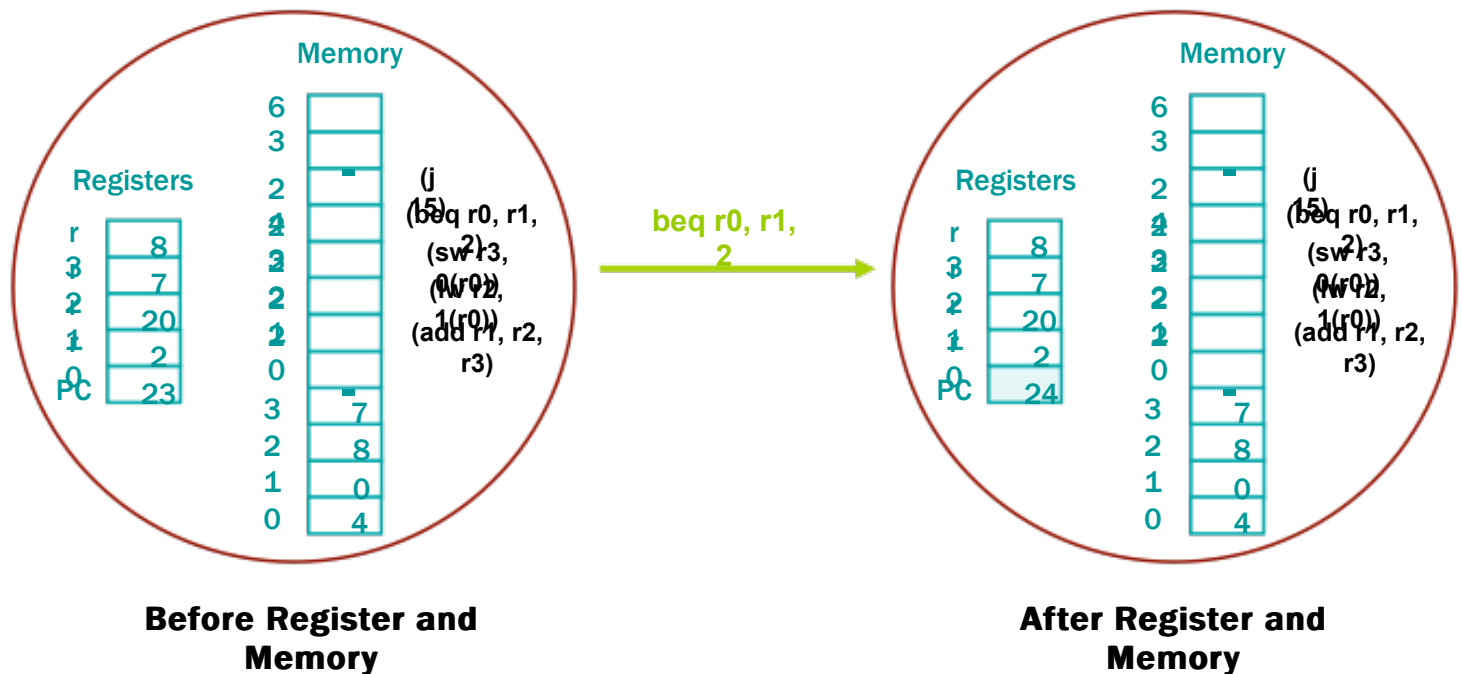
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