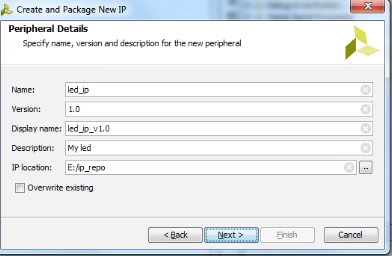
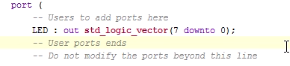
# Labo 3

# Create and Package New IP

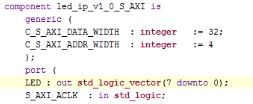


# Declare signals in the top level

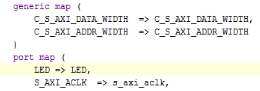


# Propogate signals down

## Figure 6



## Figure 7



# Waartoe dienen 2-1-15 en 2-1-16

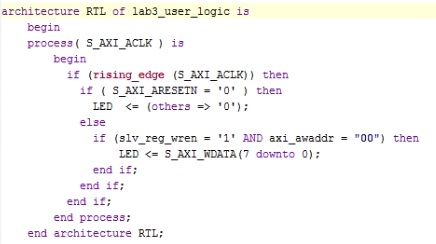
## 2-1-15

Hier wordt de declaratie van de poorten bij de component lab3\_user\_logic geplaatst.

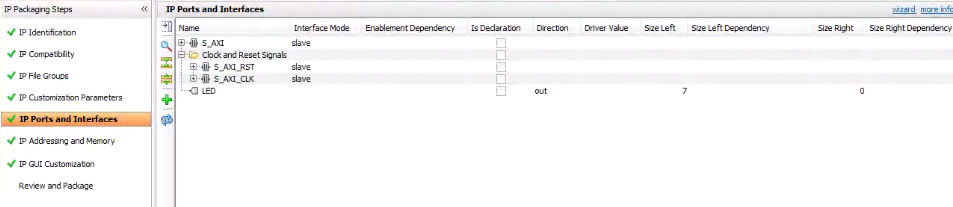
## 2-1-16

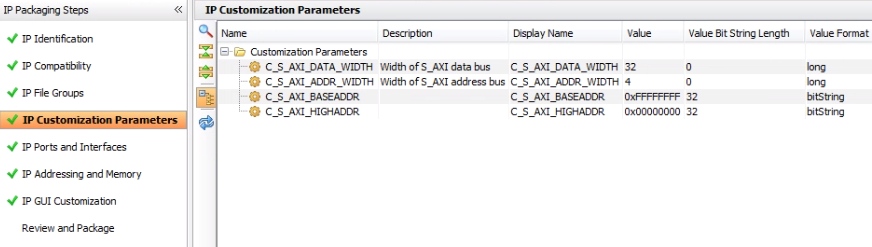
Hier worden de poorten die ervoor gedeclareerd zijn in 2-1-15 gelinkt aan de ingangen van het IP-blokje die we zelf gaan maken.

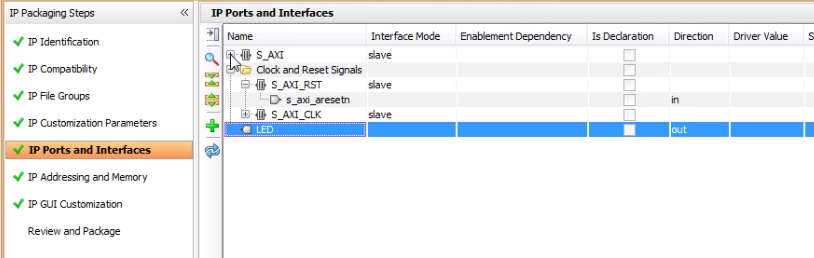
# 2-1-20



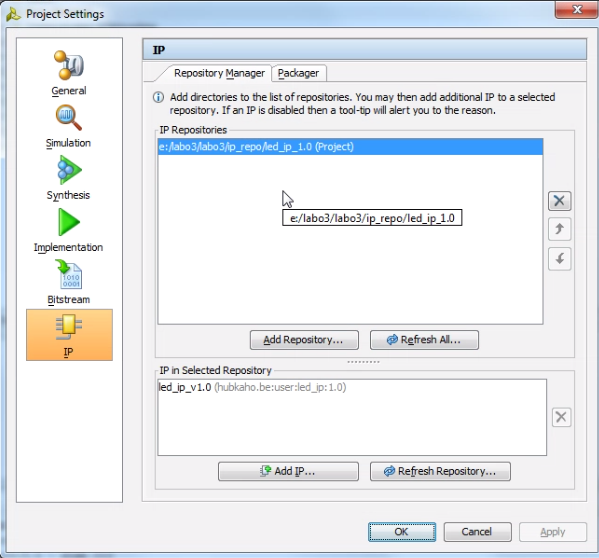
# Add the user-file to the IP project



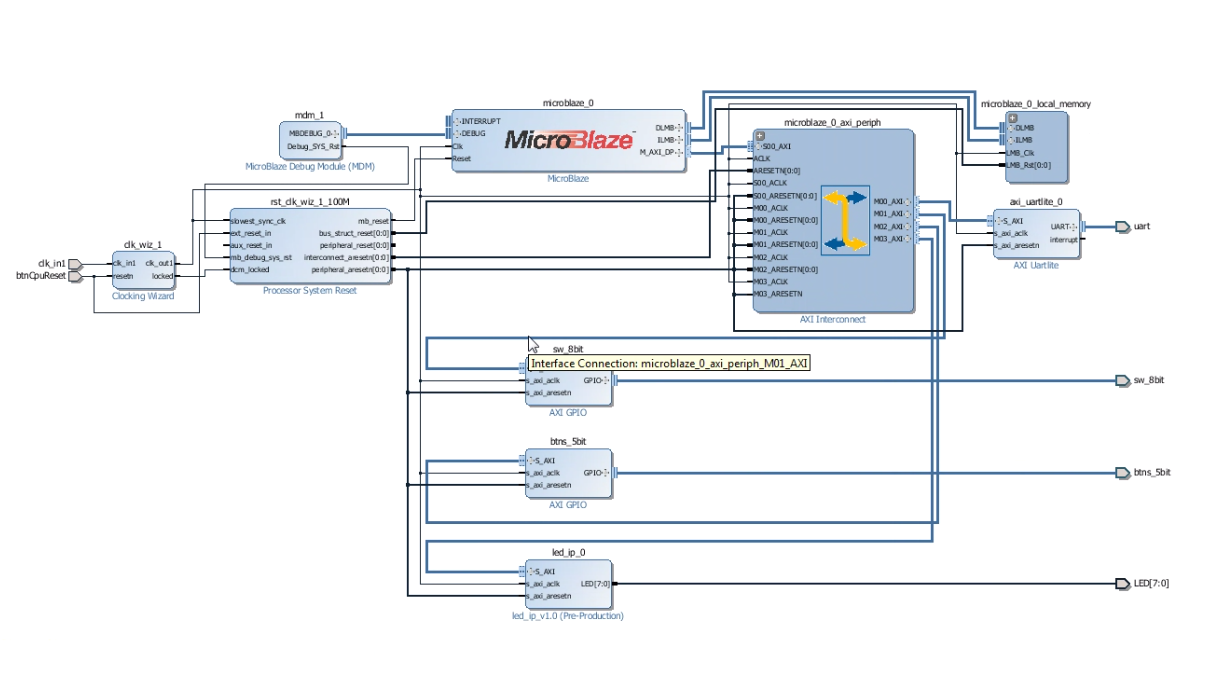




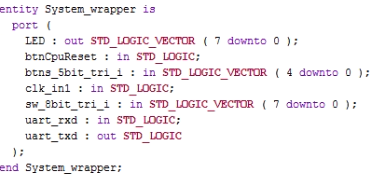
# 3-1-5



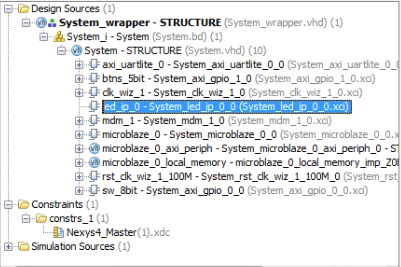
# LED external port added and connected



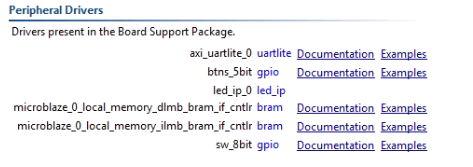
# LED-port included



# System structure



# Accessing device driver documentation



# Figure 24: completed c-file

