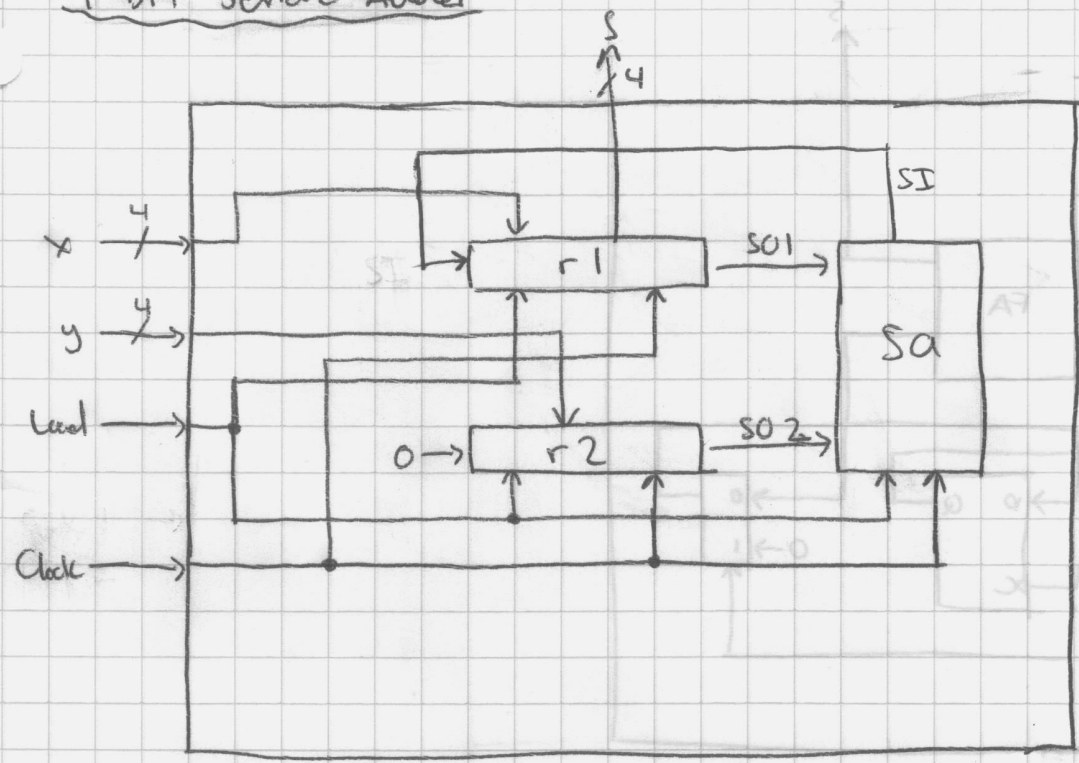
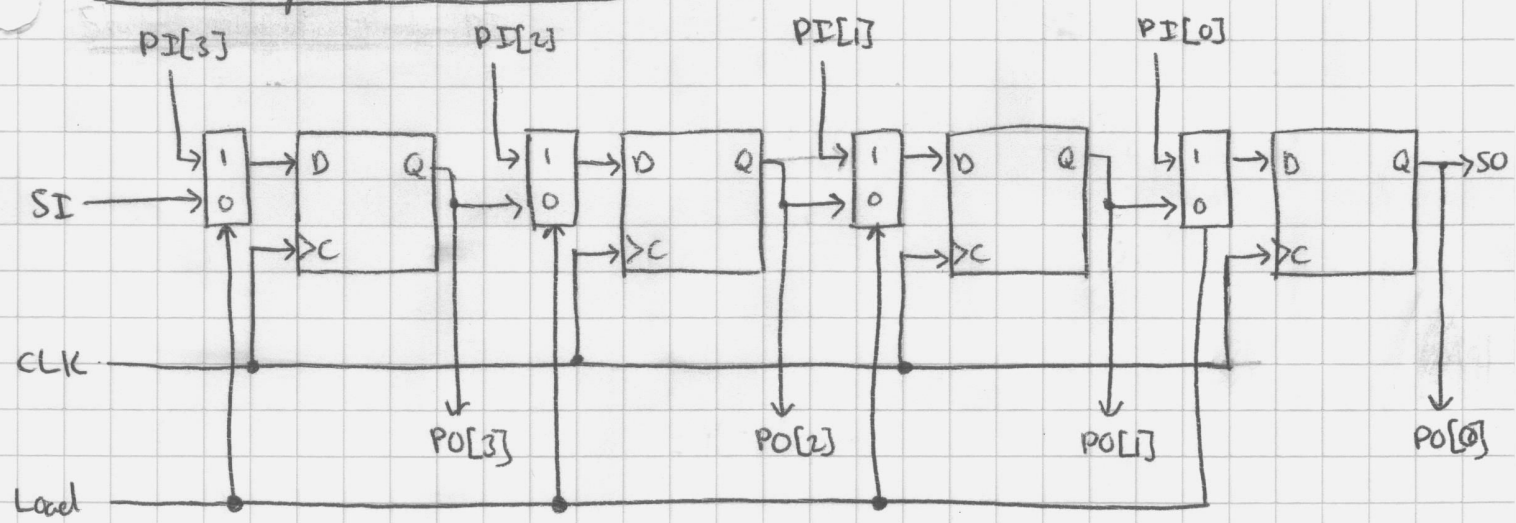


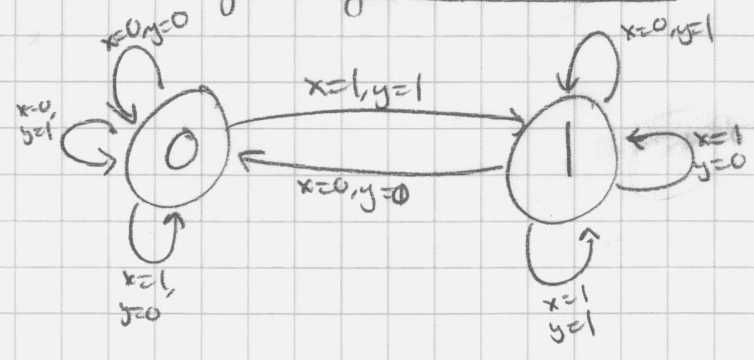
### 4-bit Serial Adder



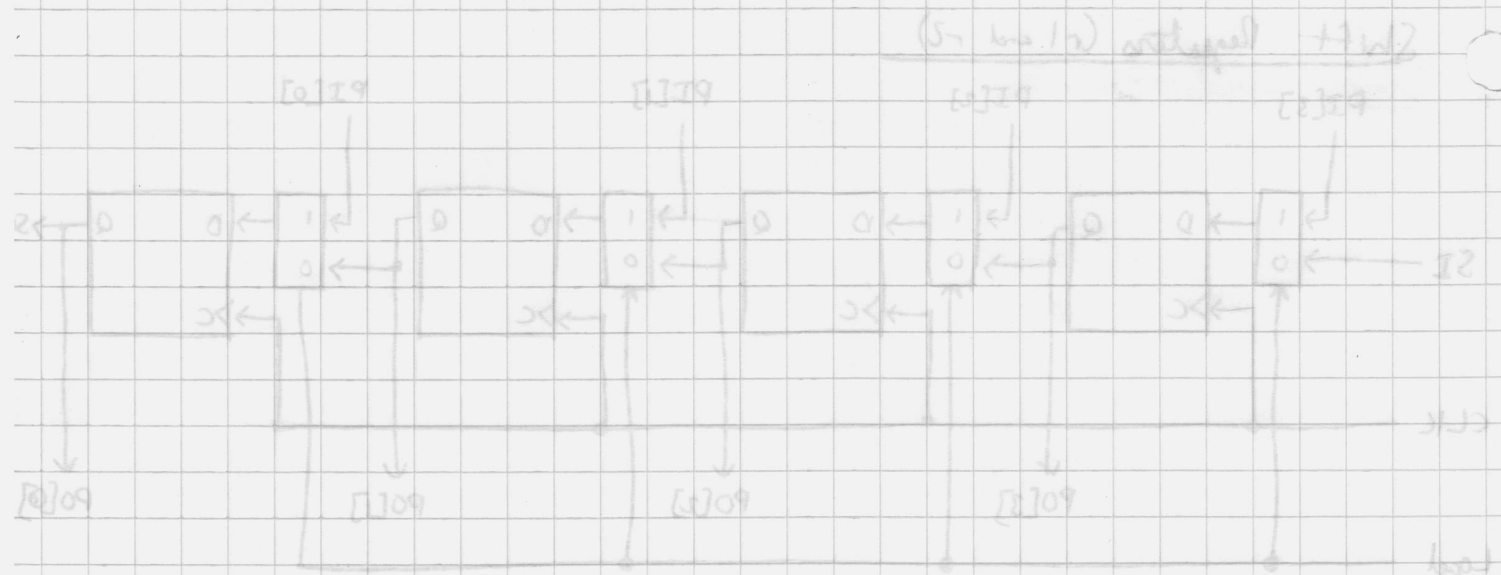
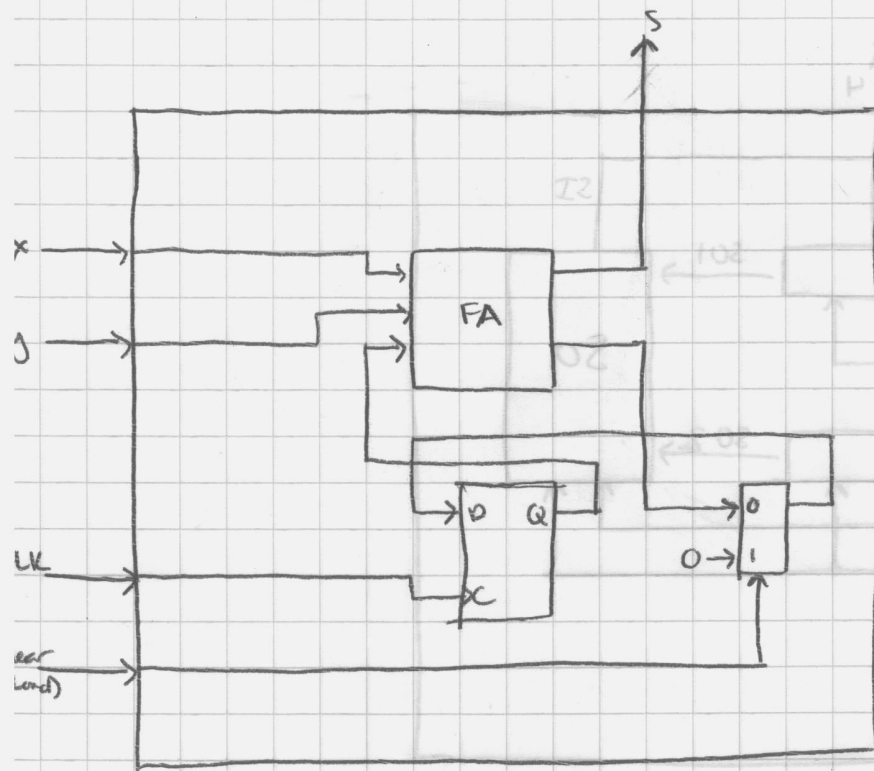
### Shift Registers ( $r1$ and $r2$ )



### State Diagram of Serial Adder:



## Serial Adder Module



State Diagram of Serial Adder:

