

DRAFT

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DESIGN CONSIDERATIONS

DESIGN NOTE:  
Example text for informational  
design notes .

DESIGN NOTE:  
Example text for critical  
design notes.

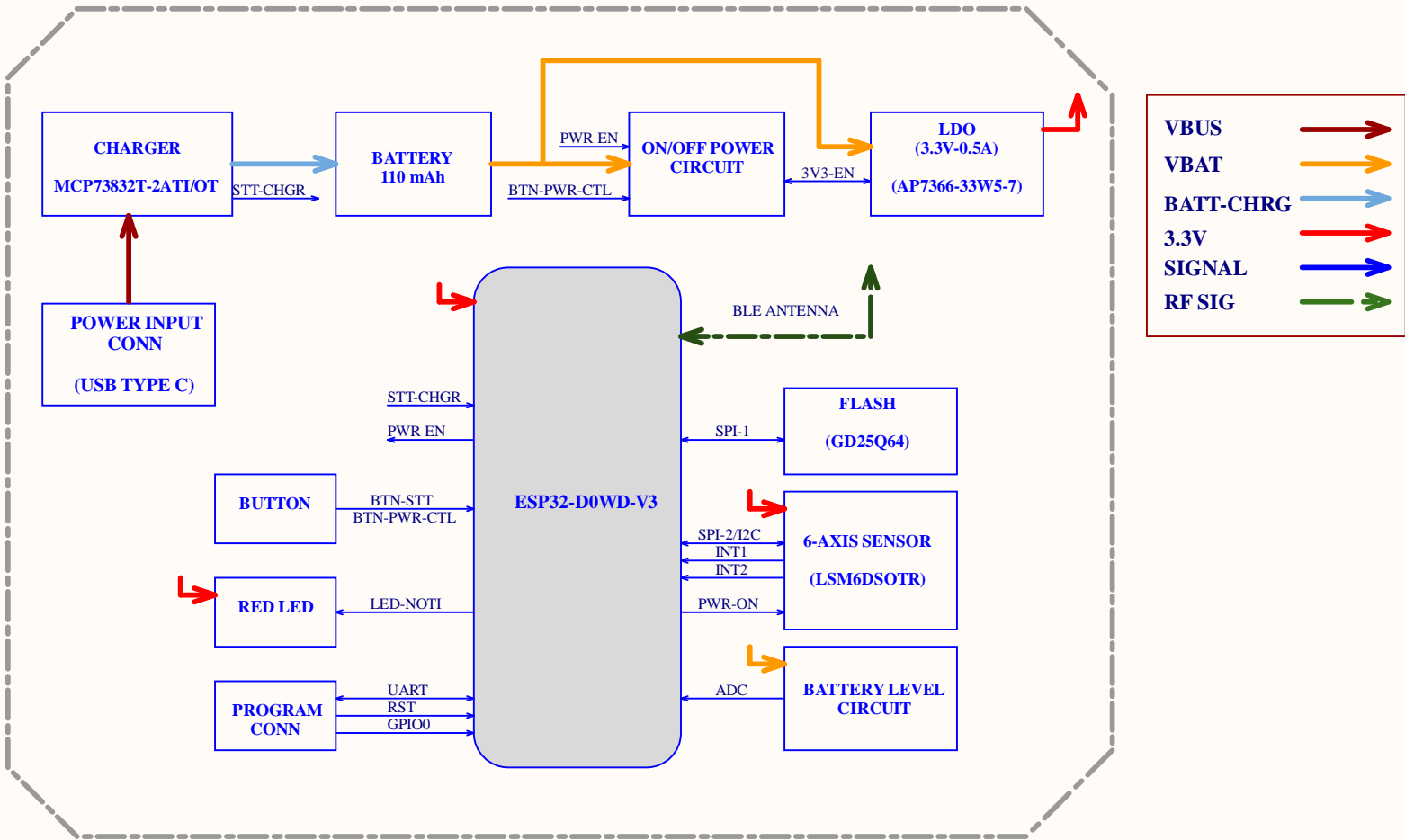
DESIGN NOTE:  
Example text for cautionary  
design notes.

LAYOUT NOTE:  
Example text for critical  
layout guidelines.

Title: [01] - COVER PAGE.SchDoc	ITR VN Corp.  Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam
Size: A4    Number: 1    Revision: v2.0	
Date: 8/26/2022    Time: 2:48:16 PM    Sheet 1 of 7	
File: [01] - COVER PAGE.SchDoc	

# TENA\_V2.0.PrjPcb

## (Block Diagram)



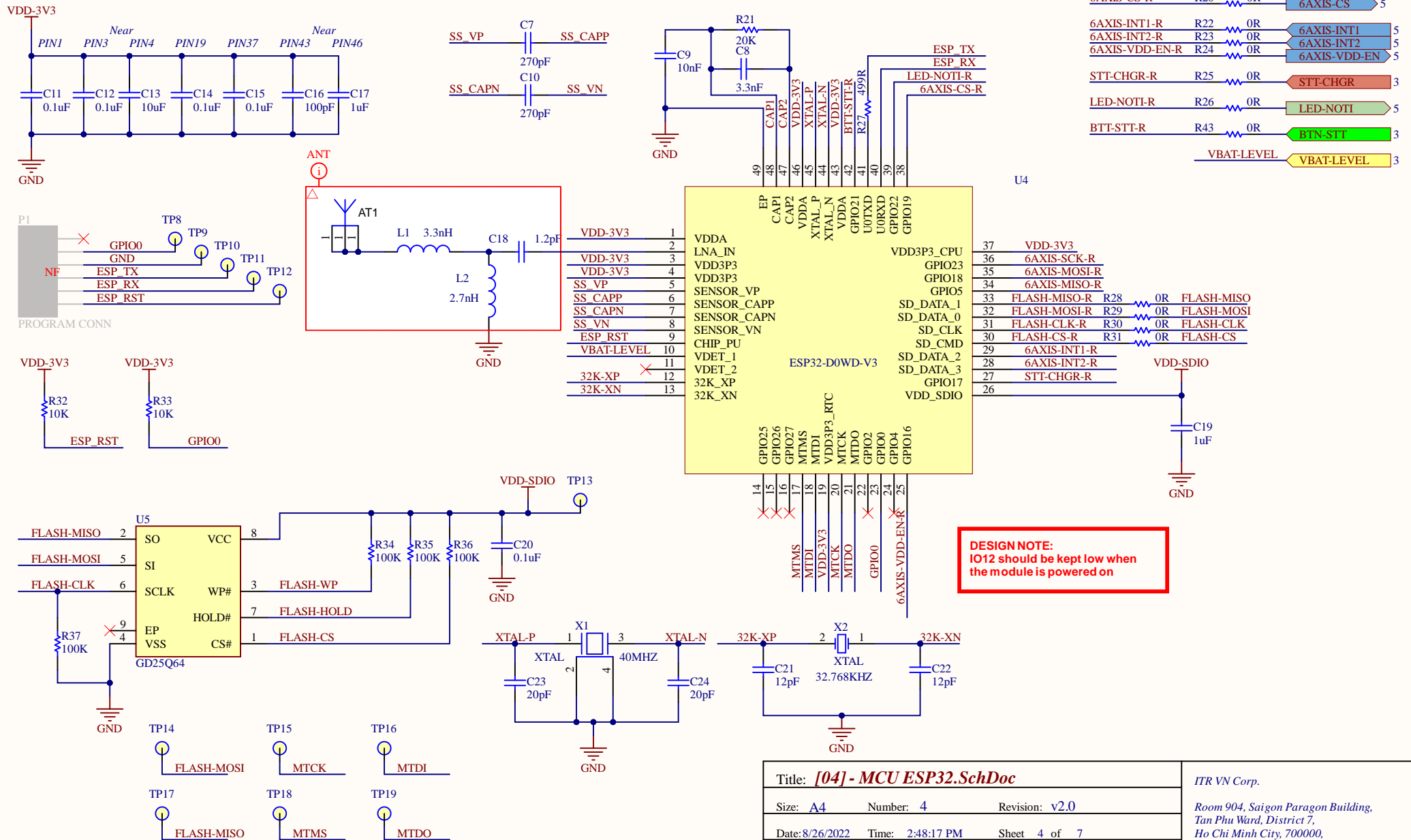
Title: <b>[02] - BLOCK DIAGRAM.SchDoc</b>			<i>ITR VN Corp.</i>  <i>Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam</i>
Size: <b>A4</b>	Number: <b>2</b>	Revision: <b>v2.0</b>	
Date: <b>8/26/2022</b>	Time: <b>2:48:16 PM</b>	Sheet <b>2</b> of <b>7</b>	
File: <b>[02] - BLOCK DIAGRAM.SchDoc</b>			

## D



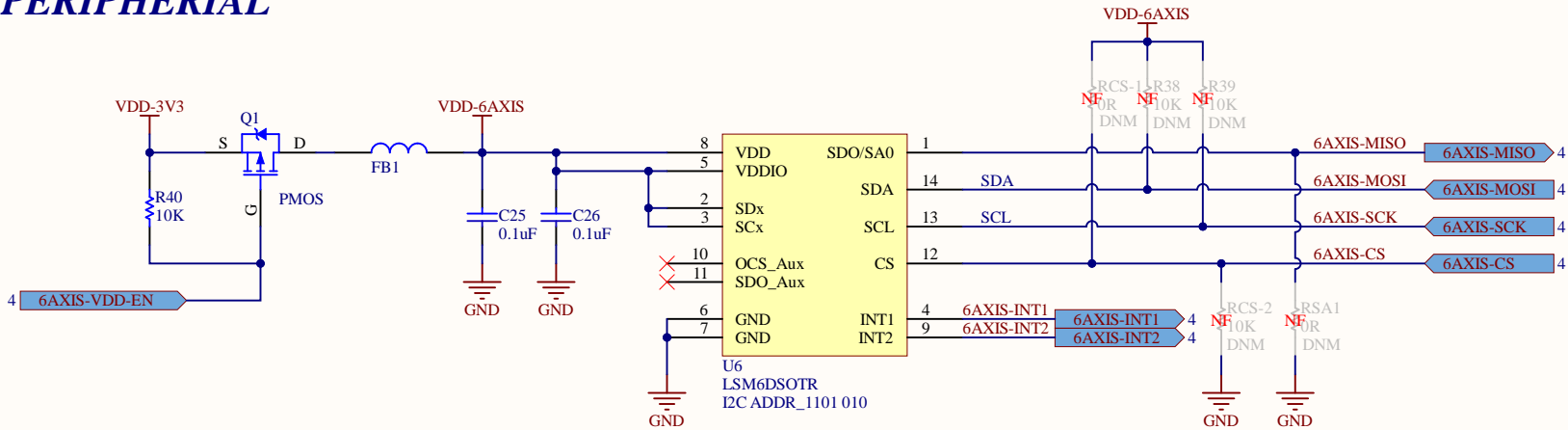
Title: <b>[03] - POWER SUPPLY.SchDoc</b>	<i>ITR VN Corp.</i>  <i>Room 904, Saigon Paragon Building,</i> <i>Tan Phu Ward, District 7,</i> <i>Ho Chi Minh City, 700000,</i> <i>Vietnam</i>
Size: <b>A4</b> Number: <b>3</b> Revision: <b>v2.0</b>	
Date: <b>8/26/2022</b> Time: <b>2:48:16 PM</b> Sheet <b>3</b> of <b>7</b>	
File: <b>[03] - POWER SUPPLY.SchDoc</b>	

# MCU ESP32

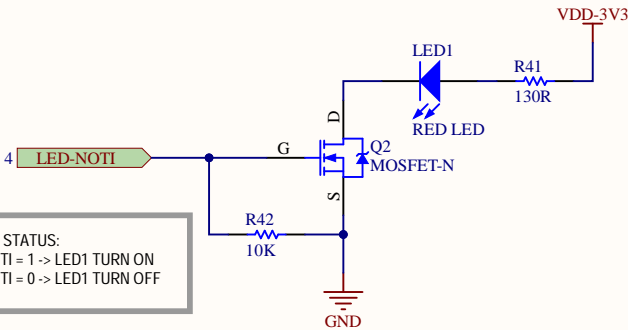


Title: <b>[04] - MCU ESP32.SchDoc</b>			<i>ITR VN Corp.</i>  <i>Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam</i>
Size: <b>A4</b>	Number: <b>4</b>	Revision: <b>v2.0</b>	
Date: <b>8/26/2022</b>	Time: <b>2:48:17 PM</b>	Sheet <b>4</b> of <b>7</b>	
File: <b>[04] - MCU ESP32.SchDoc</b>			

PERIPHERIAL



SPI Mode (Default)  
I2C Mode:  
\* RCS = VDD  
\* RSA0 = GND



LED-STATUS STATUS:  
\* LED-STATUS = 1 -> LED1 TURN ON  
\* LED-STATUS = 0 -> LED1 TURN OFF

Title: <b>[05] - PERIPHERIAL.SchDoc</b>			<i>ITR VN Corp.</i>  <i>Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam</i>
Size: <b>A4</b>	Number: <b>5</b>	Revision: <b>v2.0</b>	
Date: <b>8/26/2022</b>	Time: <b>2:48:17 PM</b>	Sheet <b>5</b> of <b>7</b>	
File: <b>[05] - PERIPHERIAL.SchDoc</b>			

# DOC: REVISION HISTORY

DATE:  
08/26/2022

REVISION:  
2.0

CONTENT:  
Initial release of TENA\_V2.0

Title: <b>[06] - DOC REVISION HISTORY.SchDoc</b>	<i>ITR VN Corp.</i>  <i>Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam</i>
Size: <b>A4</b> Number: <b>6</b> Revision: <b>v2.0</b>	
Date:8/26/2022    Time: 2:48:17 PM    Sheet 6 of 7	
File: [06] - DOC REVISION HISTORY.SchDoc	

	1	2	3	4								
A					A							
B	<div><div><div>Designator [01] - COVER PAGE.SchDoc</div><div></div></div><div><div>Designator [02] - BLOCK DIAGRAM.SchDoc</div><div></div></div><div><div>Designator [03] - POWER SUPPLY.SchDoc</div><div></div></div><div><div>U_[04] - MCU ESP32 [04] - MCU ESP32.SchDoc</div><div></div></div><div><div>U_[05] - PERIPHERIAL [05] - PERIPHERIAL.SchDoc</div><div></div></div><div><div>Designator [06] - DOC REVISION HISTORY.SchDoc</div><div></div></div></div> <div><div>Schematic Status Explanation</div><div>DRAFT - Very early stage of schematic, ignore details.</div><div>PRELIMINARY - Close to final schematic.</div><div>CHECKED - There should not be any mistakes. Tell the engineer if you find one.</div><div>RELEASED - A board with this schematic has been sent to production.</div></div> <table><tr><td>Title: <b>TENA Project.SchDoc</b></td><td rowspan="4">ITR VN Corp.  Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam</td></tr><tr><td>Size: <u>A4</u>      Number: <u>7</u>      Revision: <u>v1.0</u></td></tr><tr><td>Date:<u>8/26/2022</u>      Time: <u>2:48:17 PM</u>      Sheet <u>7</u> of <u>7</u></td></tr><tr><td>File: <u>TENA Project.SchDoc</u></td></tr></table>				Title: <b>TENA Project.SchDoc</b>	ITR VN Corp.  Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam	Size: <u>A4</u> Number: <u>7</u> Revision: <u>v1.0</u>	Date: <u>8/26/2022</u> Time: <u>2:48:17 PM</u> Sheet <u>7</u> of <u>7</u>	File: <u>TENA Project.SchDoc</u>	B	C	D
Title: <b>TENA Project.SchDoc</b>	ITR VN Corp.  Room 904, Saigon Paragon Building, Tan Phu Ward, District 7, Ho Chi Minh City, 700000, Vietnam											
Size: <u>A4</u> Number: <u>7</u> Revision: <u>v1.0</u>												
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