

ABSTRACT

Security and privacy is one of most basic human rights, that should be provided, regardless of the significance of human life. However, due to inefficiency of traditional methods currently in use, security and privacy rights can easily be bypassed and voided. Thus the study aims to implement a robust and secure system that will protect the security and privacy rights.

The researchers utilized RFID technology and Arduino board, and machine learning models of face recognition, which was evaluated for use as lock access, attendance checking, and security system. A complementary regression model was also devised to assess the cost effectiveness of Arduino-based systems.

It was found that RFID and Arduino-based lock access systems have an average unlock and lock time of $\mu_{unlock} = 2.80s$, and $\mu_{lock} = 6.60s$, and is $\approx 20 \pm 2\%$ more cost efficient than traditional lock access systems, while the machine learning face recognition models can $\approx 59 \pm 1\%$ classify faces correctly, with an average speed of $\mu_t = 0.44s$, at a $30 \pm 20\%$ computation power.

Results showed that RFID and Arduino-based lock access systems are more efficient and robust than traditional methods, due to their hidden lock system and string comparison algorithm. And despite of the drawbacks and early stages of the model, it has an above average performance. Thus it is concluded that RFID and Arduino, and machine learning models of face recognition, are suitable as replacement for traditional lock, attendance checking, and security system, respectively.

Keywords: *artificial intelligence, arduino, security, machine learning, cutting edge technologies*

THE PROBLEM AND ITS BACKGROUND

Introduction

Regardless of the significance of human life as both argued as important and non-significant by anthropic principle (Weinberg, 1989; Barrow & Tipler, 1988; Schrodinger, 1944) and the laws of nature (Kaku, 1995), respectively. Providing the rights, specifically security and privacy is arguably an utmost necessity. However, due to the inefficiency and high vulnerability of traditional systems and the technology itself, it is easily bypassed, and voided at worst. Furthermore, the exponential increase of technological advancements (Kaku, 1995) and the revolution it provides in understanding of the grand design (Katori *et al.*, 2022), consequently allowed the increased acceleration of security and privacy threats, and its metamorphosis to cyberspace, such examples are telemetry, spywares and ransomwares. Hence efforts are made to yield products that would prevent and decrease the progression of threat.

One of the yields from continuous efforts against security and privacy violation and exploitation in cyberspace is the movement towards free and open source software (FOSS), while increased and improved locking mechanism has been the direction of combat against physical threats, and combination of both has also shown to be effective. Nevertheless, both measures have its own advantages and disadvantages, such as FOSS softwares have an exposed vulnerabilities the attacker and public user can exploit or patch, and allows to be modified for certain needs, due to the accessible code base and algorithms. Physical measures on the other hand ascertain the decreased vulnerability of the mechanism, however with high expense and intensive maintenance.

Due to the accelerating improvement of technologies, it is necessary to keep up with the changes. Since stabilization is not sufficient, implementation is necessary as everyone is affected by changes in macro and micro level due to the interconnectivity (Lewin *et al.*, 2018; Lewin *et al.*, 2022) of organisms, thus a successful implementation of technological advancements would yield a benefit to the particular society. Hence, the goal of the study is to implement the cutting edge technological advancements, specifically RFID and Arduino, and machine learning models of face recognition.

Review of Related Literature

This section presents the review of related literature and studies regarding the mechanisms and theoretical principle of RFID, electromagnetism, algorithms and the programming languages, and the derivation of the equations presented in the later section.

According to Standard Model of Quantum Physics (Giancoli, 2016) the electromagnetic waves are carried by specific quanta, photons, γ , that are product of electron-electron (e^-) interactions as shown by Feynman diagram:

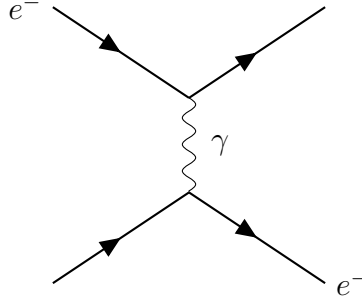


Figure 1: Electron-electron interaction as shown by Feynman's diagram.

Where as the wave function, $\phi(\{\mathbf{r}_i\sigma_i\})$, and its energy, E_v^e are given by:

$$H^e = \sum_{k=1}^N -\frac{\hbar}{2m} \nabla_{r_k}^2 + \sum_{k=1}^N v(\mathbf{r}_k) + \frac{1}{2} \frac{1}{4\pi\epsilon_0} \sum_{\substack{k,k' \\ k=1}}^N + \frac{e^2}{|\mathbf{r}_k - \mathbf{r}_{k'}|} \quad (1)$$

Quantum Mechanics, derived from Photoelectric effect of Albert Einstein, suggested that particles are in dual nature, a wave in spacetime continuum, and particle in nature, hence the term “wave particle duality” (Giancoli, 2016), which can be shown with Schödinger's equation. And the fact that energy and mass is an interchangeable entity, as shown in Special Theory of Relativity, hence mass-energy, electromagnetic waves can used as a method for efficient information transfer, as further supported by novel mass-energy-information equivalence, that mass, and energy, and information is an interchangeable entity (Vopson, 2022).

In derivation of mass-energy equivalence principle, $E = mc^2$, a hypothetical scenario was devised. Consider, an object with mass, m_0 , and assume that it moves under the influence of force, F . According to the Work-Energy theorem, the change in energy, E , is the work done

by force, F :

$$E = \int_0^x F dx \quad (2)$$

Since $F = ma$:

$$F = \frac{d}{dt_1}(m_1 v_1) = \frac{d}{dt_1} \left(\frac{m_0 v_1}{\sqrt{1 - \frac{v_1^2}{c_1^2}}} \right) \Rightarrow \frac{m_0 a_1}{\left(1 - \frac{v_1^2}{c_1^2}\right)^{3/2}}$$

As if the mass change then force, F , is changed in momentum $F = \frac{d}{dt}(mv)$. Then it follows that:

$$\begin{aligned} E &= \int_0^x F dx = \int_0^x \frac{m_0 a_1}{\left(1 - \frac{v_1^2}{c_1^2}\right)^{3/2}} dx \\ &\Rightarrow m_0 \int_0^x \frac{a_1}{\left(1 - \frac{v_1^2}{c_1^2}\right)^{3/2}} dx \end{aligned}$$

Since:

$$a_1 = \frac{dv_1}{dt_1} = \frac{dv_1}{dx} \frac{dx}{dt_1} = v_1 \frac{dv_1}{dx}$$

Through substitution E can be rewritten as:

$$\begin{aligned} E &= m_0 \int_0^x \frac{v_1}{\left(1 - \frac{v_1^2}{c_1^2}\right)^{3/2}} \frac{dv_1}{dx} dx \\ &\Rightarrow m_0 \int_0^{v_1} \frac{v_1}{\left(1 - \frac{v_1^2}{c_1^2}\right)^{3/2}} dv_1 \end{aligned} \quad (3)$$

And finally:

$$E = c^2(m_1 - m_0) \longrightarrow \Delta mc^2 \quad (4)$$

And showing that information has mass, and is energy, the mass instead of $\Delta m = \frac{E}{c^2}$, becomes:

$$m_{bit} = \frac{k_b T \cdot \ln(2)}{c^2} \quad (5)$$

Thus there must be a direct connection between the process of creating, manipulating or erasing information and laws of thermodynamics, since digital information is created as an outcome of computational process, via particular physical process that obeys the physical laws of nature, including the laws of thermodynamics (Vopson, 2019), as reasoned by Landauer's extended principle in Equation 5.

Which can be further explained that, since the content of information stored in a physical mass allows holding the information without energy dissipation indefinitely (Vopson, 2019). Thus it requires input external work and the mass, m_{bit} , is converted back into energy/heat, as implicated by the rationale that the mass-energy equivalence of Einstein's Special Relativity, can be extrapolated to the mass-energy-information equivalence principle.

The information carried by the photons, γ , are then utilized by RFID, whereas it carries information based on the wavelength, $\lambda = \frac{c}{f}$. While the matter-energy interaction can further be described using different set of equations, it is deemed unnecessary in this review.

Computers then process and allows the interaction, as well as further modification with the information, in this context by the RFID receiver, via a particular physical computational processes, since the information needs to be further processed for utilization, as it is simply an entropy in the most basic form, which is defined as by McDonnell, Ikeda, and Manton (2011) as:

$$H(X) = -E_{p(X)}[\log p(x)] \quad (6)$$

Since information is part of a system contained in this universe, and an interchangeable entity of mass and energy (Vopson, 2022; Vopson, 2019), it is assumed to dissipate over time. Zak (2018) mathematically modeled the heat death of universe using the law of thermodynamics that describes the entropy (Giancoli, 2016). Madelung (1926) cited by Zak (2018) modeled the hydrodynamics version of the Schrödinger equation, in which the quantum potential and the classical potential can be seen, as last term and F :

$$\frac{\partial S}{\partial t} + \nabla \bullet \left(\frac{\rho}{m} \nabla S \right) = 0 \quad (7)$$

$$\frac{\partial S}{\partial t} + (\nabla S)^2 + F - \frac{\hbar^2 \nabla^2 \sqrt{\rho}}{2m\sqrt{\rho}} = 0 \quad (8)$$

Where ρ and S are the components of wave function, $\Psi = \sqrt{\rho} e^{i\frac{S}{\hbar}}$ and planck constant, \hbar divided by 2π . Using ansatz, Madelung equations can be converted into Schrodinger equation:

$$\sqrt{\rho} = \Psi \exp(-i\frac{S}{\hbar})$$

Using Liouville equation:

$$\frac{\partial \rho}{\partial t} + \nabla \bullet (\rho F) = 0 \quad (9)$$

Which was used to generalized the concept of probability, ρ , generated by system of ordinary differential equations, $\frac{dv}{dt} = F[v_1(t), v_2(t), v_3(t), \dots v_n(t)]$:

$$\frac{dv}{dt} = F[p(v)] \quad (10)$$

$$\frac{\partial \rho}{\partial t} + \nabla \bullet \rho F[p(v)] = 0 \quad (11)$$

Equation 9 was generated using Liouville equation generated by Equation 10, which is in contrast to $\frac{\partial \rho}{\partial t} + \nabla \bullet (\rho F) = 0$, nonlinear with respect to the probability density, ρ .

The force, F , that plays the role of feedback from Liouville equation, to the:

$$F = c_0 + \frac{1}{2_1} \rho - \frac{c_2}{\rho} \frac{\partial \rho}{\partial v} + \frac{3}{\rho} \frac{\partial^2}{\partial v^2} \quad (12)$$

$$c_0 > 0, c_1 > 0, c_3 > 0$$

Then through reduction, it transforms into :

$$\ddot{v} = c_0 + \frac{1}{2_1} \rho - \frac{c_2}{\rho} \frac{\partial \rho}{\partial v} + \frac{3}{\rho} \frac{\partial^2}{\partial v^2} \quad (13)$$

And then the Liouville equation will transform into :

$$\frac{\partial \rho}{\partial t} + (c_0 + c_1 \rho) \frac{\partial \rho}{\partial V} - c_2 \frac{\partial^2}{\partial V^2} + c_3 \frac{\partial^3 \rho}{\partial V^3} = 0$$

This equation is known as kdV-Burgers (Korteweg-deVries-Burgers) partial differential equation (PDE) :

$$\int_{-\infty}^{\infty} \rho dV = 1$$

And finally the final change in entropy can be show by assuming that the simplest cases of the system that is $c_0 = 0, c_2 = 0, c_3 = 0, c_1 > 0$:

$$\begin{aligned} \frac{\partial H}{\partial t} &= - \frac{\partial}{\partial t} \int_{-\infty}^{\infty} \rho \ln \rho dV = - \int_{-\infty}^{\infty} \dot{\rho} (\ln \rho + 1) dV \\ &\Rightarrow \int_{-\infty}^{\infty} \frac{1}{2} c_1 \frac{\partial}{\partial V} (\rho^2) (\ln \rho + 1) dV \\ &= \frac{1}{2} c_1 \left[\int_{-\infty}^{\infty} \rho^2 (\ln \rho + 1) dV - \int_{-\infty}^{\infty} \rho dV \right] = \frac{1}{2} c_1 < 0 \end{aligned} \quad (14)$$

This difference is called *Schrödinger paradox*: in a system obeying the second law of ther-

modynamics, all isolated system, such as this universe, is expected to reach a state of maximum disorder (Zak, 2018; Kaku, 1995).

Thus reading the information stored in a physical mass can be done with the use of computers operated by programming language, since humans cannot decode the information efficiently, and due to the fact that it is entropy that can be easily increase in a short amount of time.

It is a well established basic fact that kernel interaction is the precedence of programming language or machine language interaction with the input, usually by C and C++ in operating systems, creating a loop of: interface \longrightarrow software \longrightarrow kernel \longrightarrow hardware

Programming languages can be used efficiently with algorithms, which is defined as a set of instructions for a certain task. One exemplification is its utilization in Machine Learning, also known as computational learning theory, is the logistic algorithm which is used for exploration of relation of independent and dependent variables:

$$\begin{aligned}\nabla J(\theta) &= \theta + \sum_{i=1}^m \frac{\exp\left(\left\langle -y_i \hat{\phi}(x_i), \theta \right\rangle\right)}{1 + \exp\left(\left\langle -y_i \hat{\phi}(x_i), \theta \right\rangle\right)} (-y_i \hat{\phi}(x_i)) \\ &= \theta + \sum_{i=1}^m (p(y_i|x_i, \theta) - 1) y_i \hat{\phi}(x_i)\end{aligned}$$

with its Hessian, computed as:

$$\nabla^2(\theta) = I - \sum_{i=1}^m p(y_i|x_i, \theta)(1 - p(y_i|x_i, \theta)) \hat{\phi}(x_i) \hat{\phi}(x_i)^\top \quad (15)$$

The study utilized simple linear regression, that can be derived using the direct aggression approach that minimizes the sums of squares:

$$S(\beta_0, \beta_1) = \sum_{i=1}^n \varepsilon_i^2 = \sum_{i=1}^n (y_i - \beta_0 - \beta_1 x_i)^2$$

Then the partial derivations of $S(\beta_0, \beta_1)$ with respect to β_0 , and with respect to β_1 , can be solved as Equation 16, and 17, respectively :

$$\frac{\partial S(\beta_0, \beta_1)}{\partial \beta_0} = -2 \sum_{i=1}^n (y_t - \beta_0 - \beta_1 x_i) \quad (16)$$

$$\frac{\partial S(\beta_0, \beta_1)}{\partial \beta_1} = -2 \sum_{i=1}^n (y_t - \beta_0 - \beta_1 x_i) x_i \quad (17)$$

The solutions of the two equations are called direct regression estimators, or ordinary least squares estimators of β_0 and β_1 .

$$\begin{aligned} \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_0^2} &= -2 \sum_{i=1}^n (-1) = 2n \\ \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_1^2} &= 2 \sum_{i=1}^n x_i^2 \\ \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_0^2 \partial \beta_1} &= 2 \sum_{i=1}^n x_t = 2n\bar{x} \end{aligned}$$

The Hessian matrix, is then given as :

$$H^* = \begin{pmatrix} \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_0^2} & \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_0^2 \partial \beta_1} \\ \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_0^2 \partial \beta_1} & \frac{\partial^2 S(\beta_0, \beta_1)}{\partial \beta_1^2} \end{pmatrix} \quad (18)$$

$$= 2 \begin{pmatrix} n & n\bar{x} \\ n\bar{x} & \sum_{i=1}^n x_i^2 \end{pmatrix} \quad (19)$$

$$= 2 \begin{pmatrix} \ell' \\ x' \end{pmatrix} (\ell, x) \quad (20)$$

The matrix H^* is a positive definite if its determinant and the element in the first row and column of H^* are positive. The determinant of H^* is given by:

$$|H^*| = 4 \left(n \sum_{i=1}^n x_i^2 - n^2 \bar{x}^2 \right) \quad (21)$$

$$= 4n \sum_{i=1}^n (x_i - \bar{x})^2 \quad (22)$$

$$\leq 0$$

Finally, the equation or the fitted line or the fitted linear regression model then is:

$$y = b_0 + b_i x \quad (23)$$

where β_0 or b_0 is the processed value of dependent variable, and $\theta_1, \theta_2, \dots, \theta_n$ are the parameters. Based on this, the regression model were rewritten as, $\mathbf{y}_i = m\mathbf{x}_i + b$, where the algorithm will learn the hypothesis using the existing given dataset to predict \mathbf{y}_i using \mathbf{x}_i and the parameters of the hypothesis, $\theta_1, \theta_2, \dots, \theta_n$, that can be presented in matrix form:

$$\mathbf{x}_i = \begin{pmatrix} x_{11} & x_{32} & . & . & . & . & x_{1n} \\ x_{21} & x_{32} & . & . & . & . & x_{2n} \\ x_{31} & x_{32} & . & . & . & . & x_{3n} \\ . & . & . & . & . & . & . \\ . & . & . & . & . & . & . \\ x_{m1} & x_{m2} & . & . & . & . & x_{mn} \end{pmatrix} \quad \theta = \begin{pmatrix} \theta_0 \\ \theta_1 \\ . \\ . \\ \theta_j \\ . \\ . \\ \theta_m \\ \theta_n \end{pmatrix}_{n+1,1} \quad \mathbf{y} = \begin{pmatrix} y_1 \\ y_2 \\ . \\ . \\ y_j \\ . \\ . \\ y_m \\ y_n \end{pmatrix}_{m,1} \quad (24)$$

Machine learning is a higher subset of Artificial Intelligence, with the ability of improve itself overtime via “*experience*”. However, this fact raised various conspiracy theories, such as

claims by Elon Musk, and other science fiction literature authors, and late physicist Stephen Hawking, on a context that artificial intelligence poses an imminent threat to the human civilization, that can lead to annihilation.

It is apparent that the arguments are flawed since it ignores the fact that the actions and ability of the artificial intelligence or machine learning algorithms are dependent on the training and input data provided. Thus, the actions of artificial intelligence or machine learning models are predetermined by the dataset and on how the models are trained, in the same sense, that an individual would not be a terrorist if not for the belief and philosophy taught via social-learning interactions.

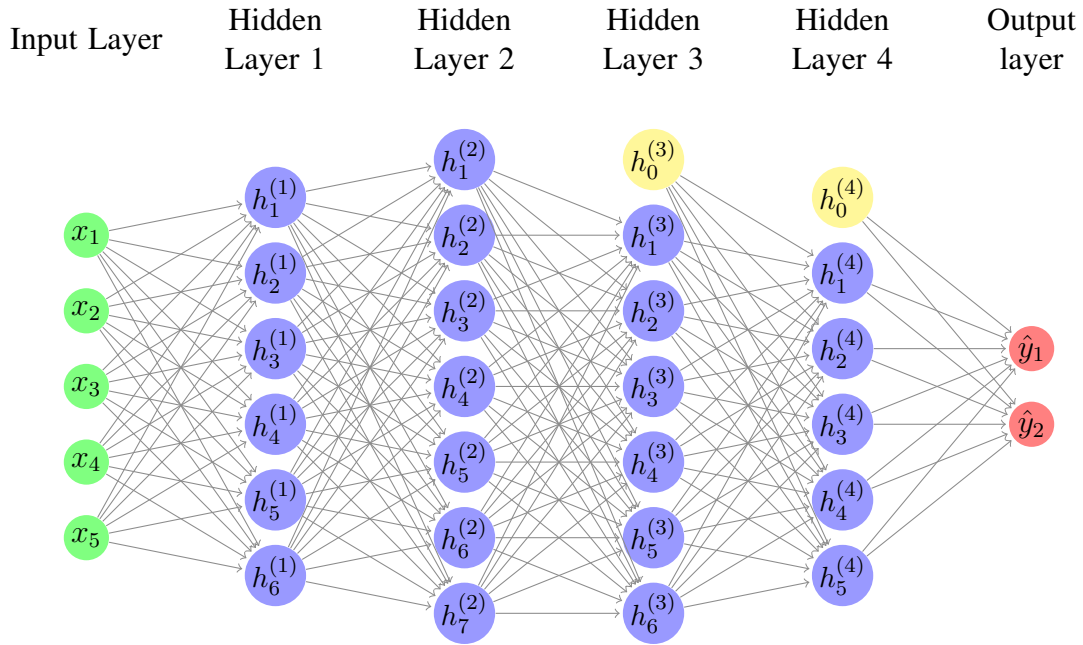


Figure 2: A simple model of a neural network.

Furthermore, fact that machine learning improves itself overtime through “*experience*”, does not imply in anyway a form of consciousness, but is justified and a normal ability of the model, explained by neural networks, precisely, artificial neural networks (ANN) (Figure 2), which is the framework of the models. Artificial Neural Networks are the base of the artificial intelligence, as well as machine learning and deep learning models (Mahmood & Shrestha, 2019; Okikanwo *et al.*, 2017), modeled from human nervous system and structure of brain (Mahmood & Shrestha, 2019) to emulate its ability (Jiang *et al.*, 2017), to deal with complex and computationally demanding tasks such as face recognition, body planning, and control of

muscle activities.

ANNs are consisted of various nodes, also called artificial neurons that are connected by synapses, which was developed from every process of events, called synapse formation in human biology (Batool *et al.*, 2019) during development that determines the functional outputs of the (nervous) system.

Figure 2 depicts the structure of ANN, which is composed of various layers, first is the input layer, where the input is taken, $\mathbf{x} = [x_1, x_2, x_3, \dots, x_n]$, and further transformed in the hidden layer, which is always higher dimension.

Regardless of its simple parallel computational structure, it has an appealing learning ability and computational power for nonlinear dynamic pattern predictions (Jiang *et al.*, 2017). Furthermore, it is also reported that via overlapping the outputs of each neurons, Radial Basis Function Neural Networks (RBFNN), depicted in Figure 3, can approximate any unknown continuous nonlinear functions (Jiang *et al.*, 2017).

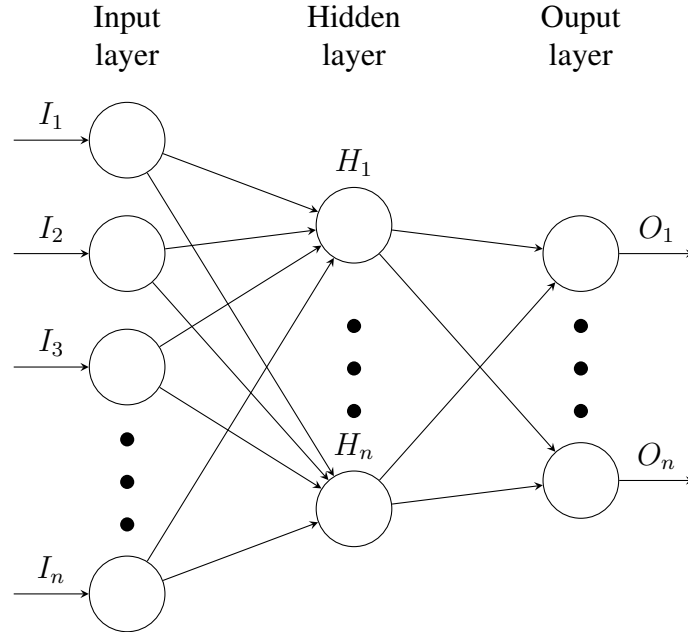


Figure 3: A simple structure of Radial Basis Function Neural Networks (RBFNN).

Suppose a continuous vector function, $F(Z)$, $\hat{F}(Z) = W^T S(Z)$, with $\hat{F}(Z)$ is the approximation of function $F(Z)$, where Z is the input of neural networks vector, the optimal weight of neural networks being, $\hat{W} = [\hat{W}_1, \hat{W}_2, \hat{W}_3, \dots, \hat{W}_n] \in R^{nl}$, $S(Z) = [s_1(Z), s_2(Z), s_3(Z), \dots, s_l(Z)]$ is the regressor, and l is the number of neurons. Going by

the common use, with regressor being a Gaussian radical basis function:

$$s_i(||Z - u_i||) = \exp\left[\frac{-(Z - u_i)^T(Z - u_i)}{\sigma_i^2}\right] \quad (25)$$

$u_i(i = 1, \dots, l)$ are distinct points in space

σ_i is the width of Gaussian membership function

This justify the ability of machine learning to improve itself, via concept of neurons and synapse connection bearing specific weight value for decisions that will allow the algorithm to give the best possible output to its extent (Jiang *et al.*, 2017; Mahmood & Shrestha, 2019).

The number of value of weights, is then determined by the number of generations, or iteration, which is the number of exploration in the training data done by the algorithm, that reveals every possible pattern and correlation. The value of weight was then stored in neurons and synapses, and finally with enough iteration, the model can then determine the best path or connection of nodes for a certain task.

Thus it is concluded that machine learning algorithms are not in anyway conscious by itself, refuting the claims of artificial intelligence taking over the world, and poses an imminent threat to the human civilizations. Quoting mathematician and philosopher, Bertrand Russell, on his “*unyielding despair*”:

That man is product of causes which had no prevision of the end they were achieving; that his origin, his growth, his hopes and fear, his loves and his beliefs, are but outcome of accidental collocation of atoms; that no fire, no heroism, no intensity of thought or feeling, can preserve a life beyond the grave; that all the labors of ages, all the devotion, all the inspiration, all the noon day brightness of human genius, are destined to extinction . . .

Which was continued as “*by the vast death of galaxy*”, not “*by computers or artificial intelligence*”, as physicist Dr. Michio Kaku (1995) once echoed, “*a computer is nothing more than a sophisticated adding machine, an impeccable idiot.*”.

Statement of the Problem

The study aims to implement a new system that replaces traditional methods, specifically the lock access, attendance checking, and security system, via the utilization of RFID technology and Arduino board, and machine learning models of face recognition. The study also sought to determine whether it can be utilized and successfully replace the traditional methods. Hence, in the final analysis the study sought to:

1. Determine if RFID technology and Arduino board can be utilized as lock access system;
2. Determine if machine learning models of face recognition can be utilized to check student's attendance, and as a security system;
3. Devise a regression model of the cost-effectiveness of RFID and Arduino-based systems against traditional methods in the following time frame:
 - (a) 12 months;
 - (b) 36 months;
 - (c) and 60 months;

Hypotheses of the Study

The researchers devised the following null hypotheses to be disproved by the study:

1. RFID technology and Arduino board are not suitable to be utilized as lock access system;
2. Machine learning models of face recognition are not compatible for utilization to check the attendance of students and as a security system.
3. RFID and Arduino-based lock access systems are less cost efficient than traditional methods in the following time frame:
 - (a) 12 months;
 - (b) 36 months;
 - (c) and 60 months;

Objectives of the Study

It is necessary to guarantee and provide the safety and security of students in school successfully. Moreover, due to the exponential improvement of technology (Kaku, 1995), adapting to the changes is a necessity as it did not only created a new path for opportunities, but also increases the number and acceleration of threat, thus the study aims to implement the technological advancements to deal with and resist the infringement on security and privacy as well as its exploitation. Therefore, the objectives of the study are:

1. Implement the current technological advancement for use in long term use.
2. Develop a *de novo* convenient and efficient system as a replacement of traditional lock access, attendance checking, and security system.
3. Utilize RFID technology and Arduino board, and machine learning models of face recognition for use in locker, and attendance and security system, respectively.

Significance of the Study

The interconnectivity of every organism (Lewin *et al.*, 2018; Lewin *et al.*, 2022) relays the changes and actions of one to another, thus a change in one society will affect another and may even affect larger systems, which is also known as butterfly effect in Chaos theory. Hence, making the correct action may likely lead to improvement of the particular society at the very least, while making the opposite may lead to acceleration of depressing consequence or the “*unyielding despair*”, once echoed by mathematician and philosopher, Bertrand Russell (Kaku, 1995).

Among the goals of the study is to address the gap and vulnerability of currently implemented mechanisms and technology of traditional methods via utilization and implementation of RFID technology and Arduino board, and machine learning models of face recognition as replacement of the inefficient traditional and other methods in current use, specifically lock access, and attendance checking and security system, respectively.

School Administrators. This study will help the school administrators to ascertain that the basic rights of students are provided. It would also help in improving comfort, in sense

of security, and convenience. Moreover, the study would significantly decrease the cost in maintenance, as well as the implementation and replacement, and the overall expenses in macro scale, thus allowing the funds to be utilized in other necessities, and help in adaptation and transition for the other upcoming innovations and technological revolutions.

Students. This study will ensure the safety and privacy of students within the school area, due to the decreased entry and simplified reporting as well as alert of break in by intruders, or other unregistered entities. Furthermore, the RFID and Arduino-based lock access systems, are more robust in providing security to the lockers, and other storage rooms, thus ensuring the security of belongings of students.

Parents and teachers. The sense of comfort the study would provide due to the assurance of safety of students, would give a significant relief in parents and teachers. Furthermore, in long term, the study would decrease the costs expended in utilities such as attendance notebooks.

Environment, and general community. The study also significantly reduces the use of other materials such as locks, and attendance notebooks in macro scale, thus decreasing the contribution for increasing solid wastes, that further contributes to pollution.

Scope and Delimitation

The study was limited in devising an RFID and Arduino-based lock access systems, and face recognition-based attendance checking and security systems derived from machine learning models of face recognition, as one of the main goals of the study is to utilize and implement the current technological advancements.

The models of RFID and Arduino-based lock access systems and face recognition-based attendance checking and security systems was evaluated in terms of unlock and lock time, and classification time as well as classification ability, respectively. While for face recognition-based attendance checking and security systems derived from machine learning models of face recognition, a complete software was created by the researchers, with a fully functioning algorithm that can be further imported as module for other purposes, and modified as per certain needs.

A git repository with complete branches and code commits history with complete description of changes were also provided for further studying of the source code, including the example database as one of the branches with template for production-ready utilization.

Linear regression models of with time frames of 12 months, 36 months and 60 months were also devised to model the cost efficiency of Arduino against traditional methods.

Theoretical Framework

Machine learning, also known as computational learning theory, is a subset of artificial intelligence that has the ability of to improve itself through repeated iteration on previous and new datasets (Mahmood & Shrestha, 2019) that allows for discovery of new patterns and correlations of the variables, improving the neural networks (Jiang *et al.*, 2017; Mahmood & Shrestha, 2019). Avrim Brum, from Carnegie Mellon University, further defined it as an ability of the algorithm to improve itself and learn from new inputs and adapt to changes, which improves the performance through “*experience*”. By doing so, machine learning algorithms can predict and simulate phenomena based on the previous dataset or random inputs from curated dataset, moreover, depending on the model type, it is also possible to produce *de novo* outputs from the algorithm.

Conceptual Framework

The series are broken into 3 phases, the first phase is the preparation, this includes the gathering of the materials needed in the study, and the writing of pseudo-code, as well as its pre-writing.

After the code is prewritten, various branch was created per certain point in milestone for (1.) implementation of new features; (2.) optimizations of code base; (3.) and debugging, marking the start of phase 2. The code of lock access algorithm was then loaded into Arduino, while the machine learning models of face recognition was executed inside virtual environment for testing, optimizations, and error and bug catching. In this phase, all of noticed and caught bugs were fixed in its respective code branch, which was assessed by Codiga, an artificial intelligence assessment utility for code repositories, per pull request for (1.) compliance to Python’s

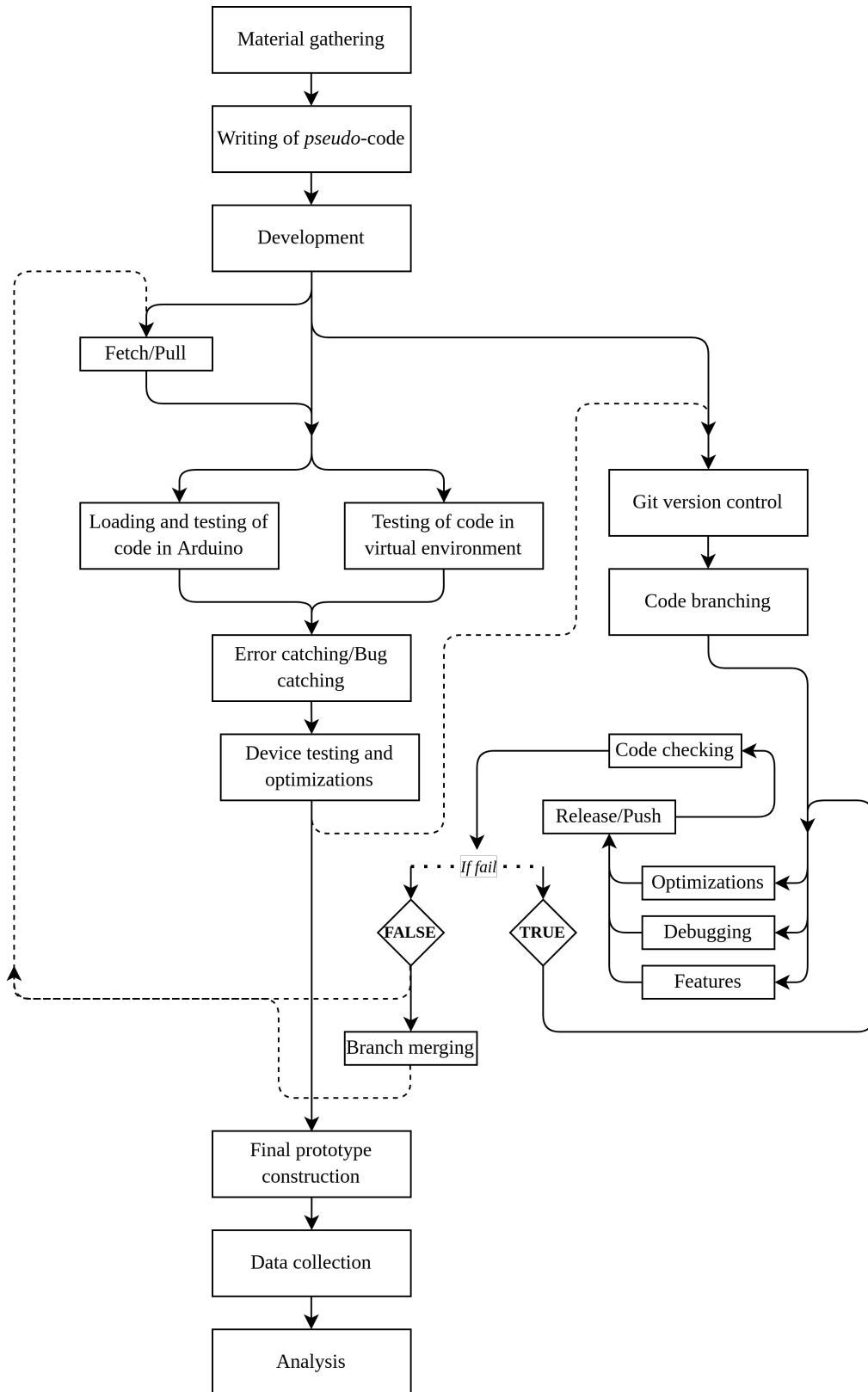


Figure 4: Conceptual framework of the study.

Enhancement Proposals (PEP), as well as suggestions of various linters such as PyLint, which was defined as rules, namely: E0XXX, R*XXX, C*XXX, WXXX, B[1-2]XXX, I.XXX, categorized

per severity as (1.) error; (2.) warning; (3.) security; (4.) and informational. If the code of the pull request violated the rules, the pull request should be fixed with another commit(s) in addition to the existing pull request. The changes from code branch then would be pulled and loaded again for further testing, and error and bug catching. Depending on the case, the branch can then be merged or further forked. The process was done repeatedly until there were no noticeable bugs in the end user perspective, has a considerable execution speed, and smooth performance of program. Finally, the all the conflicts per code branch were fixed, and then branches of the code were merged with the main branch.

In the final phase, the final prototype was constructed, and had undergone data collection. The data was then analyzed using `pandas`, `statistics`, and visualized using `matplotlib`.

Definition of Terms

Branch – is copy of the specified code branch.

Character (char) - is a data type that strictly includes only one ASCII.

Commit - the addition of change in code base for the next push, which is done after the staging of changes.

Container – is an isolated *pseudo*-operating system (OS) used for experimental purposes, such as testing and simulation to avoid main production system pollution.

Merge – is the combining of two different branches, if the pull request was accepted.

Modules – are external code library files, with a suffix of `.py` for a Python code, that contains the additional piece for a particular software.

Push – is the sending of changes from `origin` to the `git` repository.

Pull – process of fetching the new changes from the `git` repository to the cloned branch.

Origin - the source of the upcoming commits for a particular `git` repository.

String (S|string)- is a data type consisted of more than one character.

METHODOLOGY

This chapter presents the complete description of all procedures.

Materials

The main materials used in the study are 1 Arduino UNO R3 board, 20 jumper wires (mm, mf), 1 MFRC522 RFID card reader, 2 RFID cards, 1 27×4 12C LCD screen, 1 servo motor, 9V snap power to DC plug, and 9V battery,

Other materials used are plywood, wood filler, sticker and handle. Electric drill, saw, philips head screws, were also used for creation of the locker prototype.

There were no materials used in machine learning models of face recognition model. An input camera, and faces as training data, is required, however, which includes a myriad of diverse faces.

Procedures

Device Engineering

The lock access, attendance checking, and the security system, has the same input process. The systems take input data to be compared from references in database or system itself, which will be taken from a real-time input device, specifically MFRC522 card reader for lock access, and camera for attendance checking and security system.

In RFID and Arduino-based lock access systems, the receiver takes the information or the 10 character-length string datatype Unique IDentifier (UID) of the RFID card and relayed to the Arduino, while in face recognition-based attendance checking and security systems the camera takes the frame from a video feed, and relay it to the algorithm. However, it differs significantly in the process of systemically verifying and checking the information received.

The RFID and Arduino-based lock access checks the UID using the string compare algorithm loaded in the board, written in C++20. On contrary to the machine learning face recognition-based attendance checking and security system, whereas it needs a large database of students and teachers information, and intensive process, it requires the use of x86.64 or

amd64 system. The input taken from the camera, was then relayed to the checking algorithm written in Python 3.10.4.

Software Engineering

RFID and Arduino-based Lock Access System Algorithm

The algorithm of the RFID and Arduino-based lock access system was depicted by Figure 5, which was written in C++20, and loaded to the board.

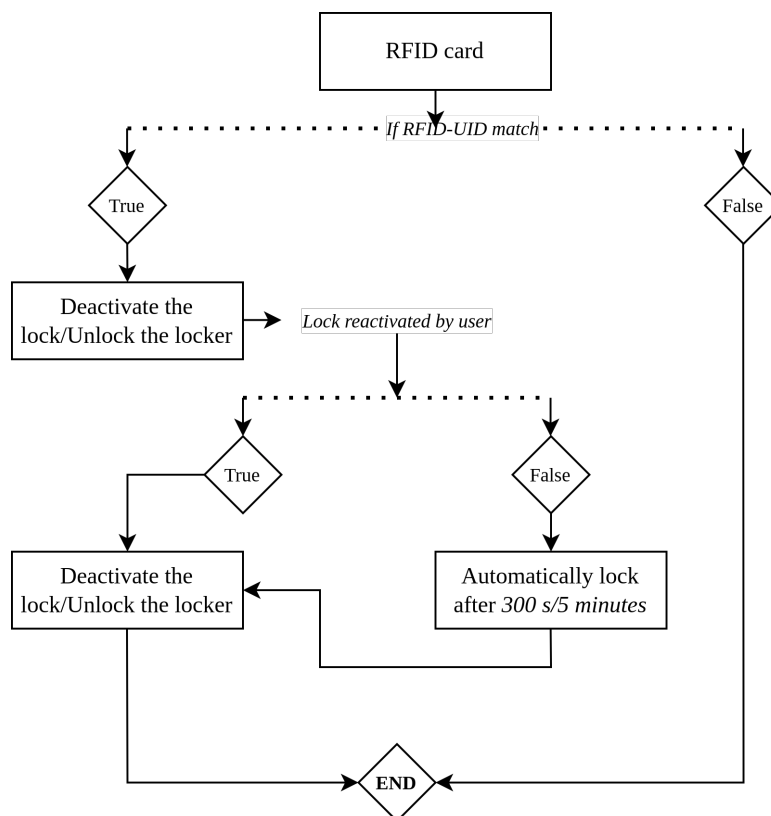


Figure 5: RFID and Arduino-based lock access system algorithm.

To check the input UID from reference UID stored as String datatype, the algorithm compares the hexdump or hexadecimal view of the two string, the reference UID, and the input UID, from memory:

```

1 # string "hello"
2 $ echo "hello" | hexdump
3 00000000 6568 6c6c 0a6f
4 00000006
5
6 # string "Hello"
7 $ echo "Hello" | hexdump
8 00000000 6548 6c6c 0a6f
9 00000006

```

```

10
11 # string "GH 5H 7Y 8U"
12 $ echo "GH 5H 7Y 8U" | hexdump
13 00000000 4847 3520 2048 5937 3820 0a55
14 0000000c
15
16 # string "GH 5H 8U 7Y"
17 $ echo "GH 5H 8U 7Y" | hexdump
18 00000000 4847 3520 2048 5538 3720 0a59
19 0000000c

```

Listing 1: Example of hexadecimal view of various strings

This returns the hexademical view of the input string, which will be compared from the reference string stored in the system. With the results, it is apparent that the string Hello is different from hello, as well as GH 5H 8U 7Y and GH 5H 7Y 8U. Using the hexadecimal view of the string, the algorithm can determine and successfully compare the input UID from reference UID.

Face Recognition-based Attendance and Security System

The main algorithm, as shown in the Figure 6, of the system was written in Python 3.10.4 with the student database stored locally as JavaScript Object Notation (JSON) file (refer to Listing 2), and synchronized with the online repository via git using systemd service.

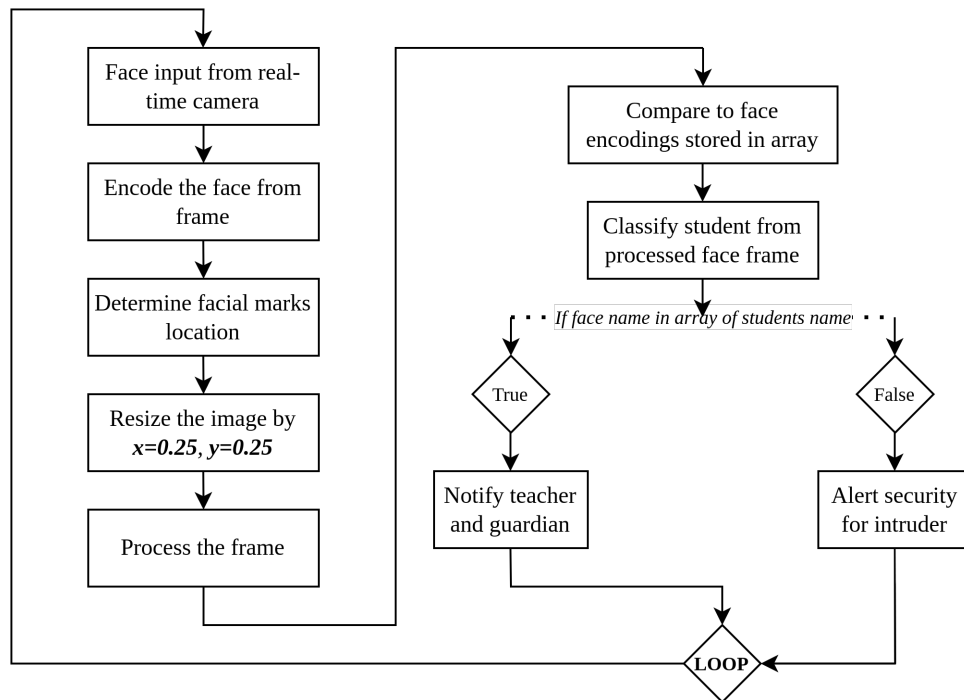


Figure 6: Algorithm of face recognition-based attendance checking and security system.

```

1 {
2
3     "Nicole Amber Hennessey": [
4         "std1.png",
5         "12 STEM - Fleming",
6         "Menard Miguel",
7         "Melody Hennesey",
8     ],
9
10    "Raven Gose": [
11        "std2.png",
12        "12 STEM - Fleming",
13        "Menard Miguel",
14        "Venifer Gose",
15    ],
16
17    "Fiona Leigh Pagtama": [
18        "std3.png",
19        "12 STEM - Fleming",
20        "Menard Miguel",
21        "Emeriza Pagtama",
22    ],
23
24    "Student n" : [
25        "std<n>.png",
26        "<section>",
27        "<adviser>",
28        "<legal guardian>",
29    ]
30
31    ...,
32
33 }

```

Listing 2: Example of stored student information in database.

Various third party modules were used to aid the Python standard library for successful execution of the function. A pre-trained machine learning model was utilized and implemented by the researchers, `face_recognition==1.3.0` based from `dlib` (`python3-dlib.fc36*`), while `numpy==1.22.3` was used to provide the needed mathematical calculation to determine the face locations, and assist in the frame processing, and `opencv-python==4.5.5.64` to access the camera feed, and further frame processing. The email was composed using `email.message.Email.Message` and sent using Python's `smtplib`, and `ssl`, Simple Mail Transfer Protocol, and Secure Sockets Layer, respectively, in addition to `socket`.

Other modules utilized are `json` for parsing of the student data stored as JSON, `sys` and `os` for executing of kernel commands, `rich.console.Console==12.4.1` to give an informative `stdout` of the program, `argparse` was used to design the commandline interface (CLI), `random` to generate of *pseudorandom* ASCII characters imported from `string` for secure access of the database, and `geocoder==1.38.1` to track the IP address of the computer. `git` repository was also setup to be able to modify the data through graphical interface, that will be pulled every 24 hours by a defined `systemd` service.

The algorithm first locates the data of students in database with `os.path.expanduser()`. After successful location, json will parse the data of the students, and store it in high dimensional array with size of N , and the array inside with size of M . The images was then loaded with `face_recognition.load_image_file()`, and encoded using `face_recognition.face_encodings()`, then was stored in a single dimensional array with size of J , as reference(s).

As depicted by the figure above, the program takes input from real-time camera accessed using `cv2.VideoCapture(x)` function from `opencv-python`, which was encoded by `face_recognition` module. To verify the input faces, the algorithm locates the facial features of the frame taken from the input, to decrease the processing time, the input frame were resized by -25% , in dimensions of x and y using `numpy`, and was recolored using `opencv-python`. After processing, the frame was encoded with the same function. Then, it compares the processed input frame encoding, with the encodings of faces stored inside array of size N , via repeated iteration, until a face was matched, or the sequence was completed:

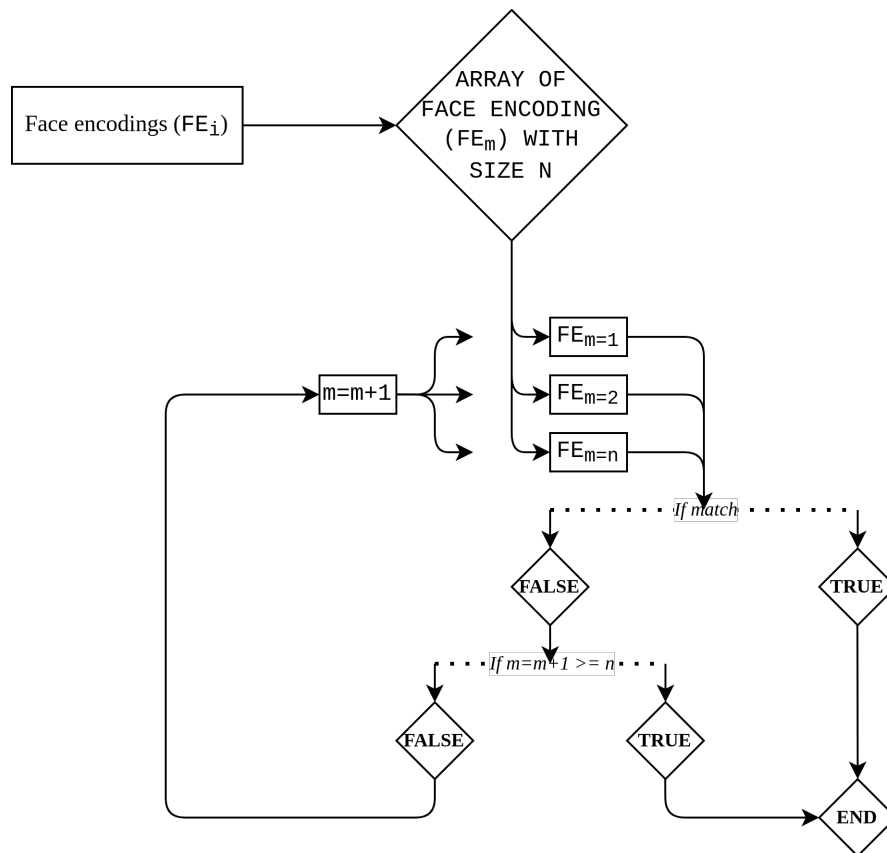


Figure 7: Comparison of encoded frame against encoded references in array.

After confirmed match, the algorithm will send an email to the (1.) registered guardian of the student; and (2.) class adviser of the student including the information of (1.) student; (2.) section; and (3.) time of attendance. If an unrecognized face is detected, on the other hand, an alert email would be sent to the registered security personnel.

Statistical Treatment and Analysis

To study the cost efficiency of the Arduino-based systems, a regression model was devised. Where the algorithm will learn the hypothesis, h using the existing given data set that it can be used to predict y_i using x_i and the parameters of the hypothesis, $\theta_1, \theta_2, \dots, \theta_n$, in an equation form of:

$$h_{\theta}(x_i) = \theta_n + \theta_o(x_i) \quad (26)$$

The regression model devised includes a time frames of 12 months, 36 months, and 60 months, based on the previous fluctuation of prices of Arduino from March 03, 2021 to May 17, 2022 from records of https://pricehistory.in/s/SZ_0E. The data was used to initially generate the set of x_i and y_i , which was further used to generate the new generation data.

Then the generated items from data was further processed with Equation 27:

$$x_i = \frac{\mathbb{R}(\text{dataset}_n) \ln(\mathbb{R}(\text{dataset}_n))}{\mathbb{R}(\text{dataset}_n) + \mathbb{R}(\text{dataset}_n)} \quad (27)$$

And finally, was subjected to linear regression.

PRESENTATION AND INTERPRETATION OF RESULTS

This project was conducted in order to determine whether RFID technology and Arduino board, and machine learning models of face recognition can be utilized and implemented as lock access, and attendance checking and security system, respectively.

Research Findings

RFID and Arduino-based Lock Access System

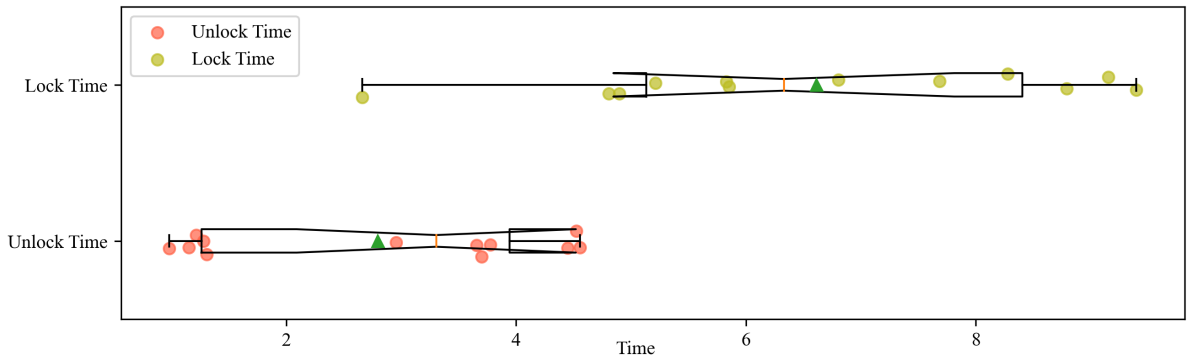


Figure 8: Unlock and lock times of RFID and Arduino-based lock access systems.

According to the measurements conducted, the RFID and Arduino-based lock access system, have an average of $\mu_{t_{unlock}} = 2.797147062378335s \rightarrow 2.80s \pm 0.10s$ for unlock time, and $\mu_{t_{lock}} = 6.613831762216549s \rightarrow 6.60s \pm 0.10s$, as shown by 12 trials in Figure 8. Compared to traditional access methods, such as padlocks, which uses different lock systems such as pin, among others, or a standard doorknob with keys, that may take up to $5s < \mu_t$.

Moreover, the hidden lock system of the RFID and Arduino-based lock access systems render the common forms of lock bypassing methods useless, and the string compare algorithm also further decreases the vulnerability of the system, since a 10 character-length string contains a significantly different checksum than other UID, as shown by Listings 1. This decreases the hacking vulnerability of the system. Thus an RFID and Arduino-based lock access systems, provides more robust and secure system, compared to the traditional lock access systems.

Furthermore, it should also be considered that the performance of the system can further be improved, considering the compromisation of speed due to the high latency time of the LCD

display and the module for its access, `<LiquidCrystal_I2C.h>`, as well as the delay caused by other functions particularly by `<Chrono.h>` that provides `.hasPassed(time)` function for the countdown functionality, and the `<Servo.h>` module that facilitates the control of servo motors used in the locking. Suggesting that the models, despite not in best possible performance, yields an average response time of $\mu_t = 4.7s \pm 0.10s$.

Face Recognition-based Attendance Checking and Security System

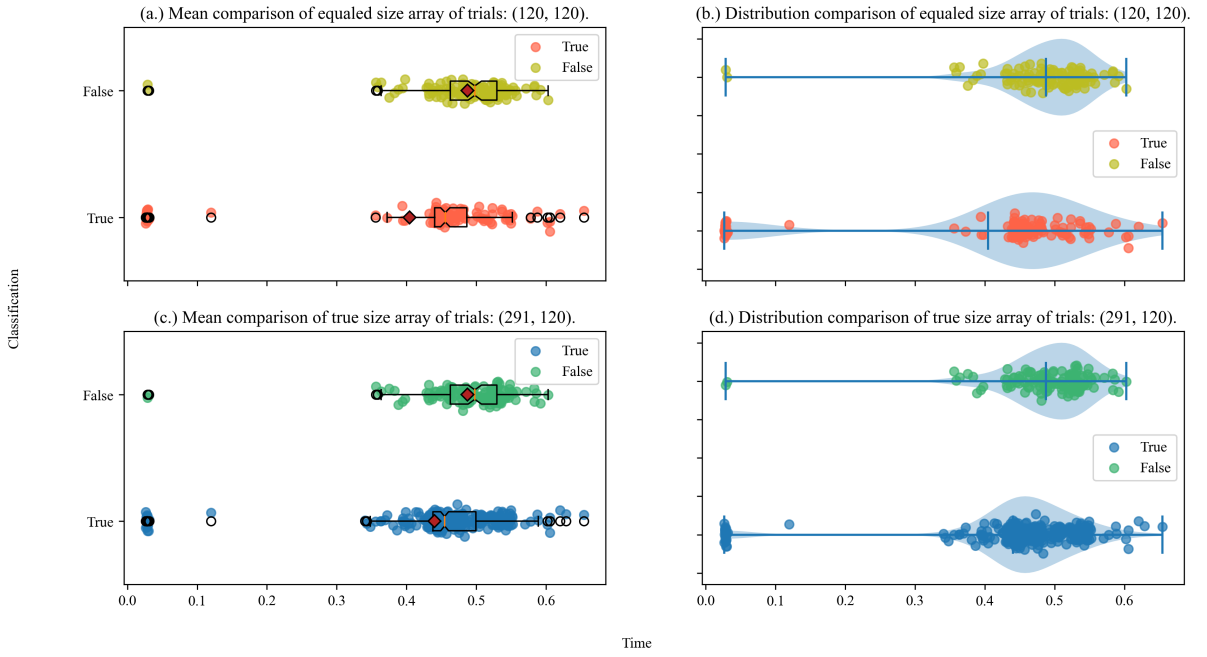


Figure 9: Comparison of average mean and distribution of time trials from 411 trials including random faces. (a.) and (b.) Presents the data of equal size (N) of array (False: 120 vs True: 120/291), and (c.) and (d.) Presents the data of the whole array (False: 120 vs True: 291), except for NaN data type (Not a Number from pandas), since $n > 0$

Concerning the speed and execution of the algorithm provided with the program, as exhibited by Figure 9, averages on, $\mu_{\text{True}} = 0.440194808185567s \rightarrow 0.44s \pm 0.05s$ for correct classification of faces, and $\mu_{\text{False}} = 0.4873850283166667s \rightarrow 0.49s \pm 0.05s$ for wrong classification, on an amd64 system with Linux kernel of `5.17.7-300.fc36.x86_64` \rightarrow `5.17.7` inside virtual environment of Python 3.10.4, image container (Toolbox), and udev rules of 10% CPU utilization, and further specifications given by Listings 3.

```

1 $ lscpu
2 Architecture:          x86_64
3   CPU op-mode(s):      32-bit, 64-bit
4   Address sizes:        39 bits physical, 48 bits virtual
5   Byte Order:           Little Endian
6 CPU(s):                8
7   On-line CPU(s) list: 0-7
8 Vendor ID:              GenuineIntel
9   Model name:           Intel(R) Core(TM) i5-10210U CPU @ 1.60GHz
10  CPU family:            6
11  Model:                 142
12  Thread(s) per core:    2
13  Core(s) per socket:    4
14  Socket(s):             1
15  Stepping:              12
16  CPU(s) scaling MHz:    19%
17  CPU max MHz:           4200.0000
18  CPU min MHz:           400.0000
19  BogomIPS:              4199.88
20  Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mc
21  a cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss
22  ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art
23  arch_perfmon pebs bts rep_good nopl xtopology nonstop_
24  tsc cpuid aperfmperf pni pclmulqdq dtes64 monitor ds_cp
25  l vmx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4_1
26  sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsav
27  e avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault
28  epb invpcid_single ssbd ibrs ibpb stibp ibrs_enhanced
29  tpr_shadow vnmi flexpriority ept vpid ept_ad fsgsbase t
30  sc_adjust bmi1 avx2 smep bmi2 erms invpcid mpx rdseed a
31  dx smap clflushopt intel_pt xsaveopt xsavec xgetbv1 xsa
32  ves dtherm ida arat pln pts hwp hwp_act_window hwp_epp
33  md_clear flush_lld arch_capabilities
34 Virtualization features:
35   Virtualization:       VT-x
36 Caches (sum of all):
37   L1d:                  128 KiB (4 instances)
38   L1i:                  128 KiB (4 instances)
39   L2:                   1 MiB (4 instances)
40   L3:                   6 MiB (1 instance)
41 NUMA:
42   NUMA node(s):         1
43   NUMA node0 CPU(s):    0-7
44 Vulnerabilities:
45   Itlb multihit:        KVM: Mitigation: VMX disabled
46   L1tf:                 Not affected
47   Mds:                  Not affected
48   Meltdown:             Not affected
49   Spec store bypass:     Mitigation; Speculative Store Bypass disabled via prctl
50   Spectre v1:           Mitigation; usercopy/swapgs barriers and __user pointer
51   sanitization
52   Spectre v2:           Mitigation; Enhanced IBRS, IBPB conditional, RSB fillin
53   g
54   Srbds:                Mitigation; TSX disabled
55   Tsx async abort:      Not affected
56
57 $ dmidecode
58 Handle 0x0004, DMI type 4, 48 bytes
59 Processor Information
60   Socket Designation: U3E1
61   Type: Central Processor
62   Family: Core i5
63   Manufacturer: Intel(R) Corporation
64   ID: EC 06 08 00 FF FB EB BF
65   Signature: Type 0, Family 6, Model 142, Stepping 12
66   Flags:
67     FPU (Floating-point unit on-chip)
68     VME (Virtual mode extension)
69     DE (Debugging extension)
70     PSE (Page size extension)
71     TSC (Time stamp counter)
72     MSR (Model specific registers)
73     PAE (Physical address extension)

```

```

74     MCE (Machine check exception)
75     CX8 (CMPXCHG8 instruction supported)
76     APIC (On-chip APIC hardware supported)
77     SEP (Fast system call)
78     MTRR (Memory type range registers)
79     PGE (Page global enable)
80     MCA (Machine check architecture)
81     CMOV (Conditional move instruction supported)
82     PAT (Page attribute table)
83     PSE-36 (36-bit page size extension)
84     CLFSH (CLFLUSH instruction supported)
85     DS (Debug store)
86     ACPI (ACPI supported)
87     MMX (MMX technology supported)
88     FXSR (FXSAVE and FXSTOR instructions supported)
89     SSE (Streaming SIMD extensions)
90     SSE2 (Streaming SIMD extensions 2)
91     SS (Self-snoop)
92     HTT (Multi-threading)
93     TM (Thermal monitor supported)
94     PBE (Pending break enabled)
95     Version: Intel(R) Core(TM) i5-10210U CPU @ 1.60GHz
96     Voltage: 0.8 V
97     External Clock: 100 MHz
98     Max Speed: 8300 MHz
99     Current Speed: 2574 MHz
100    Status: Populated, Enabled
101    Upgrade: Socket BGA1528
102    L1 Cache Handle: 0x0005
103    L2 Cache Handle: 0x0006
104    L3 Cache Handle: 0x0007
105    Serial Number: To Be Filled By O.E.M.
106    Asset Tag: To Be Filled By O.E.M.
107    Part Number: To Be Filled By O.E.M.
108    Core Count: 4
109    Core Enabled: 4
110    Thread Count: 8
111    Characteristics:
112        64-bit capable
113        Multi-Core
114        Hardware Thread
115        Execute Protection
116        Enhanced Virtualization
117        Power/Performance Control
118
119    Handle 0x0005, DMI type 7, 27 bytes
120        Cache Information
121        Socket Designation: L1 Cache
122        Configuration: Enabled, Not Socketed, Level 1
123        Operational Mode: Write Back
124        Location: Internal
125        Installed Size: 0 kB
126        Maximum Size: 0 kB
127        Supported SRAM Types:
128        Synchronous
129        Installed SRAM Type: Synchronous
130        Speed: Unknown
131        Error Correction Type: Parity
132        System Type: Unified
133        Associativity: 8-way Set-associative
134
135    Handle 0x0006, DMI type 7, 27 bytes
136        Cache Information
137        Socket Designation: L2 Cache
138        Configuration: Enabled, Not Socketed, Level 2
139        Operational Mode: Write Back
140        Location: Internal
141        Installed Size: 0 kB
142        Maximum Size: 0 kB
143        Supported SRAM Types:
144        Synchronous
145        Installed SRAM Type: Synchronous
146        Speed: Unknown

```

```

147 Error Correction Type: Single-bit ECC
148 System Type: Unified
149 Associativity: 4-way Set-associative
150
151 Handle 0x0007, DMI type 7, 27 bytes
152 Cache Information
153 Socket Designation: L3 Cache
154 Configuration: Enabled, Not Socketed, Level 3
155 Operational Mode: Write Back
156 Location: Internal
157 Installed Size: 0 kB
158 Maximum Size: 0 kB
159 Supported SRAM Types:
160 Synchronous
161 Installed SRAM Type: Synchronous
162 Speed: Unknown
163 Error Correction Type: Multi-bit ECC
164 System Type: Unified
165 Associativity: 12-way Set-associative
166
167 Handle 0x0008, DMI type 10, 6 bytes
168 On Board Device Information
169 Type: Video
170 Status: Enabled
171 Description: Video Graphics Controller
172
173 Handle 0x000D, DMI type 16, 23 bytes
174 Physical Memory Array
175 Location: System Board Or Motherboard
176 Use: System Memory
177 Error Correction Type: None
178 Maximum Capacity: 32 GB
179 Error Information Handle: No Error
180 Number Of Devices: 2
181
182 Handle 0x000E, DMI type 17, 40 bytes
183 Memory Device
184 Array Handle: 0x000D
185 Error Information Handle: No Error
186 Total Width: 64 bits
187 Data Width: 64 bits
188 Size: 8 GB
189 Form Factor: SODIMM
190 Set: None
191 Locator: ChannelA-DIMM0
192 Bank Locator: BANK 0
193 Type: DDR4
194 Type Detail: Synchronous
195 Speed: 2667 MT/s
196 Manufacturer: Transcend
197 Serial Number: 00005907
198 Asset Tag: 9876543210
199 Part Number: JM2666HSG-8G
200 Rank: 1
201 Configured Memory Speed: 2667 MT/s
202 Minimum Voltage: 1.5 V
203 Maximum Voltage: 1.5 V
204 Configured Voltage: 1.2 V
205
206 Handle 0x000F, DMI type 17, 40 bytes
207 Memory Device
208 Array Handle: 0x000D
209 Error Information Handle: No Error
210 Total Width: 64 bits
211 Data Width: 64 bits
212 Size: 4 GB
213 Form Factor: SODIMM
214 Set: None
215 Locator: ChannelB-DIMM0
216 Bank Locator: BANK 2
217 Type: DDR4
218 Type Detail: Synchronous
219 Speed: 3200 MT/s

```

```

220 Manufacturer: Micron
221 Serial Number: 00000000
222 Asset Tag: 9876543210
223 Part Number: 4ATF51264HZ-3G2J1
224 Rank: 1
225 Configured Memory Speed: 2667 MT/s
226 Minimum Voltage: 1.5 V
227 Maximum Voltage: 1.5 V
228 Configured Voltage: 1.2 V
229
230 Handle 0x0011, DMI type 20, 35 bytes
231 Memory Device Mapped Address
232 Starting Address: 0x000000000000
233 Ending Address: 0x001FFFFFFFFF
234 Range Size: 8 GB
235 Physical Device Handle: 0x000E
236 Memory Array Mapped Address Handle: 0x0010
237 Partition Row Position: Unknown
238 Interleave Position: 1
239 Interleaved Data Depth: 1
240
241 Handle 0x0012, DMI type 20, 35 bytes
242 Memory Device Mapped Address
243 Starting Address: 0x000000000000
244 Ending Address: 0x000FFFFFFFFF
245 Range Size: 4 GB
246 Physical Device Handle: 0x000F
247 Memory Array Mapped Address Handle: 0x0010
248 Partition Row Position: Unknown
249 Interleave Position: 2
250 Interleaved Data Depth: 1

```

Listing 3: Specification of system from `lscpu` and `dmidecode`

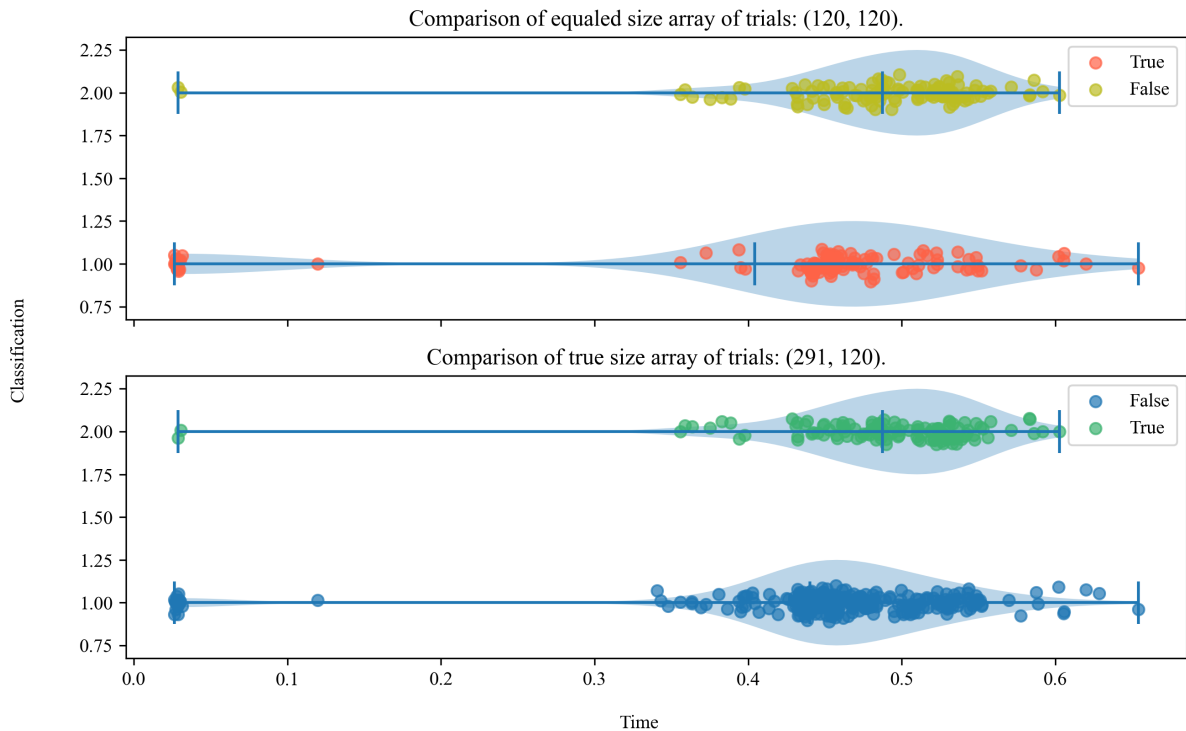


Figure 10: Time trials of face recognition-based attendance checking and security system.

The values collected, exhibited by Figure 10, correspond to the specifications of the system presented by Listings above, since the processing of the data depends on the computation power

of the system (Vopson, 2019; Vopson, 2022). And factoring the udev rules implemented in the system, that reduces the processing speed to at least $30\% \pm 20\%$, as well as the extra processes for data collection, this includes the call of `time.process_command()` method to measure Δt convert as `stdout` redirected (`>>`) to a file for data collection, where $t = 0$ was redeclared every loop as `decimal` datatype, the performance of the algorithm is above expectation.

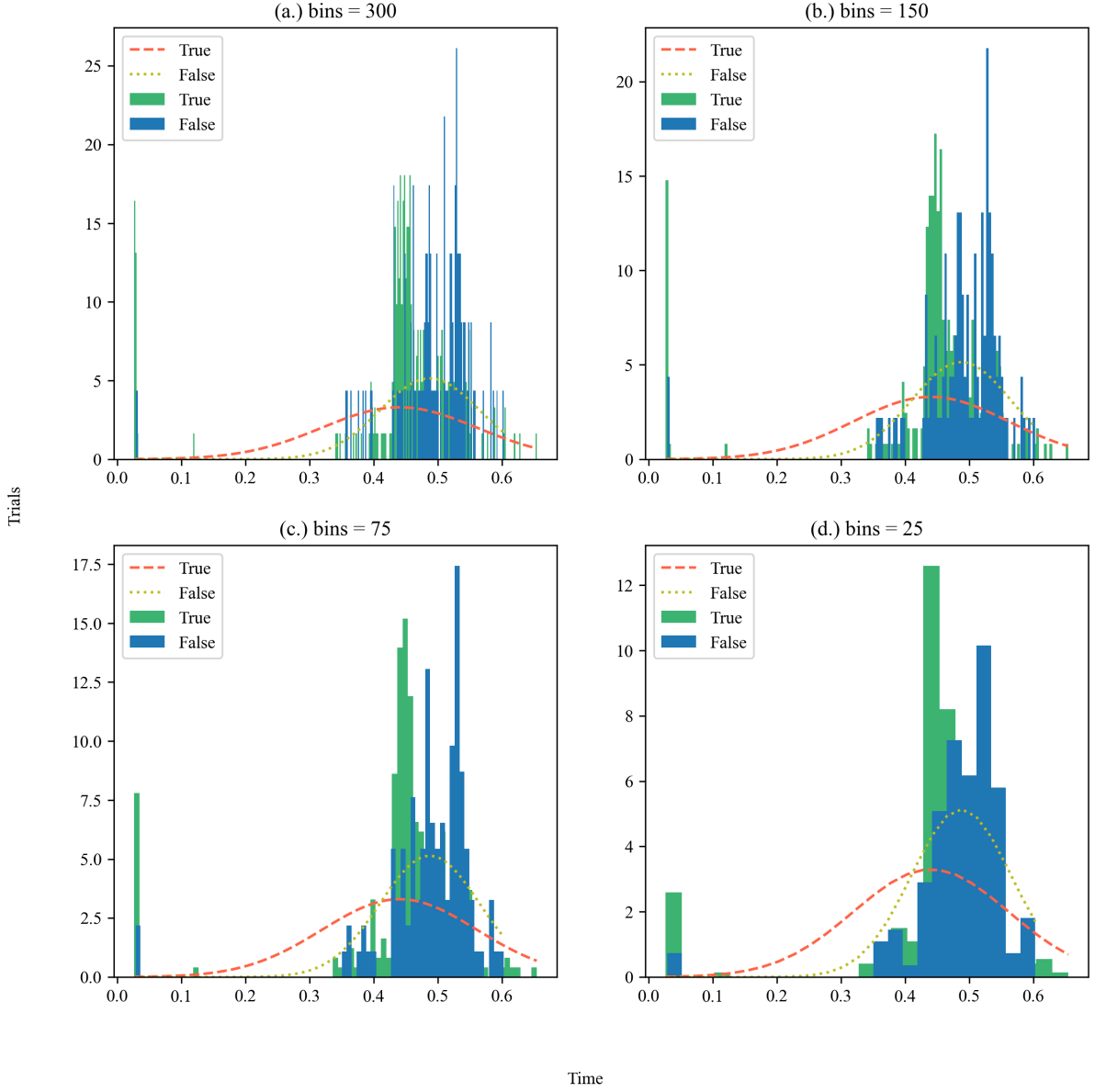


Figure 11: Time distributions of trials, starting from top-right, $bins = [300, 150, 75, 25]$.

However, false classifications are also in significant amount which comprises $41.23\% \pm 1.000\%$ ($40\% \pm 1\%$) of the data, as presented by Figure 11. This is due to the early stages of the model, since the machine learning models are heavily dependent on the training data as well as

new inputs (Jiang *et al.*, 2017, Shrestha & Mahmood, 2019), it can be reasoned that the model did not managed to generate enough amount of weight per neurons and synapses (Shrestha & Mahmood, 2019) to determine the best path in the layers for optimal output, since the input and data it requires for better classification and development of neural networks were not met.

And as opposed to common belief and perception, the efficiency and ability of model to correctly recognize faces is not time-dependent, since the model comparatively analyzes the encoding of faces, in reductive manner, thus allowing for fast and efficient recognition, in contrary to the depiction in science fictions, and other models, that a comparatively analyzes the input, in holistic manner.

It is also noted that algorithm creates two type of error, first is the Type I error, this occurs when the algorithm classified an unregistered face as face of a registered student, and the Type II error, if the algorithm classified a registered student as unknown. These type of errors were not explored, and was classified as False. This occurrence is caused by the confusion of facial features due to the almost similar samples, and due to the early stage of the model, as it did not have the sufficient amount of weight of neurons and synapses to correctly identify the facial features of each student, thus not giving the best output expected.

Nevertheless, a mean time of $\mu_t = 0.47s \pm 0.05s$ classification time on a computing power-limited system, the performance of the model can be considered above average. Moreover, the model can be transformed from its current state of unsupervised face recognition model, to a partially supervised model, whereas the model would prompt for confirmation upon confusion of inputs.

This allows the machine learning models of face recognition suitable for use in attendance checking and security systems application, considering that the model can be configured to improve in unsupervised manner in the later stages.

Regression Model of Prices of Arduino

To model the cost efficiency of the systems, the researchers devised a linear regression model, with $x := \text{demand}$, and $y := \ln(\text{price})e + 2$ since price and demand has a perfect correlation, as shown by Pearson's Correlation Coefficient, $R = \frac{\sum(x-m_x)(y-m_y)}{\sqrt{\sum(x-m_x)^2 \sum(y-m_y)^2}} = 1.0 \pm$

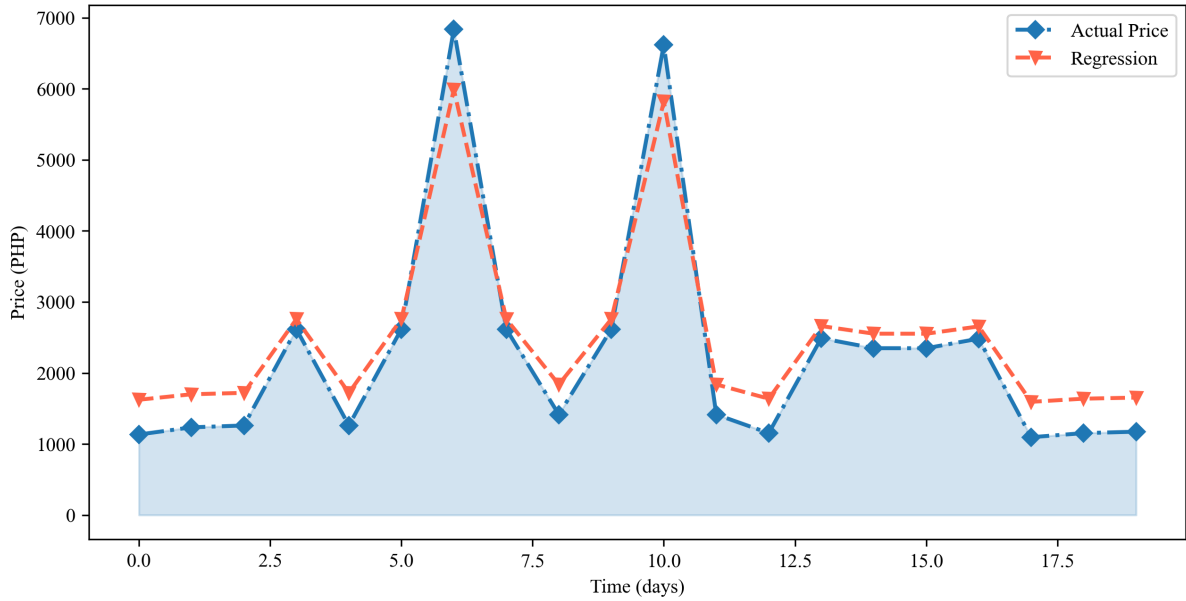


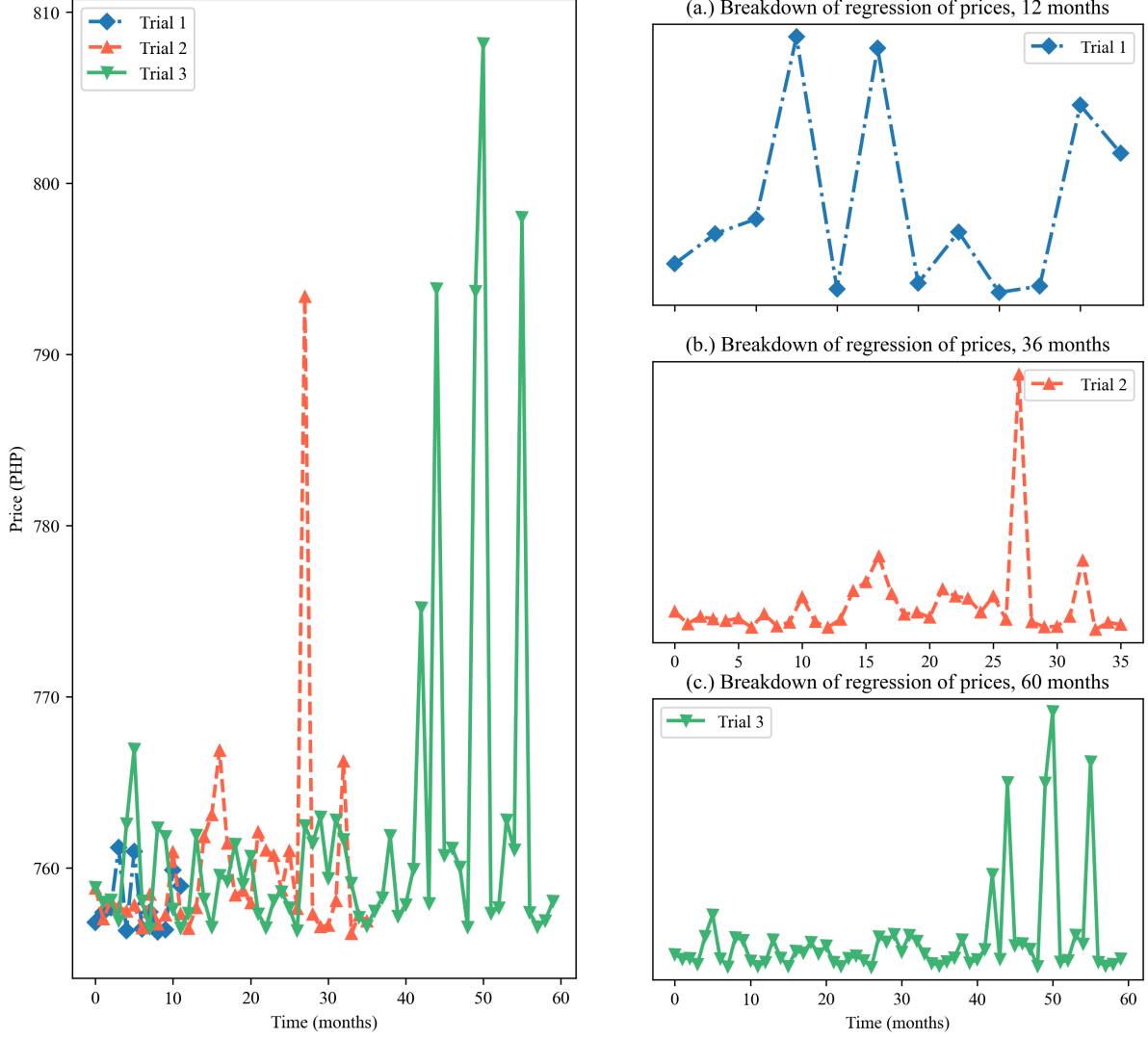
Figure 12: Regression model of prices of Arduino from fluctuations of price from March 03, 2021 to May 17, 2022.

0.1 (performed by `scipy.stats.pearsonr(x, y)` provided by SciPy (`scipy`)), shows a high precision and accuracy of the regression model, as presented by Figure 12.

Many individuals in the society are tend to think that cutting edge technologies are of high prices, rather unaffordable, and expensive to maintain, that can be explained by the socio-cultural background of an individual determined by social-learning interactions.

The models of basic perception of people towards specific matter depends on the social stereotype (Judd & Park, 1993), which can be inaccurate and flawed (val Tilburg, Igou &, Panjwani, 2022), but still contributes significantly to culture of an individual that plays a crucial role in the fast judgment, decision making, and perception towards specific matter (val Tilburg, Igou &, Panjwani, 2022; DiMaggio, 1997). Moreover, the perception towards subject matter varies significantly from one person to another regardless of being with the same stereotype, since different individuals process and understand culture differently via different social-learning interactions, thus creating a difference before passing the learned information from one another, as only the individual can only be the source of cultural data (Handwerker, 2001).

Hence the perception of individuals in the society towards the expense of modern tech-



(a) Combination of the regression model.

(b) Breakdown of the regression model.

Figure 13: Regression of prices in timeframes of: 12 months, 36 months, and 60 months, with

mean of $\mu_{12months} = 798.284359 \pm 5PHP$, $\mu_{36months} = 800.018379 \pm 5PHP$, and

$$\mu_{60months} = 801.532445 \pm 5PHP.$$

nology can be attributed to the socio-cultural background. In contrary to the commonly held belief by the society, regression models of Arduino, suggests that RFID and Arduino-based systems are significantly more cost efficient than traditional methods that is in current use. Moreover, the model predicts that in the time frame of 60 months, the price of Arduino boards would decrease significantly, specifically decrease by $\mu_{decrease12months} = 20.74\% \pm 0.25\%$, $\mu_{decrease36months} = 20.84\% \pm 0.25\%$ and $\mu_{decrease60months} = 20.82\% \pm 0.25\%$, as depicted by Figure 13.

Since Arduino systems can last for long term use, with an estimated of $3 < time(years) < 10$, an 800 PHP RFID and Arduino-based lock access system is therefore more cost efficient than a 500 PHP/year* lock maintenance and replacement of parts to ensure robustness on traditional lock access systems.

Moreover, the cost can be further decreased by utilizing other less expensive Arduino-based systems, it compromises the functionality and increases the complexity of the system, however. Nevertheless, it would still provide the most basic necessity for the use case and retain the security and convenience of the other model.

*Assuming that a branded items were used.

SUMMARY CONCLUSION AND RECOMMENDATION

Summary of Findings

To determine whether cutting edge technologies, specifically RFID and Arduino-based systems, and machine learning models of face recognition, can be utilized and implemented as lock access system, and attendance checking and security system, respectively, the researchers devised and tested the models experimentally.

Conclusions

Based on the results, it is concluded that:

1. RFID and Arduino-based systems, despite of the drawbacks caused by dependencies and other features, are more functional and efficient with an unlock and lock time of $\mu_{t_{lock}} = 2.80s \pm 0.10s$ and $\mu_{t_{unlock}} = 6.60s \pm 0.10s$, than traditional lock access system, with an average lock and unlock time of $5s < \mu_t$
2. Due to the hidden lock system and string checking algorithm of RFID and Arduino-based lock access system, common form of lock bypassing methods, as well as hacking vulnerabilities, are rendered useless, and decreased significantly, respectively, providing a more secure and robust lock access system.
3. Regardless of the early stages of the machine learning models of face recognition, and without any applied patches, the face recognition-based attendance checking and security system, exhibits an above average performance of $\approx 59\% \pm 1.0\%$ correct classifications at a mean time of $\mu_t = 0.47s \pm 0.05s$, in a limited power of machine at $30\% \pm 20\%$ core process.
4. The linear regression models devised by the researchers suggests that RFID and Arduino based systems are significantly more cost efficient than traditional methods, and predicted that would be more cost efficient of at least $20.74\% \pm 0.25\%$, $20.84\% \pm 0.25\%$, and $20.82\% \pm 0.25\%$, in the following 12 months, 36 months, and 60 months, respectively.

Recommendations

Based on the conclusions, these are recommended for further improvements:

1. Improve the other parts of the lock access system, specifically:
 - (a) LCD, by decrease the latency;
 - (b) utilize a high speed servo motor to decrease the unlock and lock time;
 - (c) avoid the use of external dependencies to increase speed of the algorithm;
 - (d) improve the module utilized for the hardware communication of the algorithm;
2. Patch the algorithm to further decrease the classification time;
3. Configure the machine learning models of face recognition to allow unsupervised learning from input;
4. Transition the face recognition algorithm in partial supervision, to decrease the error rate;
5. Further optimize the code base of the attendance checking and security system;
6. Utilize other machine learning face recognition models;
7. Further train the machine learning face recognition models;
8. Evaluate the performance of other Arduino boards for use in lock access systems;
9. Assess the perception of people towards the use of RFID technology and Arduino boards as lock access system, and machine learning models of face recognition as attendance checking and security system, in terms of:
 - (a) convenience;
 - (b) sense of security;
 - (c) and sense of privacy;
10. Compare the devised models with other commercially available models of RFID and Arduino-based lock access system, and face recognition-based attendance and security systems.