Kernel Masters Beagle Bone Black Pin Mapping					
P8 Header					
AM3358 Soc		BBB Pin Details		Kernel Masters BBB	
Pin Name	Pin No	BBB Pin No	Function Name	Function Name	Mode
	DGND	P8_1	DGND	DGND	DGND
	DGND	P8_2	DGND	DGND	DGND
gpmc_ad6	R9	P8_3	MMC1_DATA6	-	-
gpmc_ad7	T9	P8_4	MMC1_DAT7	-	
gpmc_ad2	R8	P8_5	MMC1_DAT2	-	-
gpmc_ad3	T8	P8_6	MMC1_DAT3	-	-
gpmc_advn_ale	R7	P8_7	TIMER4	-	-
gpmc_oen_ren	T7	P8_8	TIMER7	-	-
gpmc_be0n_cle	T6	P8_9	TIMER5	-	-
gpmc_wen	U6	P8_10	TIMER6	-	-
gpmc_ad13	R12	P8_11	GPIO1_13	LCD_D5	7
GPMC_AD12	T12	P8_12	GPIO1_12	LCD_D4	7
gpmc_ad9	T10	P8_13	EHRPWM2B	-	-
gpmc_ad10	T11	P8_14	GPIO0_26	SW_UP	7
gpmc_ad15	U13	P8_15	GPIO1_15	LCD_D7	7
gpmc_ad14	V13	P8_16	GPIO1_14	LCD_D6	7
gpmc_ad11	U12	P8_17	GPIO0_27	SW_DOWN	7
gpmc_clk_mux0	V12	P8_18	GPIO2_1	-	-
gpmc_ad8	U10	P8_19	EHRPWM2A	-	-
gpmc_csn2	V9	P8_20	MMC1_CMD	-	-
gpmc_csn1	U9	P8_21	MMC1_CLK	-	-
gpmc_ad5	V8	P8_22	MMC1_DAT5	-	-
gpmc_ad4	U8	P8_23	MMC1_DAT4	-	-
gpmc_ad1	V7	P8_24	MMC1_DAT1	-	-
gpmc_ad0	U7	P8_25	MMC1_DAT0	-	-
gpmc_csn0	V6	P8_26	GPIO1_29	-	-
lcd_vsync	U5	P8_27	GPIO2_22	LCD_RS	7
lcd_pclk	V5	P8_28	GPIO2_24[]	LCD_EN	7
lcd_hsync	R5	P8_29	GPIO2_23[]	LCD_RW	7
lcd_ac_bias_en	R6	P8_30	GPIO2_25[]	KEY_PAD_8	7
lcd_data14	V4	P8_31	UART5_CTSN	USER_LED	7
lcd_data15	T5	P8_32	UART5_RTSN	USER_SW	7
lcd_data13	V3	P8_33	UART4_RTSN	BUZZER	7
lcd_data11	U4	P8_34	UART3_RTSN	-	-
lcd_data12	V2	P8_35	UART4_CTSN	-	-
lcd_data10	U3	P8_36	UART3_CTSN	-	-
lcd_data8	U1	P8_37	UART5_TXD	UART5_TXD	-
lcd_data9	U2	P8_38	UART5_RXD	UART5_RXD	-

lcd_data6	T3	P8_39	GPIO2_12	KEY_PAD_6	7
lcd_data7	T4	P8_40	GPIO2_13	KEY_PAD_7	7
lcd_data4	T1	P8_41	GPIO2_10	KEY_PAD_4	7
lcd_data5	T2	P8_42	GPIO2_11	KEY_PAD_5	7
lcd_data2	R3	P8_43	GPIO2_8	KEY_PAD_2	7
lcd_data3	R4	P8_44	GPIO2_9	KEY_PAD_3	7
lcd_data0	R1	P8_45	GPIO2_6	KEY_PAD_0	7
lcd_data1		P8_46	GPIO2_7	KEY_PAD_1	7

Kernel Masters Beagle Bone Black Pin Mapping					
P9 Header					
AM3358 Soc		BBB Pin Details		Kernel Masters Board	
Pin Name	Pin No	BBB Pin No	Function Name	Function Name	Mode
		P9_1	GND	GND	-
		P9_2	GND	GND	-
		P9_3	DC_3.3V	DC_3.3V	-
		P9_4	DC_3.3V	DC_3.3V	-
		P9_5	VDD_5V	VDD_5V	-
		P9_6	VDD_5V	VDD_5V	-
		P9_7	SYS_5V	SYS_5V	-
		P9_8	SYS_5V	SYS_5V	-
		P9_9	PWR_BUT	SW_POWER	-
RESET_OUT	A10	P9_10	SYS_RESETn	SW_RESET	-
gpmc_wait0	T17	P9_11	UART4_RXD	UART4_RXD	6
gpmc_be1n	U18	P9_12	GPIO1_28	RS485_CONTROL	7
gpmc_wpn	U17	P9_13	UART4_TXD	UART4_TXD	6
gpmc_a2	U14	P9_14	EHRPWM1A	-	
gpmc_a0	R13	P9_15	GPIO1_16	ADXL345_INT	
gpmc_a3	T14	P9_16	EHRPWM1B	-	
spi0_cs0	A16	P9_17	I2C1_SCL	I2C1_SCL	2
spi0_d1	B16	P9_18	I2C1_SDA	I2C1_SDA	2
uart1_rtsn	D17	P9_19	I2C2_SCL	CAN0_RX	2
uart1_ctsn	D18	P9_20	I2C2_SDA	CAN0_TX	2
spi0_d0	B17	P9_21	UART2_TXD	UART2_TXD	1
spi0_sclk	A17	P9_22	UART2_RXD	UART2_TXD	1
gpmc_a1	V14	P9_23	GPIO1_17	ENC_INT	
uart1_txd	D15	P9_24	UART1_TXD	UART1_TXD	0
mcasp0_ahclkx	A14	P9_25	GPIO3_21	UART1_CTL	-
uart1_rxd	D16	P9_26	UART1_RXD	UART1_RXD	0
mcasp0_fsr	C13	P9_27	GPIO3_19	UART2_CTL	-
mcasp0_ahclkr	C12	P9_28	SPI1_CS0	SPI1_CS0	3
mcasp0_fsx	B13	P9_29	SPI1_D0	SPI1_D0	3

mcasp0_axr0	D12	P9_30	SPI1_D1	SPI1_D1	3
mcasp0_aclkx	A13	P9_31	SPI1_SCLK	SPI1_SCLK	3
		P9_32	VDD_ADC	VDD_ADC	-
	C8	P9_33	AIN4	-	
		P9_34	GNDA_ADC	GNDA_ADC	-
	A8	P9_35	AIN6	-	
	B8	P9_36	AIN5	-	
	B7	P9_37	AIN2	-	
	A7	P9_38	AIN3	-	-
	B6	P9_39	AIN0	LM35	
	C7	P9_40	AIN1	POT(AIN1)	-
xdma_event_intr1	D14	P9_41	CLKOUT2	-	
mcasp0_axr1	D13	P9_42	GPIO0_7	-	
eCAP0_in_PWM0_	C18	P9_43	DGND	-	
Mcasp0_aclkr	B12	P9_44	DGND	-	
		P9_45	DGND	-	
		P9_46	DGND	-	