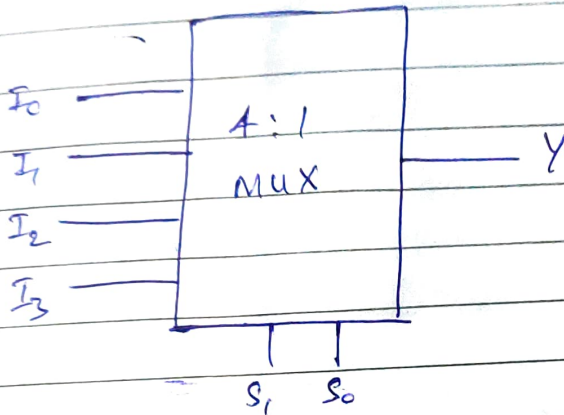


EC201 : Digital Logic Design

Remote Midsem Exam

① 202051213

∴ last four digits are $\rightarrow 1213$



∴ digits are in between 0 and 3

$$I_0 = I_1 = I_2 = I_3 = 0$$

∴ Truth Table

S_1	S_0	Output
0	0	$I_0 = 0$
0	1	$I_1 = 0$
1	0	$I_2 = 0$
1	1	$I_3 = 0$

∴ Output of Multiplexer is always 'false'.

or

$$Y = \overline{S_1} \overline{S_0} I_0 + \overline{S_1} S_0 I_1 + S_1 \overline{S_0} I_2 + S_1 S_0 I_3$$

$$Y = 0 + 0 + 0 + 0$$

$$\boxed{Y = 0}$$

2. Let the logical variable be X, Y, Z

$$f(X, Y, Z) = \sum m(0, 2, 3, 4, 5, 6)$$

the K-map for the function f is

YZ	00	01	11	10
$X=0$	1	1	1	1
$X=1$	1	1		1

possible pairings are shown by black pen

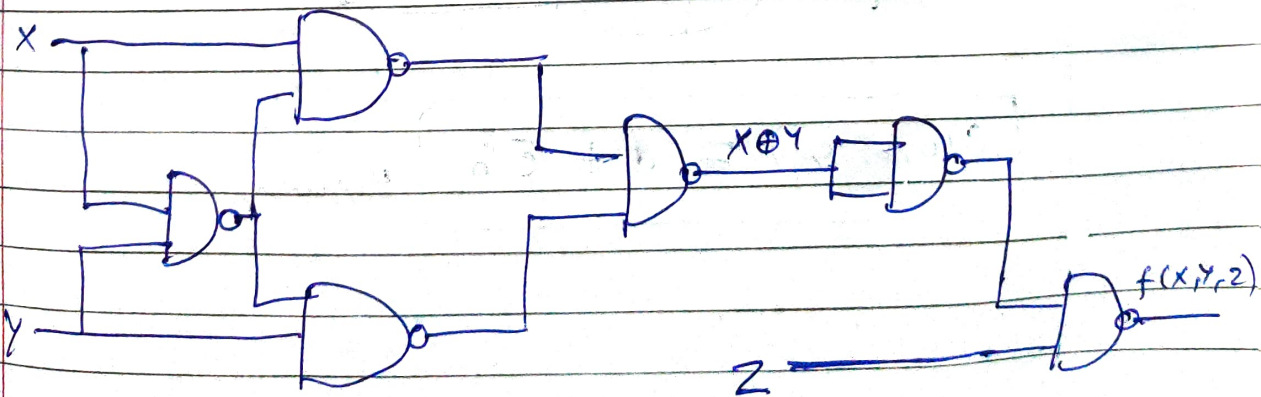
$$\begin{aligned} \therefore f(X, Y, Z) &= I + II + III \\ &= \bar{Z} + \bar{X}Y + X\bar{Y} \\ &= \bar{Z} + (X \oplus Y) \end{aligned}$$

Roll No. \rightarrow 202051213

sum = 16

sum is even

realizing circuit using NAND gates



Q.3(1)

$$f(a,b,c) = \sum m(0,1,6,7) + \sum d(3,4,5)$$

K-map

	00	01	11	10
a \ b	0	1	1	0
1	1	1	1	1

$$\cancel{f(a,b,c) = \bar{a}\bar{b} + ab}$$

$$f(a,b,c) = \bar{b} + a$$

Answer

(2) $f(a,b,c,d) = \sum m(0,1,3,5,7,8,9,11,13,15)$

	00	01	11	10
a \ b	00	01	11	10
00	1	1	1	1
01	1	1	1	1
11	1	1	1	1
10	1	1	1	1

$$f(a,b,c,d) = d + \bar{c}\bar{b}$$

Answer

Arclit Approval
202051213
Arclit Approval



4.

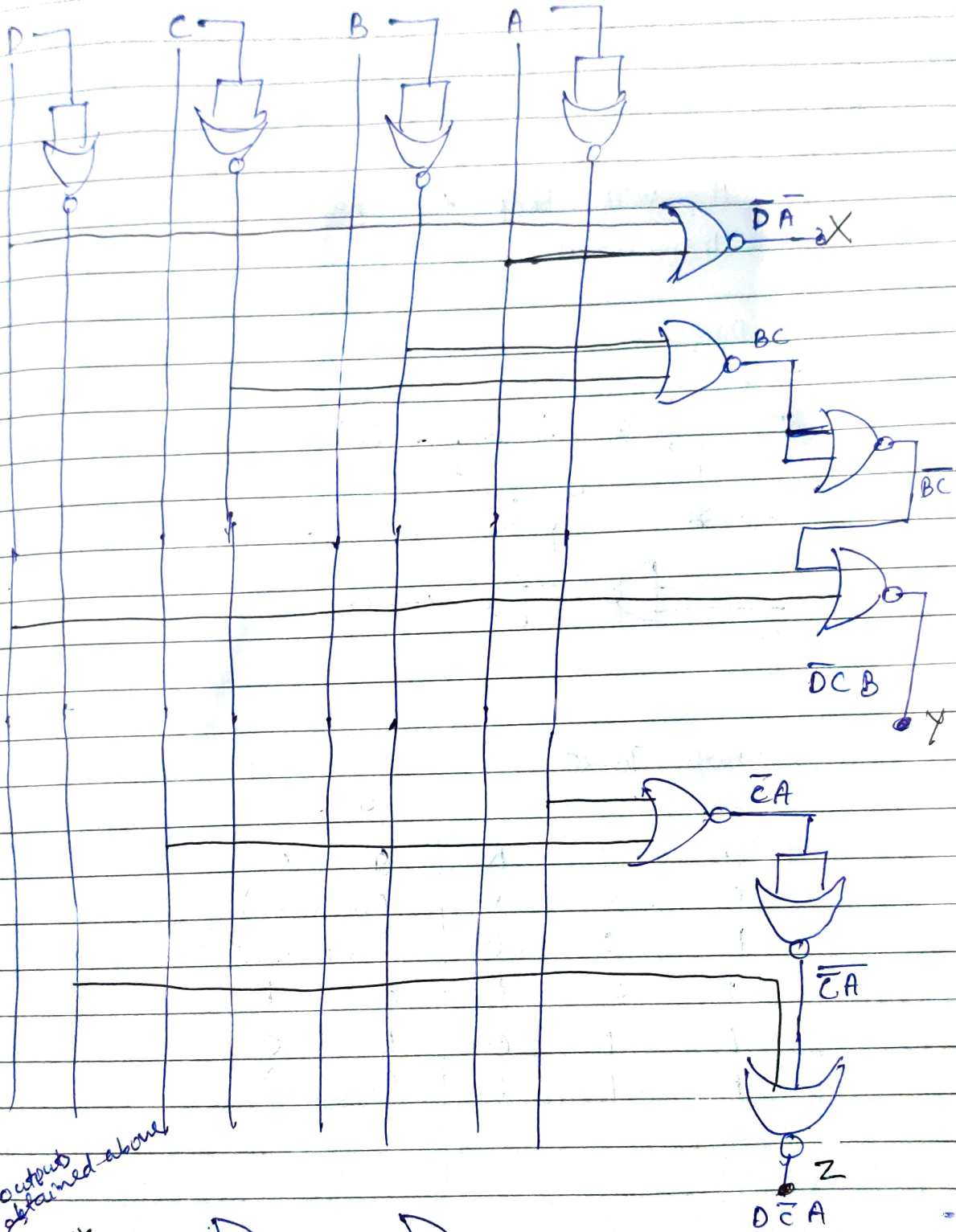
	D	C	B	A	Output
	0	0	0	0	1
Jan (31)	0	0	0	1	0
Feb (28-29)	0	0	1	0	1
Mar (31)	0	0	1	1	0
Apr (30)	0	0	0	0	1
May (31)	0	1	0	1	0
June (30)	0	1	1	0	1
July (31)	0	1	1	1	1
August (31)	0	1	1	0	0
Sept (30)	1	0	0	1	1
Oct (31)	1	0	0	0	0
Nov (30)	1	0	1	1	1
Dec (31)	1	0	1	1	1

the K-map for the table is

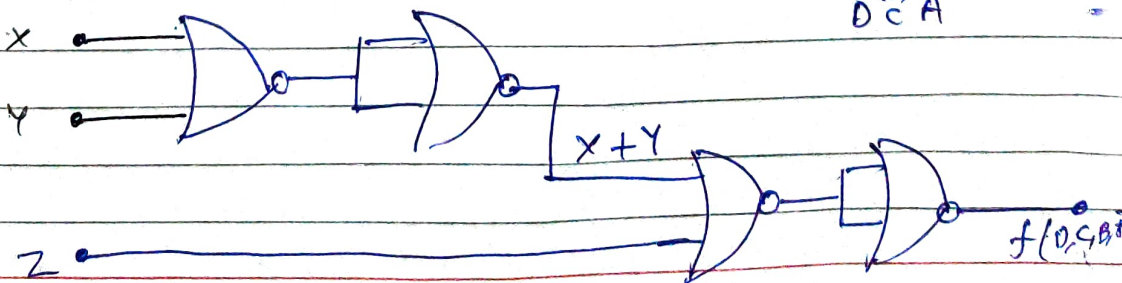
DC \ BA	00	01	11	10
00	1			1 I
01	1		1 II	1
11	X	X	X	X
10		1 II	1	

Let the function be $f(D, C, B, A)$.

$$f(D, C, B, A) = \bar{D}\bar{A} + \bar{D}CB + D\bar{C}A$$



These outputs were obtained above

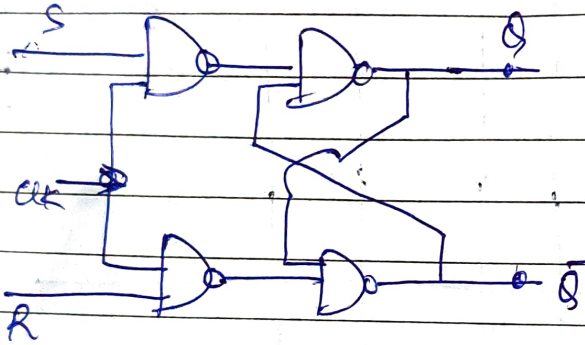


⑤ last digit is $3 \rightarrow 0011$

last two bits are 11 .

If S and R are $1, 1$ the, SR flip flop invalidates this state and it will keep the previous output memorized

Diagram



Truth Table

			Output	
clk	S	R	Q	Q'
0	X	X	(memory)	
1	0	0	(memory)	
1	0	1	0	1
1	1	0	1	0
1	1	1	(invalid)	