

LAB3: Single Cycle CPU

EE 312 Computer Architecture

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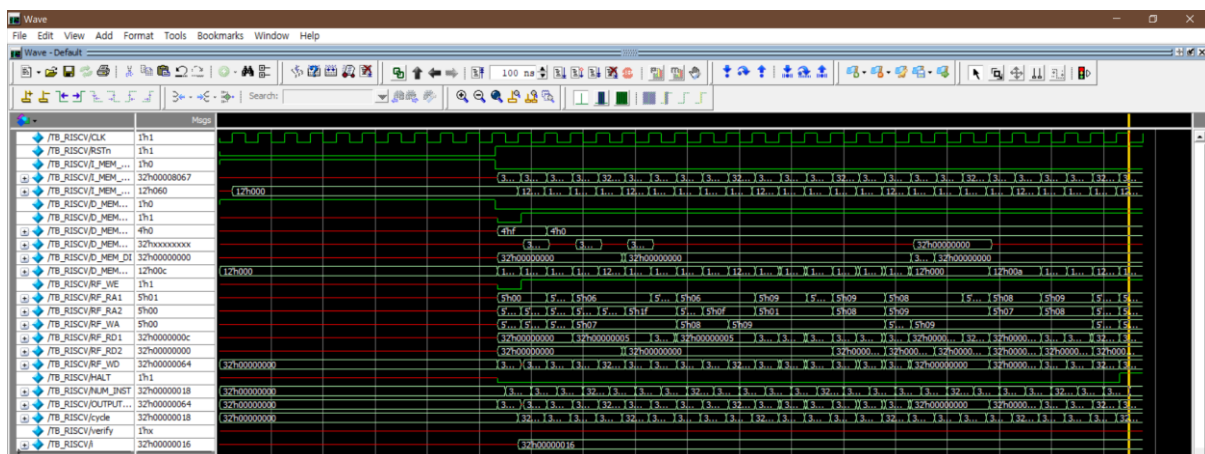
20150146 Sanghyun Kim

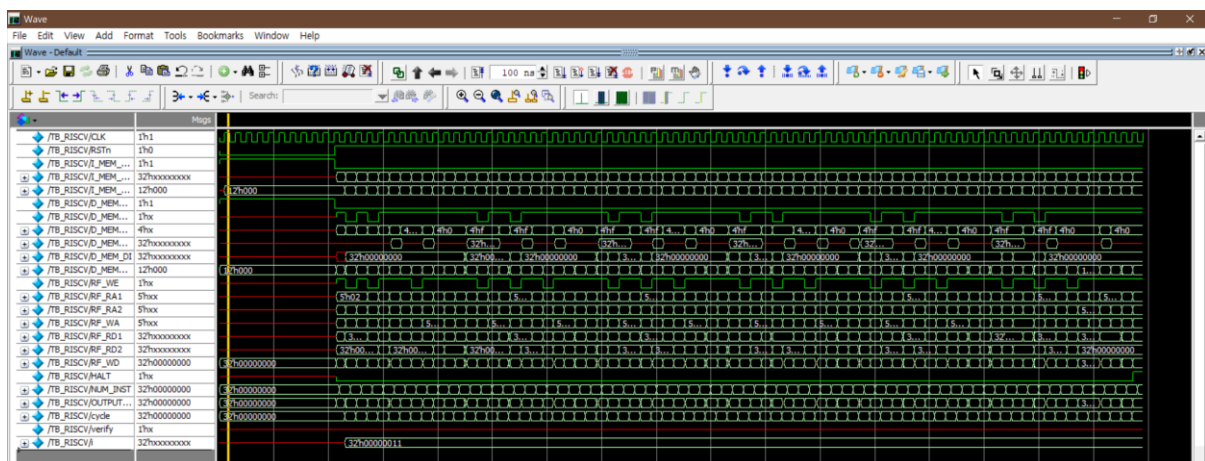
Inst test:

Transcript:

```
VSIM 16> run -all
# Test # 1 has been passed
# Test # 2 has been passed
# Test # 3 has been passed
# Test # 4 has been passed
# Test # 5 has been passed
# Test # 6 has been passed
# Test # 7 has been passed
# Test # 8 has been passed
# Test # 9 has been passed
# Test # 10 has been passed
# Test # 11 has been passed
# Test # 12 has been passed
# Test # 13 has been passed
# Test # 14 has been passed
# Test # 15 has been passed
# Test # 16 has been passed
# Test # 17 has been passed
# Test # 18 has been passed
# Test # 19 has been passed
# Test # 20 has been passed
# Test # 21 has been passed
# Test # 22 has been passed
# Finish: 24 cycle
# Success.
*** Note: $finish : C:/Users/iamarohan/Desktop/lab33/TB_RISCV_inst.v(174)
# Time: 355 ns Iteration: 1 Instance: /TB_RISCV
```

Wave:



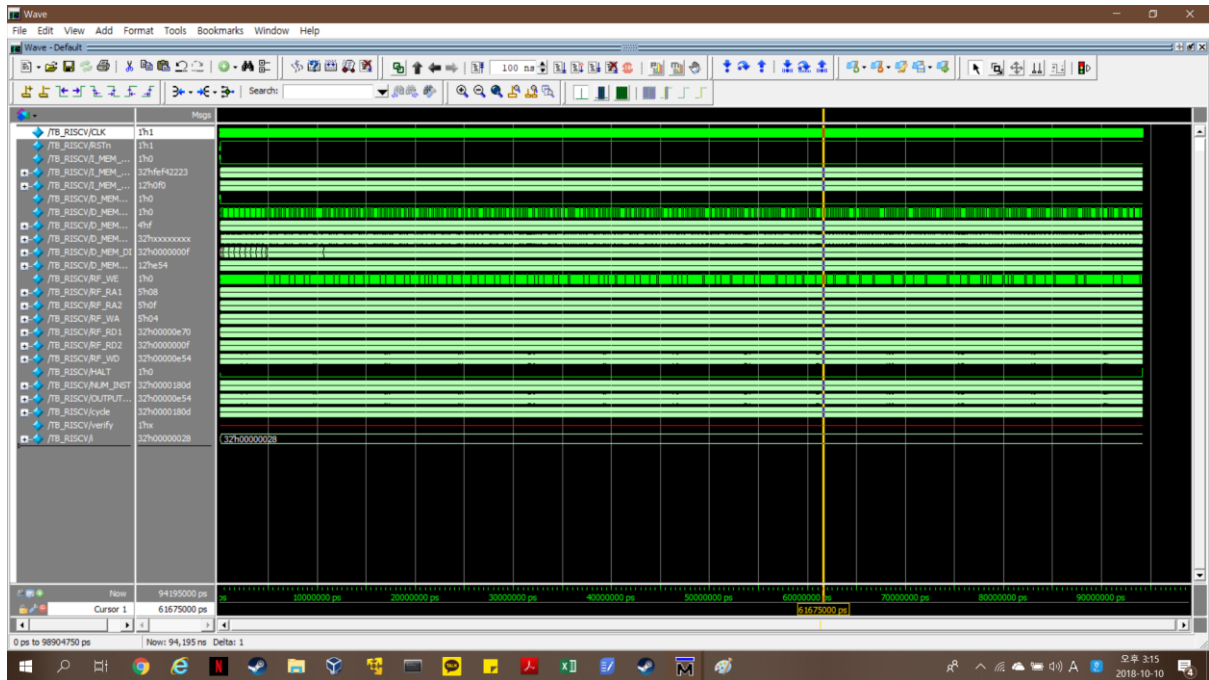


sort test:

Transcript

```
add wave -position end sim:/RISCV_TOP/*
VSIM 7> run -all
# Test #    1 has been success!
# Test #    2 has been success!
# Test #    3 has been success!
# Test #    4 has been success!
# Test #    5 has been success!
# Test #    6 has been success!
# Test #    7 has been success!
# Test #    8 has been success!
# Test #    9 has been success!
# Test #   10 has been success!
# Test #   11 has been success!
# Test #   12 has been success!
# Test #   13 has been success!
# Test #   14 has been success!
# Test #   15 has been success!
# Test #   16 has been success!
# Test #   17 has been success!
# Test #   18 has been success!
# Test #   19 has been success!
# Test #   20 has been success!
# Test #   21 has been success!
# Test #   22 has been success!
# Test #   23 has been success!
# Test #   24 has been success!
# Test #   25 has been success!
# Test #   26 has been success!
# Test #   27 has been success!
# Test #   28 has been success!
# Test #   29 has been success!
# Test #   30 has been success!
# Test #   31 has been success!
# Test #   32 has been success!
# Test #   33 has been success!
# Test #   34 has been success!
# Test #   35 has been success!
# Test #   36 has been success!
# Test #   37 has been success!
# Test #   38 has been success!
# Test #   39 has been success!
# Test #   40 has been success!
# Finish:      9408 cycle
# Success.
# ** Note: $finish      : C:/Users/iamarohan/Desktop/lab33/TB_RISCV_sort.v(193)
# Time: 94195 ns  Iteration: 1  Instance: /TB_RISCV
```

Wave:



Wave (zoomed in):

