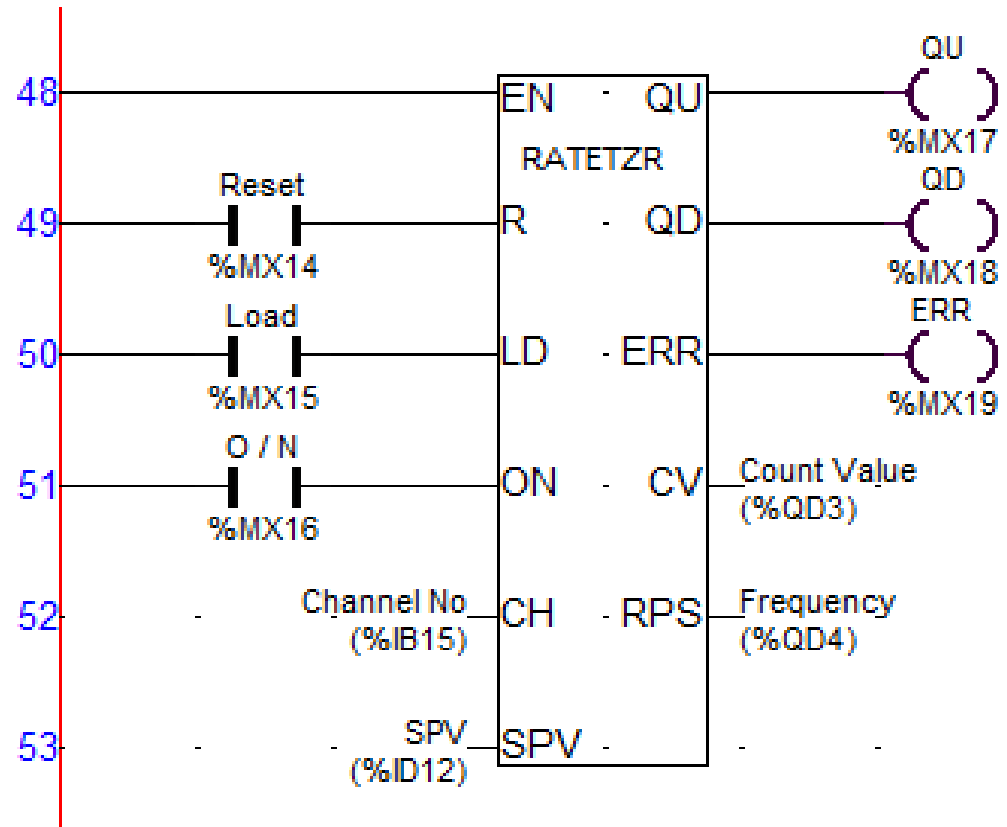


1. After adding register.



2. Description of the variable used in the block

Signal	Data type	Range	Description
EN	BOOL	0 / 1	Enables the Rate Totalizer Block.
R	BOOL	0 / 1	0 – Reset Inactive 1 – Reset Active (level triggered)
LD	BOOL	0 / 1	0 – Loads CV = 0 at Reset (Counted Pulses) 1 – Loads CV= SPV at Reset (Remaining Pulses)
O/N	BOOL	0 / 1	0 – Non Over Run Mode (Counting stops and latches when CV completes SPV pulses) 1 – Over Run Mode (Counting continues even after CV completes SPV pulses)
CH	USINT	0 / 1	CH0 - IP0/IP1 Inputs (Interrupt option 1 must be Rate Totalizer -Uni/Bi/Quad) CH1 - IP2/IP3 Inputs (Interrupt option 2 must be Rate Totalizer -Uni/Bi/Quad)
SPV	DINT	- 2147483647 to 2147483647	Preset Value of the Counter

Output :

Signal	Data type	Range	Description
QU	BOOL	0 / 1	Set when CV >= SPV
QD	BOOL	0 / 1	Set when CV <= Zero
ERR	BOOL	0 / 1	0-No Error, 1-Channel Number Error (see Error Condition table below)
CV	DINT	- 2147483647 to 2147483647	Current Value of the Counter
RPS	UDINT	0 to 3000000 4294967295	Input Frequency Measurement – Valid RPS measurement - 0.06Hz to 30000.00Hz

Table given below shows function of inputs IP0,IP1 and IP2, IP3 for different direction of Rate Totalizer block.

Mode	Channel 0		Channel 1	
	IP0	IP1	IP2	IP3
Unidirectional	Count Input pulses	Standard DI	Count Input pulses	Standard DI
Bidirectional	Input pulses	Direction sense Open – Up Counting Close – Down Counting	Input pulses	Direction sense Open – Up Counting Close – Down Counting
Quadrature	Count Input pulses with 90° phase shift		Count Input pulses with 90° phase shift	

Error Conditions

Selected CH (channel)	Condition	Error State
0	Interrupt option 1 != Rate Totalizer Uni/Bi/Quad	Set
1	Interrupt option 2 != Rate Totalizer Uni/Bi/Quad	Set
>1	-	Set