

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19
ADD	0	0	0	0	0		Dest Register				SRC1			0	0	0		SRC2		
SUB	0	0	0	1	0		Dest Register				SRC1			0	0	0		SRC2		
AND	0	0	1	0	0		Dest Register				SRC1			0	0	0		SRC2		
OR	0	0	1	1	0		Dest Register				SRC1			0	0	0		SRC2		
XOR	0	1	0	0	0		Dest Register				SRC1			0	0	0		SRC2		
ADDI	0	0	0	0	1		Dest Register				SRC						IMM7			
SUBI	0	0	0	1	1		Dest Register				SRC						IMM7			
ANDI	0	0	1	0	1		Dest Register				SRC						IMM7			
ORI	0	0	1	1	1		Dest Register				SRC						IMM7			
XORI	0	1	0	0	1		Dest Register				SRC						IMM7			
LD	0	1	0	1	0		Dest Register								PCOffset11					
ST	0	1	1	0	0		SRC								PCOffset11					
JUMP	0	1	1	1	0								Address							
PUSH	1	0	0	0	0		SRC			0	0	0	0	0	0	0	0	0	0	1
POP	1	0	0	1	0		SRC			0	0	0	0	0	0	0	0	0	0	0
BE	1	0	1	0	0		SRC1				SRC2						Address			
BNE	1	0	1	0	0		SRC1				SRC2						Address			