

Experiment Name: Implementation of 4×1 MUX
with basic gates.

Roll: 1710776121

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Course: CSE-2112

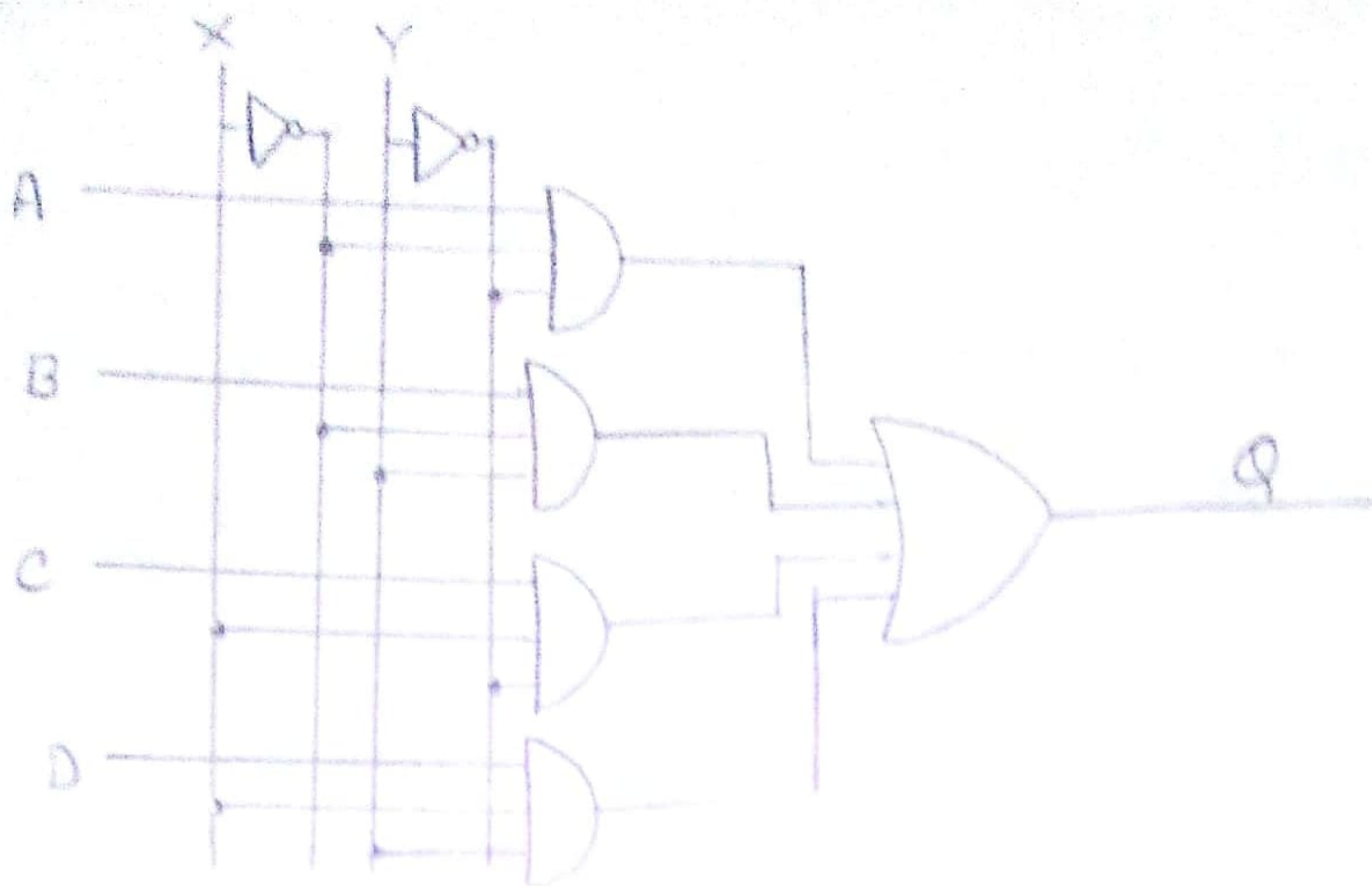
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Experiment: Implementation of 4×1 MUX with basic gates

Theory: MUX or Multiplexer is a digital logic circuit which is used to select and execute 1 line output from 2^n input line with help of n selector. For different state in selector different input line is being selected. It can be said that in MUX output is from input but it depend on selectors.

Instruments: wire, bread-board, power source, and MUX IC.

Circuit:



Truth Table:

X	Y	Q	Correct
0	0	A	✓
0	1	B	✓
1	0	C	✓
1	1	D	✓

Result and Discussion: Here we have designed a 4×1 MUX circuit with basic gates. We implemented the circuit and we got the same result we expected. So the circuit and logics are right.

Pre-caution:

1. Connect the circuit when design is complete.
2. Please check the circuit before connecting.
3. Wear shoes in the lab.
4. After finishing experiment switch off the power source.