Experiment Name: Implementation of priority Encoder (4 to 2).

Roll: 1710776121

Session: 2016-17

Course: CSE-2112
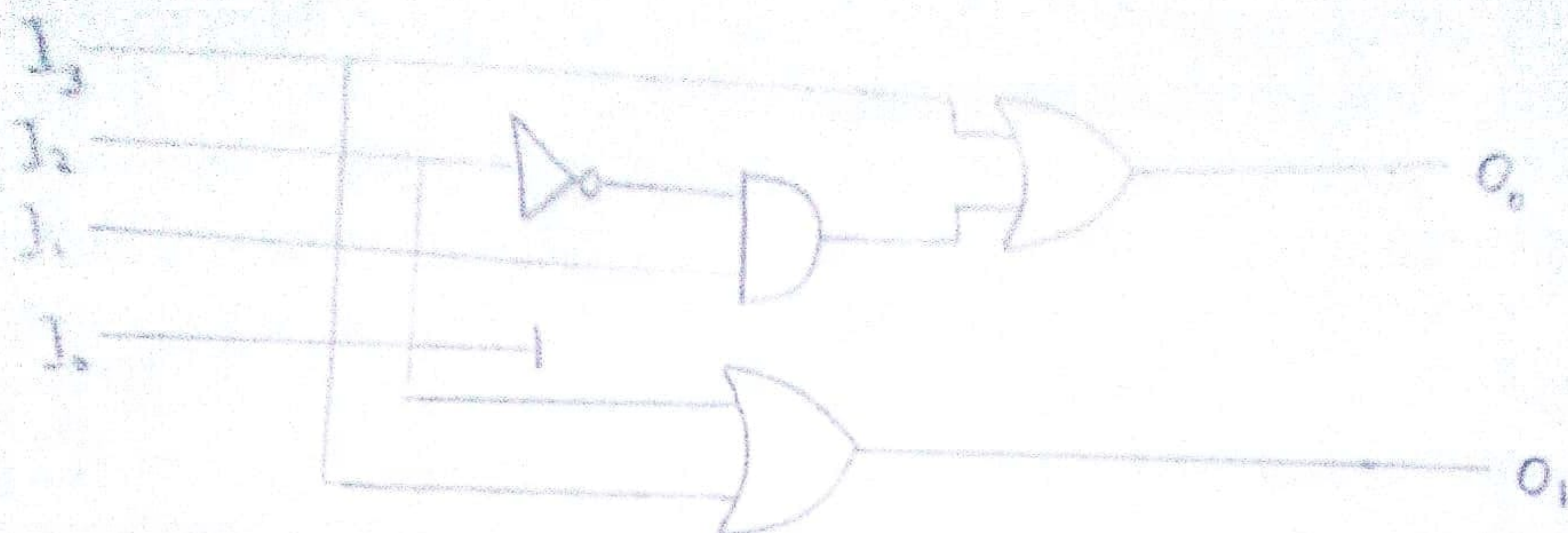
Date: 13-05-2018

Experiment Name: Implementation of priority Encoder (4 to 2)

Theory: Priority Encoder is one type of encoder. In this encoder inputs are counted as prior. In four inputs if any input becomes 1 then the rest will be don't care. That is the term where is 1 is the prior term. In priority encoder where there is no 1 the output will be don't cared.

Instrument: wire, bread-board, power source, NOT gate, AND gate, OR gate.

Circuit:

Truth Table:

I_3	I_2	I_1	I_0	Remark	O_1	O_0
0	0	0	0	✓	×	×
0	0	0	1	✓	0	0
0	0	1	×	✓	0	1
0	1	×	×	✓	1	0
1	×	×	×	✓	1	1

Result and Discussion: From the circuit we have designed the results we got are similar to the results of truth table. The results we got are valid. So the logic circuit is alright.

Pre-caustion:

1. Connect the circuit when design is complete.

2. Please check the circuit before connecting.

3. Ware shoes in the lab.

4. After finishing enperiment switch off the power source.