# EE214 Mid Semester Examination SOP (March 2021)

## Prerequisites for attending mid semester exam

1. **Hardware Requirements** (Please ensure you satisfy at least one of the options).
   **Make sure you have a backup plan for any option you choose.**

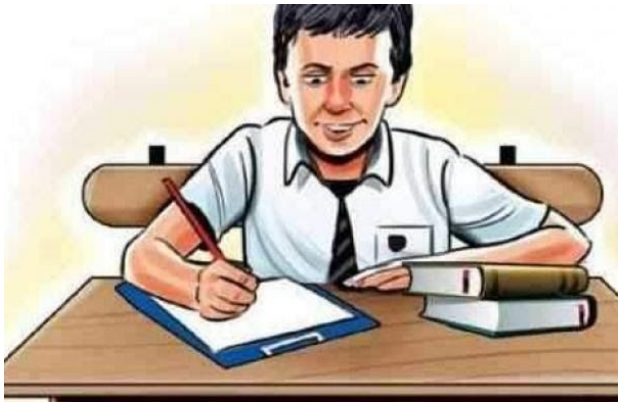
   *Option 1***:** A laptop with webcam facility.

   *Option 2:* A laptop and a Mobile phone with camera facility (In case you do not have a webcam in your laptop).

2. **Software Requirements**
   a. Quartus, ModelSim and python installed and are in working condition.

3. **Power and Network Requirements**
   a. Completely charge the batteries of your laptop before the commencement of the exam.
   b. Please move to a geographical area where the network is stable and make sure you have mobile data as backup.
   c. Have a charged power bank with you.

4. **Full view setup**
   a. Setup the camera in your mobile or Laptop in such a way that your full view is available.



Representative Image of full view

5. **Stationery**
    a. Keep a stack of **white sheets** for answering the questions. Please fill the following details on the top of the first sheet:
        i. Roll Number:
        ii. Name:
        iii. Course:
        iv. Course Code:
    b. Please enter roll number and **page** numbers legibly on the top of **every** sheet you use. (Do this activity well before the exam starts)
    **You may write the answers on both sides of every sheet, if you want. But then every side of the page should have your roll number on the top.**

**Please note:**

1. The questions are expected to be attempted using "**structural style of modelling**" only.
2. You are allowed to use the resources uploaded on MSTeams. **Accessing the internet for resources during the exam is strictly prohibited.**
3. Spend enough time to make the design compact so that it will be easy for you to describe it in VHDL and debug.
4. You are allowed to use only the components in **Gates.vhdl** and your own **VHDL descriptions** in the experiments/homework problems so far.
5. Demonstrate your **RTL** and **gate level simulation** to your TA using the given tracefile for the overall design. You are encouraged to break down your design conveniently (you may have to generate your own tracefiles for the sub-parts) to demonstrate the working of the sub parts if necessary. This will help you get partial credits.

**Step by step instructions to be followed on the day of exam**

1) Join the meeting at **10:00 AM** in the General channel of the course on MSTeams to clarify your doubts regarding the exam.
2) Join the respective channels on MS Teams by **10:15 AM** as per the student channel allocation document shared with all students.
3) Question paper and trace file will be uploaded by TAs in your respective channels.
4) Please ensure that your devices are working properly and adjust to full view by communicating with the proctoring TA.
5) Start local screen recording as a backup in the event of network outage.
6) Clarify your doubts with your channel TAs before **10:30 AM.**
7) **Image of the first** handwritten design to be uploaded by all students on Moodle. The upload link for the design will close after 45 mins after commencement of the exam (Moodle design submission portal closes exactly at **11:15 AM**)
    **Note:** Handwritten design should comprise of truth-tables, K-maps, Boolean equations, justifications for the boolean equations by inspection (as the case may be) to explain your approach along with the entities of the overall design and the decomposed components in a single zip file.

8) **Second submission** of all your project files in a single zipped file after successful demonstration to the corresponding channel TA. (**Submission of project files without demo will not be considered for the evaluation**)
9) The Moodle final submission portal will close exactly at **12:45 AM**.
10) Do not leave the online proctoring mode even if you complete the submissions in the portal. Stay online till the exam is over.

**Note:** Please do a test run with a friend (emulating TA's role) and test all related functionalities at least a day before the commencement of the exam. Report the issues immediately to TAs and discuss possible courses of action one day before the exam.