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## SANJAY MALAKAR

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### RESEARCH INTERESTS

Static Program Analysis, Automated Program Repair, Compilers (LLVM/MLIR)

### EDUCATION

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|--|--------------------------|-----------------------------------|
| <b>University of California, Riverside</b>   | <b>CA, USA</b>           | <b>September 2023 – Present</b>   |
| • Ph.D. in Computer Science   Advisor: Prof. Manu Sridharan                            |                          |                                   |
| <b>University of California, Riverside</b>   | <b>CA, USA</b>           | <b>September 2023 – June 2025</b> |
| • M.Sc. in Computer Science  |                          |                                   |
| <b>Bangladesh University of Engineering and Technology (BUET)</b>                      | <b>Dhaka, Bangladesh</b> | <b>March 2016 – February 2021</b> |
| • B.Sc. in Computer Science and Engineering   Thesis supervisor: Prof. Rifat Shahriyar |                          |                                   |

### RESEARCH EXPERIENCE

|   |   |                                   |
|---|---|-----------------------------------|
| <b>Graduate Research Assistant</b>  | <b>UC Riverside</b>                         | <b>July 2024 - Present</b>        |
| Leading independent research on program analysis and automated repair of resource management bugs.  |   |                                   |
| • Developed <b>Arodnap</b> , an end-to-end tool to fix resource leaks in Java. <ul style="list-style-type: none"><li>◦ Integrates resource-specification inference, reasons about resource wrapper lifecycles, and applies minimal-scope code transformations to generate behavior-preserving patches.</li><li>◦ Increased automated leak repair coverage from 41% (prior work) to 68% on an open-source benchmark of 285 projects.</li></ul> |   |                                   |
| <b>Compiler Research Intern</b>   | <b>Nissan North America, Inc.   CA, USA</b> | <b>June 2025 - September 2025</b> |
| Worked with the Software-Defined Vehicles team, building a <i>Python → Torch-MLIR → MLIR → LLVM/RISC-V</i> pipeline and a fused dot-product primitive targeting custom accelerator backends for on-vehicle perception workloads.  |   |                                   |
| • Designed a custom MLIR dialect/op with lowering to LLVM/RISC-V, adding an LLVM intrinsic that lowers to a custom RISC-V instruction.  |   |                                   |
| • Built a runtime simulation workflow and demonstrated consistent kernel-level throughput gains on representative GEMM workloads while maintaining correctness.   |   |                                   |
| <b>Undergraduate Thesis</b>   | <b>BUET</b>                                 | <b>March 2020 - January 2021</b>  |
| Dynamic analysis-guided repair of concurrency bugs in C/C++.  |   |                                   |
| • Built <b>RaceFixer</b> , a Clang LibTooling tool that leverages ThreadSanitizer reports and static analysis to repair data races and atomicity violations through lock-reuse synchronization.   |   |                                   |

### PROFESSIONAL EXPERIENCE

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|--|--------------------------------------|------------------------------------|
| <b>Senior Software Engineer</b>  | <b>OpenRefactory, Inc.   CA, USA</b> | <b>February 2021 – August 2023</b> |
| Contributed to Intelligent Code Repair ( <b>iCR</b> ), a static application security testing (SAST) tool for Java/Python/Go.             |                                      |                                    |
| • Designed and implemented whole-program pointer analysis with incremental re-analysis to accelerate re-scans and CI.                    |                                      |                                    |
| • Modeled threading and framework lifecycles to resolve framework-invoked callbacks and hidden call paths, improving analysis precision. |                                      |                                    |
| • Developed custom checkers for advanced bug detection, including taint analysis and null-pointer dereference.                           |                                      |                                    |
| • Led the migration from a monolith to Kubernetes-based microservices and established CI/CD for scalable, automated deployments.         |                                      |                                    |

### PUBLICATIONS

1. **Repairing Leaks in Resource Wrappers**, *2025 IEEE/ACM International Conference on Automated Software Engineering (ASE'25, Accepted)*  
**Sanjay Malakar**, Martin Kellogg, Michael D. Ernst, Manu Sridharan
2. **Developer Discussion Topics on the Adoption and Barriers of Low-Code Software Development Platforms**, *2023 Empirical Software Engineering Journal (EMSE'23, Accepted)*  
Md Abdullah Al Alamin, Gias Uddin, **Sanjay Malakar**, Sadia Afroz, Tameem Bin Haider, Anindya Iqbal [[\[preprint\]](#)]

3. **An Empirical Study of Developer Discussions on Low-Code Software Development Challenges, 2021 IEEE/ACM 18th International Conference on Mining Software Repositories (MSR'21, Accepted)**  
Md Abdullah Al Alamin, **Sanjay Malakar**, Gias Uddin, Sadia Afroz, Tameem Bin Haider, Anindya Iqbal [[preprint](#)]
4. **RaceFixer - An Automated Data Race Fixer**  
**Sanjay Malakar**, Tameem Bin Haider, Rifat Shahriar [[preprint](#)]

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#### TEACHING EXPERIENCE

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|---|--|----------------------------------|
| <b>Graduate Teaching Assistant</b>  | <b>University of California, Riverside</b> | <b>January 2025 – March 2025</b> |
| CS 180: Introduction to Software Engineering – led labs, held office hours, and graded assignments. |  |                                  |

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#### HONORS & AWARDS

- Awarded **Dean's Distinguished Fellowship** at the University of California, Riverside
- Received **Dean's Award** in Junior year from Bangladesh University of Engineering and Technology