

DASH NO.	TYPE OR MODEL	NEXT ASSY
	Avalon	P1060966

REV	DESCRIPTION OF CHANGE	APPROVED	DATE
A	Initial release		

-001 SHOWN

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March 3, 2025

Title: FPGA, STN-CONT HDW

1. Introduction

This document describes the programming details for the hardwired FPGA of the Stand Controller PCBA.

2. Device Programming Information

2.1 Target device

The target device is a Microchip MPF050T-FCVG484E.

2.2 Labeling

Labeling is not required for this device.

2.3 Programming File

Table 1: Programming File

File Name	Description	Version	Checksum (hex)
P1062761-001.stp	Programming File	4.7	8666

2.4 Programming Procedure

Program as follows:

1. Connect a suitable JTAG programmer (e.g. Microchip FLASHPRO4) to a computer which contains the JTAG programming software (e.g. Libero IDE or dedicated software for stand-alone operation).
2. Connect the JTAG programmer to the hardwired FPGA's JTAG programming connector.
3. Program the FPGA using the file shown in Table 1.
4. After programming completes, verify that the checksum matches that shown in Table 1.

2.5 Notes

1. CAUTION: STATIC SENSITIVE DEVICES. OBSERVE PRECAUTIONS FOR HANDLING.

Signature File



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Date	Time	User	Status
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