

Subject: Basic Electrical & Electronics Engg.

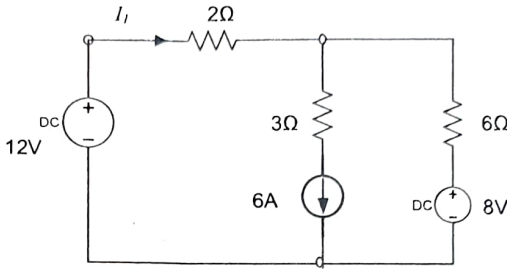
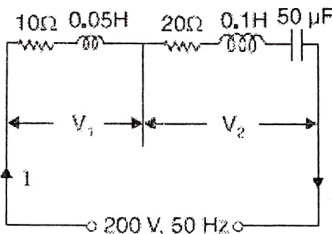
Time : 3 Hours

Subject code : EE-108

Max. Marks : 50

Note: (i) Assume the necessary data suitably, if any missing.

(ii) All questions carry equal marks.

Q.No.	Questions	Marks
Q.1.	<p>a) Derive the condition of maximum power transfer in any D.C. circuit and find the efficiency when the source is transferring maximum power to the load.</p> <p>(b) Determine the current I_1 in the circuit of given figure using Superposition theorem.</p> 	<p>5</p> <p>5</p>
Q.2.	<p>(a) In the given circuit, find the values of : (i) the current I (ii) V_1 and V_2 and (iii) Power factor. Draw the phasor diagram.</p>  <p>(b) Describe the phenomenon of resonance in parallel circuit and explain its Q factor. Why parallel resonance circuit is often regarded as rejector circuit?</p>	<p>5</p> <p>5</p>
Q.3.	<p>(a) In a 50 kVA, 1100/220 V transformer, the iron and copper losses at full load are 350 W and 425 W, respectively. Calculate the efficiency at</p> <ol style="list-style-type: none"> half load with unity power factor full load with 0.8 power factor lagging and Maximum Efficiency and the load at which maximum efficiency occurs assuming the load to be resistive. <p>(b) Draw the equivalent circuit diagram of the transformer referred to secondary side. Draw the phasor diagram for this circuit and determine voltage regulation expression for lagging p.f. load.</p>	<p>5</p> <p>5</p>

Q.4.	<p>a) What are the main parts of D C Machine ? Write the function of each part and state the material of which each part is made?</p> <p>(b) Develop an expression for the speed of a d c motor in terms of back emf and flux per pole.</p>	<p>5</p> <p>5</p>
Q.5.	<p>a) Draw and explain V-I characteristic of P-N Junction diode when it is (i) forward biased (ii) reverse biased.</p> <p>b) Drive the expression of output voltage and efficiency of full wave rectifier.</p>	<p>5</p> <p>5</p>