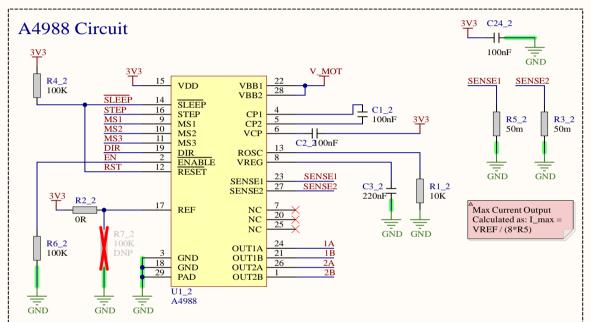
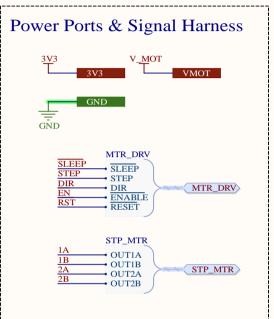
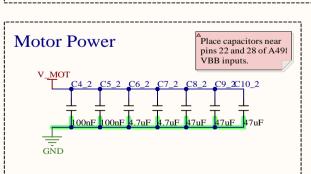
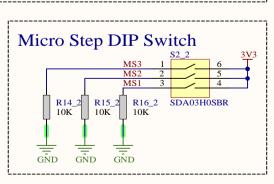


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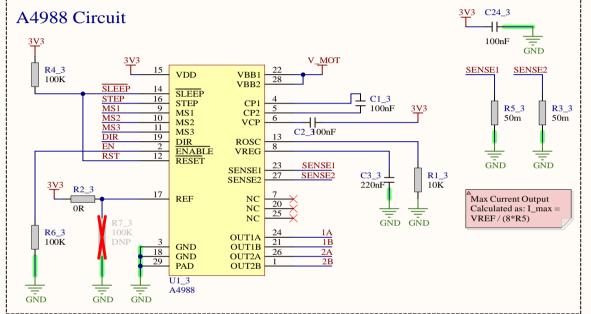


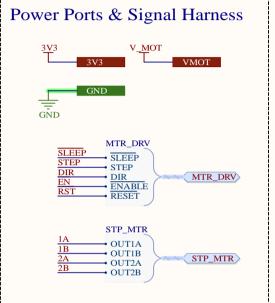


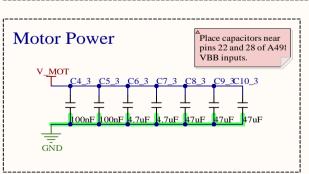
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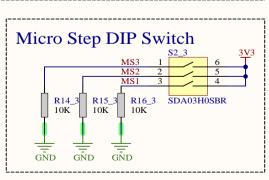
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A4988 Circuit A4988 Circuit Power Ports & Signal Harness 100nF R4_3 100K SENSE2 VDD VBB1 VBB2 SLEEP SLEEP GND C1_3 100nF SLEEP STEP MS1 MS2 MS3 DIR ENABLE RESET CP1 CP2 VCP R3 3 R5_3 50m C2_300nF ROSC VREG MTR DRV SLEEP SENSE1 SENSE2 • STEP SENSE1 SENSE2 C3_3 1 220nFT R1 3 MTR_DRV DIR **ENABLE** REF Max Current Output RESET Calculated as: I_max = VREF / (8*R5)



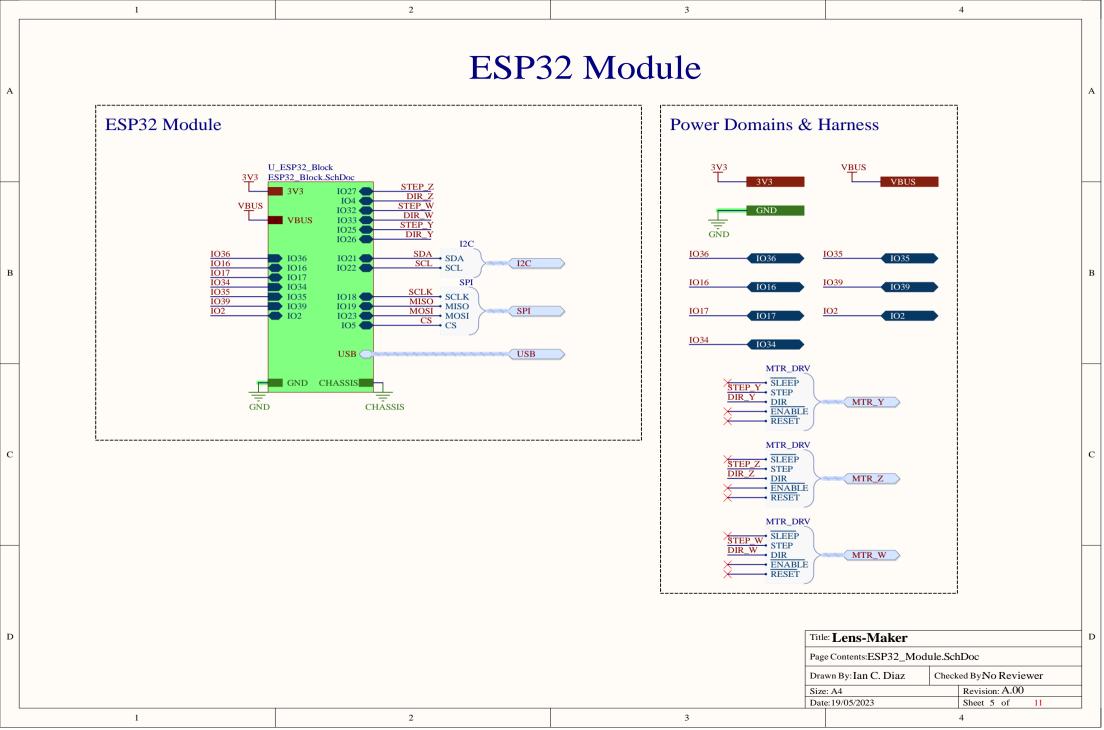


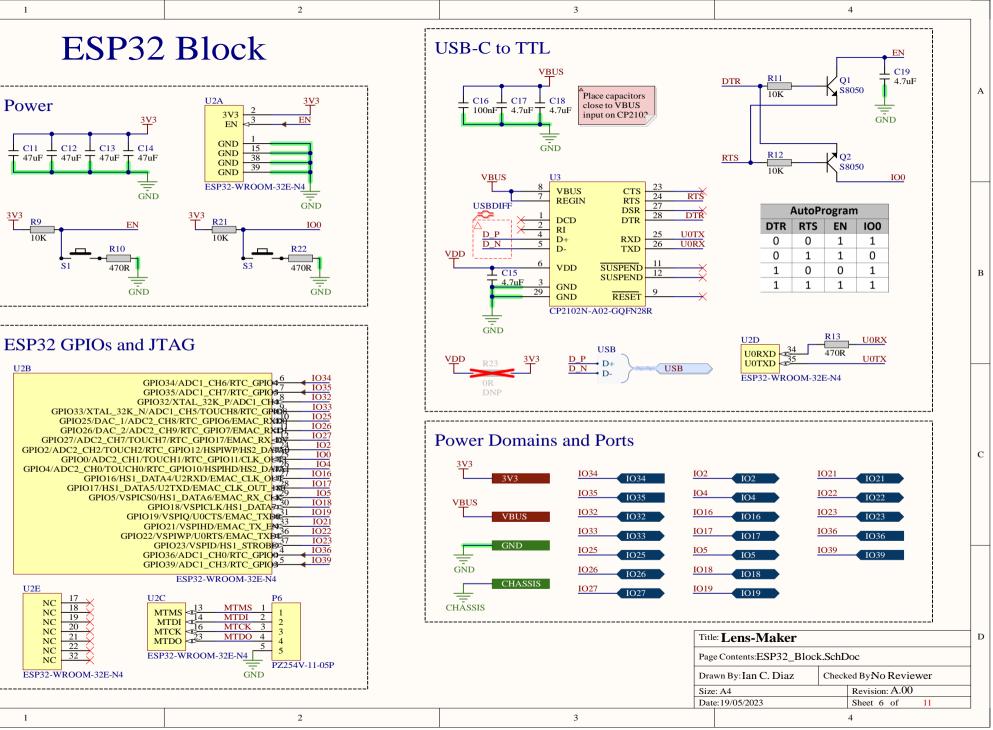




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Power

U2B

U2E

ESP32-WROOM-32E-N4

