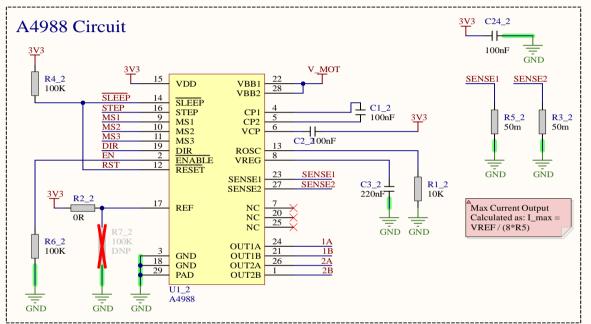
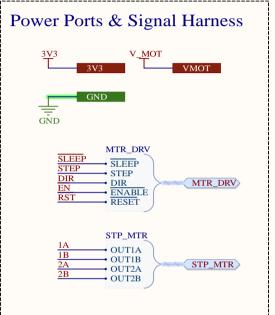
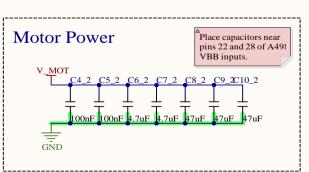
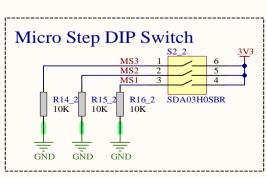


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Page Contents: A4988. Sch	Doc		
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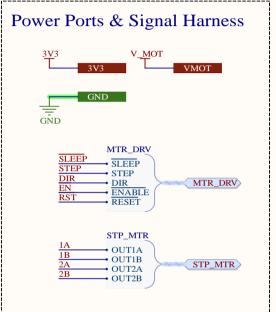


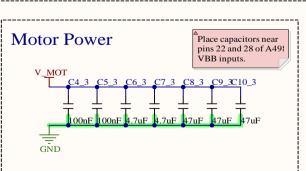
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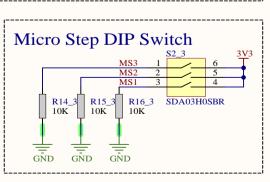
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A4988 Circuit A4988 Circuit Power Ports & Signal Harness 100nF R4_3 100K SENSE2 VDD VBB1 VBB2 SLEEP SLEEP GND C1_3 100nF SLEEP STEP MS1 MS2 MS3 DIR ENABLE RESET CP1 CP2 VCP R3 3 R5_3 50m C2_300nF ROSC VREG MTR DRV SLEEP • STEP R1 3

SENSE1 SENSE2 SENSE1 SENSE2 C3_3 1 220nFT REF Max Current Output Calculated as: I_max = VREF / (8*R5) R6_3 100K OUT1A OUT1B OUT2A OUT2B GND GND PAD 2A U1 3 A4988 — GND

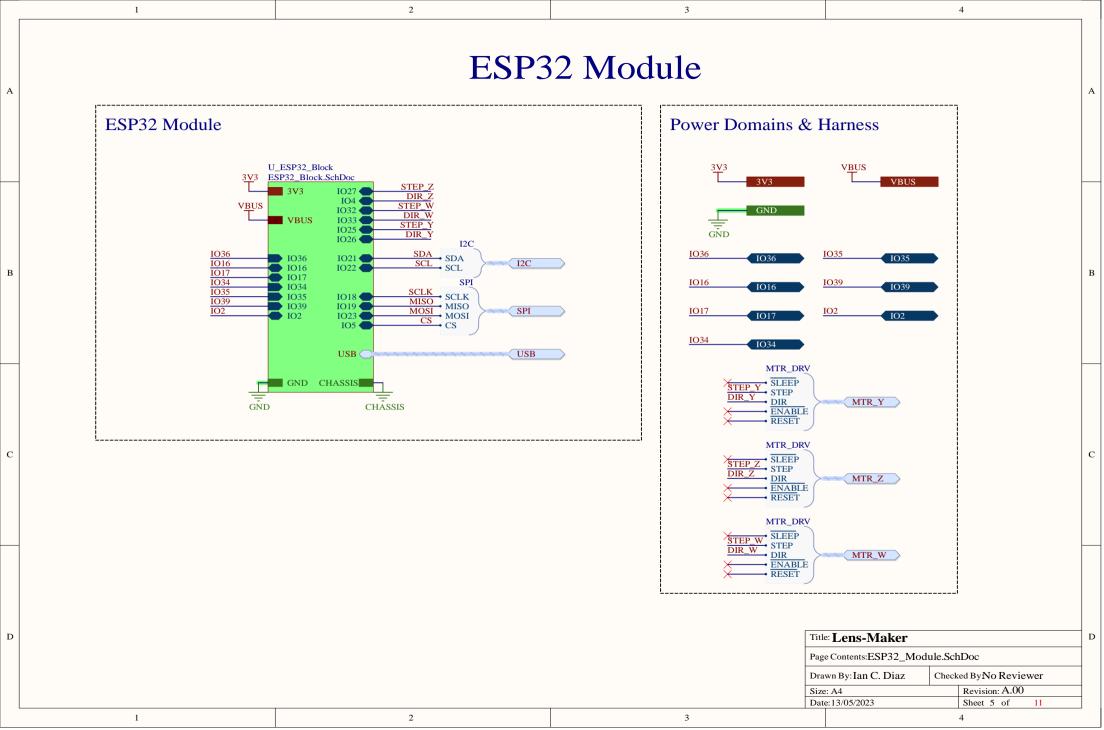


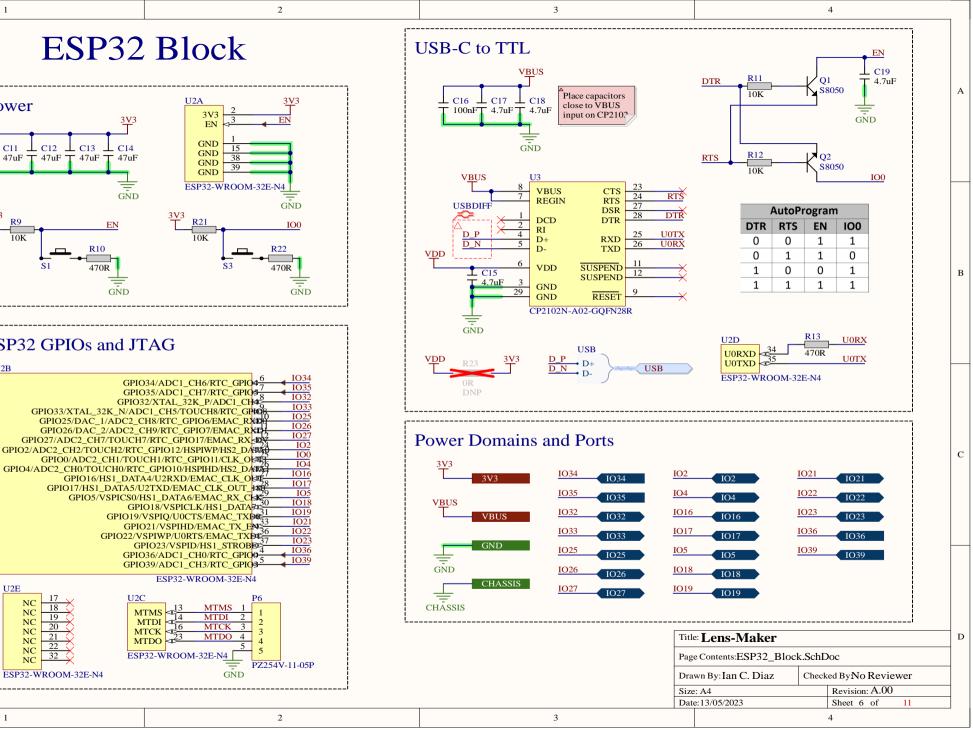




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ESP32-WROOM-32E-N4

U2E

Power

U2B

ESP32 GPIOs and JTAG

MTDI 🖘

