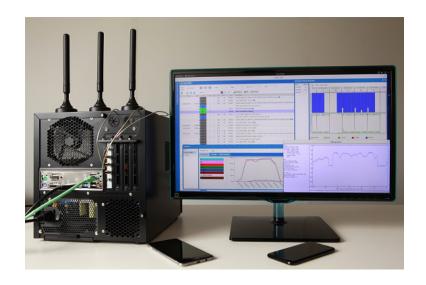


# **Application Note**

## PCIe SDR Card Calibration



Version: 2024-12-23

# Table of Contents

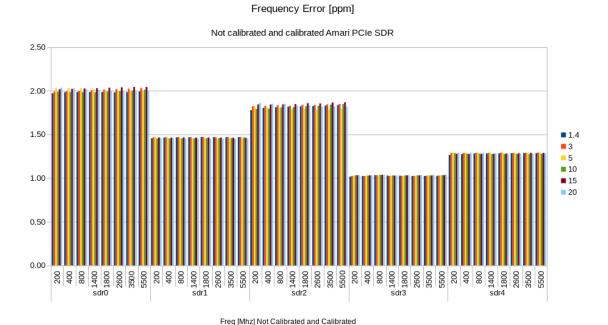
1	Introduction	. 1
<b>2</b>	Frequency Calibration	. 2
	2.1 Frequency Error Measurement	2
	<ul><li>2.2 Frequency Calibration Methodology</li><li>2.3 Frequency Calibration with GPS</li></ul>	2
	2.3 Frequency Calibration with GPS	3
3	DL Power Calibration	5
	3.2 DL Power Calibration Methodology	5
4	UL Power Calibration	
5	Additional Information	. 7

## 1 Introduction

This application note explains how to calibrate Amarisoft PCIe SDR card.

#### 2 Frequency Calibration

Amarisoft PCIe card has a maximum frequency error less than 2 ppm. The following figure depicts the measured frequency error on a sample of 5 cards.



#### 2.1 Frequency Error Measurement

Start lteenb with a 10 MHz SISO FDD cell and dl\_earfcn corresponding to Fset MHz. Connect the TX1 SDR connector to an analyzer. Set the Fset as central frequency of the signal analyzer. Read Fmeasured or Fmeasured-Fset from the signal analyzer, then compute:

In general

|Ferror|< 2 ppm

This error does not depend on the bandwidth or the frequency. It only depends on the SDR HW.

#### 2.2 Frequency Calibration Methodology

This error can be corrected by using the following command

/root/trx\_sdr/sdr\_util clock\_tune -<Ferror in ppm>

As an example, if Ferror = 1.6 ppm then the command to use is

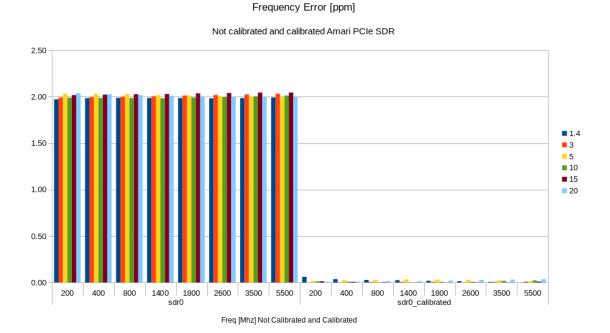
/root/trx\_sdr/sdr\_util clock\_tune -1.6

if Ferror = -1 ppm then the command to use is

/root/trx\_sdr/sdr\_util clock\_tune 1

Note that the setting is power cycle persistent. In case you have many SDRs connected to the PC, use the option -c device\_num to select the device (default = all)

After calibration, you can expect Ferror < 0.1 ppm. The following figure shows the frequency error on a calibrated and non calibrated PCIe SDR cards.



### 2.3 Frequency Calibration with GPS

Since test release version 2019-10-31, frequency calibration can be performed via GPS signal. To calibrate a SDR card, stop the lte service

service lte stop

connect a GPS antenna to the GPS SMA connector of the SDR card. Any active GPS antenna accepting a 3.3V DC supply can be used, for example:

http://www.mouser.fr/Search/ProductDetail.aspx?R=ANT-GPS-SH-SMAvirtualkey59000000virtualkey

You can check the GPS state with

./sdr\_util gps\_state

The GPS might takes a few minutes to lock.

 $Under\ /root/trx\_sdr/\ folder\ execute$ 

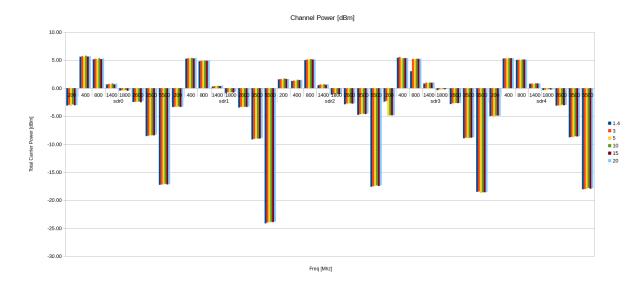
where:

<n> sdr device number
<t> GPS clock acquisition time in seconds, 30 seconds is a good value
-s option to store the resulting ppm correction to a flash memory on the card

If you use an Amarisoft product where the master sdr0 provides the clock to the other slave cards, you only need to calibrate the master card

#### 3 DL Power Calibration

The channel power is the integrated power over the cell bandwidth. The downlink power of the Amarisoft PCIe SDR card depends on the frequency and varies roughly between +5 dBm and -10 dBm for f < 3500 MHz, and can go down to -25 dBm for higher frequencies as can be viewed in the following figure.



#### 3.1 DL Power Measurement

You can measure the channel power for each frequency using the attached eNodeB config and measuring with a power meter the integrated power over the bandwidth. You can do this for each unit you want to calibrate and keep the value in a table (Channel power for 20 MHz versus Frequency).

```
for f = fmin;f<fmax;f=fdeta
    Set the frequency in the config file to f, and run enb (dl_freq_min parameter in the set the frequency and bandwidth in the power meter
    Capture the channel power from the power meter
end for</pre>
```

Note that the measurement shall be taken with a BW = 20 MHz and tx\_gain=90. Note 2 any path loss shall be added to the value read from the power meter. Note 3 The more fdelta is small the more accurate will be the interpolation.

### 3.2 DL Power Calibration Methodology

To achieve a certain channel power, you know the value for tx\_gain = 90 from the calibration array (or interpolation of the values), then you can select the right tx\_gain to achieve you target channel gain. To achieve a certain RSRP, you have to convert the RSRP value to channel power using the target bw value, then you use the method for channel power.

## 4 UL Power Calibration

In order to calibrate the UL power of the card, you can split the uplink signal to both eNodeB and power meter. Make sure you add a 3 dB attenuation in the dl path as well to have the same path loss in both directions. Then you can compare the power reported by the eNodeB, with the one reported by the power meter.

The initial value of the rx\_gain set in the configuration file is taken into account when the eNodeB computes the uplink power. But if you change the value during the execution using the rx\_gain command line interface, then you have to apply the rx\_gain delta between the new value and the one in the configuration file by yourself. We recommend not to change the rx\_gain value dynamically.

## 5 Additional Information

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