

Signal Name	FPGA Pin No.	Description
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CLK		
MAX10_CLK1_50	PIN_P11	50MHz clock input

Button		
KEY0	PIN_B8	Push-button[0]
KEY1	PIN_A7	Push-button[1]

Switch		
SW0	PIN_C10	Slide Switch[0]
SW1	PIN_C11	Slide Switch[1]
SW2	PIN_D12	Slide Switch[2]
SW3	PIN_C12	Slide Switch[3]
SW4	PIN_A12	Slide Switch[4]
SW5	PIN_B12	Slide Switch[5]
SW6	PIN_A13	Slide Switch[6]
SW7	PIN_A14	Slide Switch[7]
SW8	PIN_B14	Slide Switch[8]
SW9	PIN_F15	Slide Switch[9]

LED		
LEDR0	PIN_A8	LED[0]
LEDR1	PIN_A9	LED[1]
LEDR2	PIN_A10	LED[2]
LEDR3	PIN_B10	LED[3]
LEDR4	PIN_D13	LED[4]
LEDR5	PIN_C13	LED[5]
LEDR6	PIN_E14	LED[6]
LEDR7	PIN_D14	LED[7]
LEDR8	PIN_A11	LED[8]
LEDR9	PIN_B11	LED[9]

Key Pad		
KEYR0	PIN_AB2	Key Pad Row[0]
KEYR1	PIN_AB3	Key Pad Row[1]
KEYR2	PIN_AA5	Key Pad Row[2]
KEYR3	PIN_AA6	Key Pad Row[3]
KEYC0	PIN_Y5	Key Pad Col[0]
KEYC1	PIN_Y4	Key Pad Col[1]
KEYC2	PIN_Y3	Key Pad Col[2]
KEYC3	PIN_AA2	Key Pad Col[3]

## 7-SEGMENT DISPLAY SEGMENTS



Signal Name	FPGA Pin No.	Description
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7-segment Displays		
HEX00	PIN_C14	Seven Segment Digit 0[0]
HEX01	PIN_E15	Seven Segment Digit 0[1]
HEX02	PIN_C15	Seven Segment Digit 0[2]
HEX03	PIN_C16	Seven Segment Digit 0[3]
HEX04	PIN_E16	Seven Segment Digit 0[4]
HEX05	PIN_D17	Seven Segment Digit 0[5]
HEX06	PIN_C17	Seven Segment Digit 0[6]
DP0	PIN_D15	DP[0]
HEX10	PIN_C18	Seven Segment Digit 1[0]
HEX11	PIN_D18	Seven Segment Digit 1[1]
HEX12	PIN_E18	Seven Segment Digit 1[2]
HEX13	PIN_B16	Seven Segment Digit 1[3]
HEX14	PIN_A17	Seven Segment Digit 1[4]
HEX15	PIN_A18	Seven Segment Digit 1[5]
HEX16	PIN_B17	Seven Segment Digit 1[6]
DP1	PIN_A16	DP[1]
HEX20	PIN_B20	Seven Segment Digit 2[0]
HEX21	PIN_A20	Seven Segment Digit 2[1]
HEX22	PIN_B19	Seven Segment Digit 2[2]
HEX23	PIN_A21	Seven Segment Digit 2[3]
HEX24	PIN_B21	Seven Segment Digit 2[4]
HEX25	PIN_C22	Seven Segment Digit 2[5]
HEX26	PIN_B22	Seven Segment Digit 2[6]
DP2	PIN_A19	DP[2]
HEX30	PIN_F21	Seven Segment Digit 3[0]
HEX31	PIN_E22	Seven Segment Digit 3[1]
HEX32	PIN_E21	Seven Segment Digit 3[2]
HEX33	PIN_C19	Seven Segment Digit 3[3]
HEX34	PIN_C20	Seven Segment Digit 3[4]
HEX35	PIN_D19	Seven Segment Digit 3[5]
HEX36	PIN_E17	Seven Segment Digit 3[6]
DP3	PIN_D22	DP[3]
HEX40	PIN_F18	Seven Segment Digit 4[0]
HEX41	PIN_E20	Seven Segment Digit 4[1]
HEX42	PIN_E19	Seven Segment Digit 4[2]
HEX43	PIN_J18	Seven Segment Digit 4[3]
HEX44	PIN_H19	Seven Segment Digit 4[4]
HEX45	PIN_F19	Seven Segment Digit 4[5]
HEX46	PIN_F20	Seven Segment Digit 4[6]
DP4	PIN_F17	DP[4]
HEX50	PIN_J20	Seven Segment Digit 5[0]
HEX51	PIN_K20	Seven Segment Digit 5[1]
HEX52	PIN_L18	Seven Segment Digit 5[2]
HEX53	PIN_N18	Seven Segment Digit 5[3]
HEX54	PIN_M20	Seven Segment Digit 5[4]
HEX55	PIN_N19	Seven Segment Digit 5[5]
HEX56	PIN_N20	Seven Segment Digit 5[6]
DP5	PIN_L19	DP[5]

Signal Name	FPGA Pin No.	Description
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Dot Matrix		
DMR0	PIN_AA8	Dot Matrix Row[0]
DMR1	PIN_AA10	Dot Matrix Row[1]
DMR2	PIN_W10	Dot Matrix Row[2]
DMR3	PIN_Y8	Dot Matrix Row[3]
DMR4	PIN_V7	Dot Matrix Row[4]
DMR5	PIN_W9	Dot Matrix Row[5]
DMR6	PIN_V8	Dot Matrix Row[6]
DMR7	PIN_W7	Dot Matrix Row[7]
DMC00	PIN_AB13	Dot Matrix Col_0[0]
DMC01	PIN_AA14	Dot Matrix Col_0[1]
DMC02	PIN_W5	Dot Matrix Col_0[2]
DMC03	PIN_Y11	Dot Matrix Col_0[3]
DMC04	PIN_W6	Dot Matrix Col_0[4]
DMC05	PIN_AB12	Dot Matrix Col_0[5]
DMC06	PIN_W12	Dot Matrix Col_0[6]
DMC07	PIN_W13	Dot Matrix Col_0[7]
DMC10	PIN_AB10	Dot Matrix Col_1[0]
DMC11	PIN_AA15	Dot Matrix Col_1[1]
DMC12	PIN_V5	Dot Matrix Col_1[2]
DMC13	PIN_Y7	Dot Matrix Col_1[3]
DMC14	PIN_W8	Dot Matrix Col_1[4]
DMC15	PIN_AA9	Dot Matrix Col_1[5]
DMC16	PIN_AB11	Dot Matrix Col_1[6]
DMC17	PIN_W11	Dot Matrix Col_1[7]

VGA		
Signal Name	FPGA Pin No.	Description
VGA_R0	PIN_AA1	VGA Red[0]
VGA_R1	PIN_V1	VGA Red[1]
VGA_R2	PIN_Y2	VGA Red[2]
VGA_R3	PIN_Y1	VGA Red[3]
VGA_G0	PIN_W1	VGA Green[0]
VGA_G1	PIN_T2	VGA Green[1]
VGA_G2	PIN_R2	VGA Green[2]
VGA_G3	PIN_R1	VGA Green[3]
VGA_B0	PIN_P1	VGA Blue[0]
VGA_B1	PIN_T1	VGA Blue[1]
VGA_B2	PIN_P4	VGA Blue[2]
VGA_B3	PIN_N2	VGA Blue[3]
VGA_HS	PIN_N3	VGA H_SYNC
VHA_VS	PIN_N1	VGA V_SYNC