

Basic Setting

1. **Device Family:** MAX 10(DA/DF/DC/SA/SC)

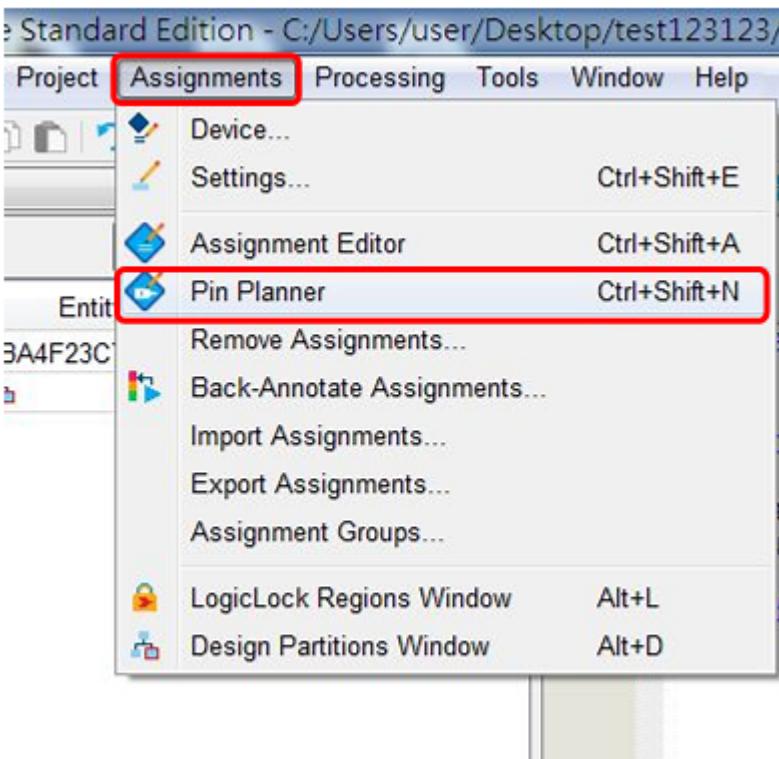
2. **Available Devices:** 10M50DAF484C7G

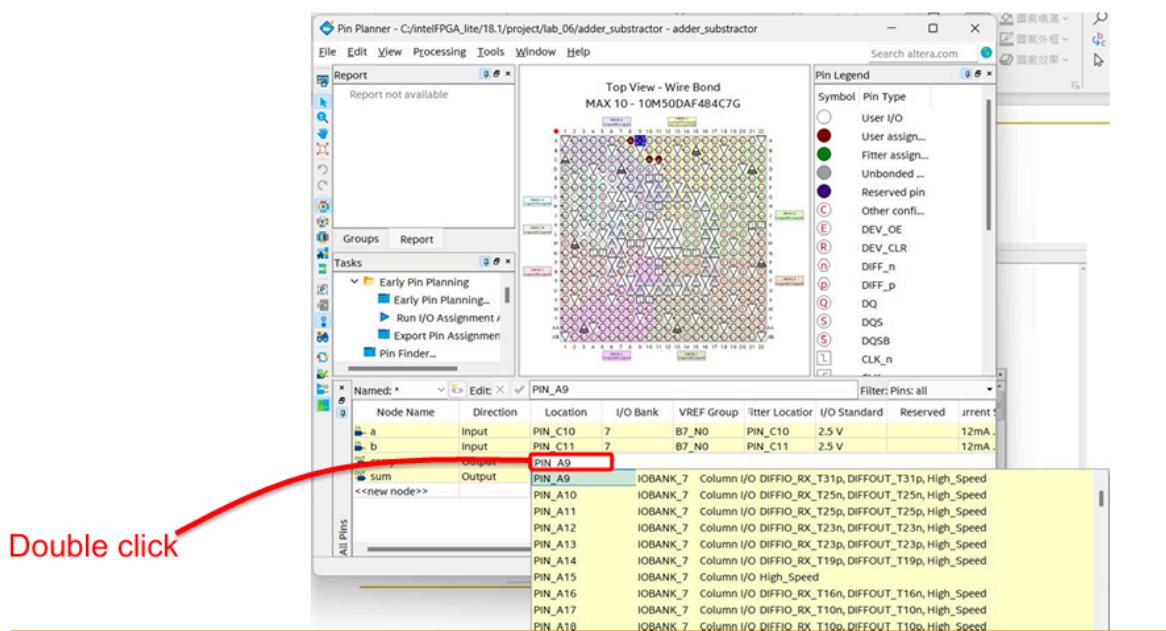
Available devices:

Name	Core Voltage	LEs	Total I/Os	GPIOs	Memory Bits	Embedded multiplier 9-bit el
10M50DAF256I7G	1.2V	49760	178	178	1677312	288
10M50DAF484C6GES	1.2V	49760	360	360	1677312	288
10M50DAF484C7G	1.2V	49760	360	360	1677312	288
10M50DAF484C8G	1.2V	49760	360	360	1677312	288
10M50DAF484C8GES	1.2V	49760	360	360	1677312	288

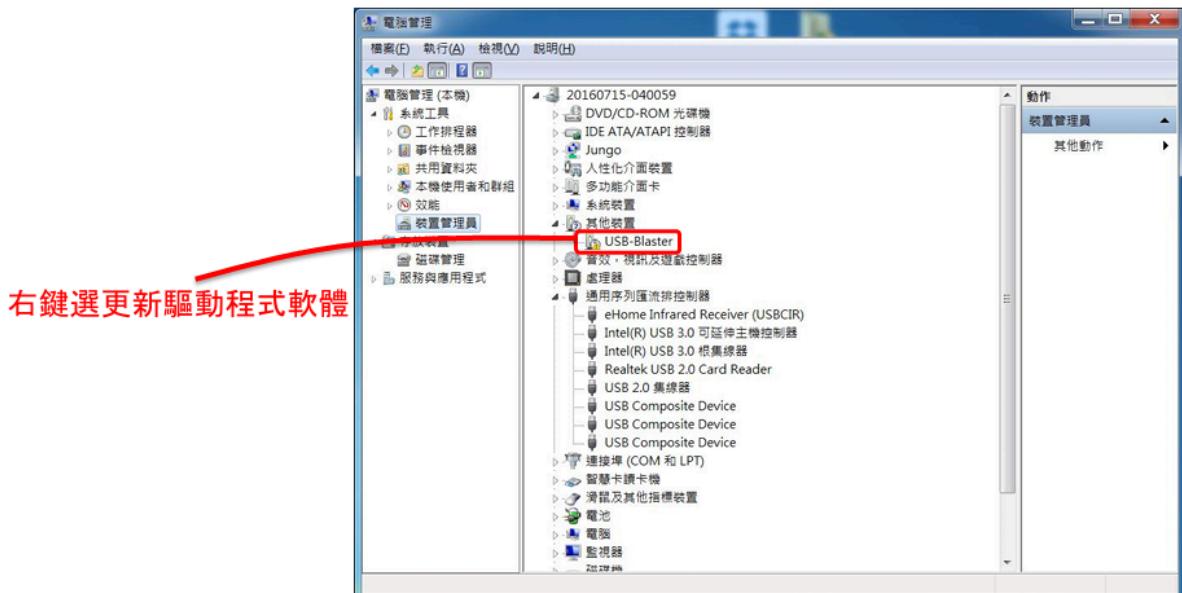
3. **EDA Tool Settings, Simulation:** Modelsim-Altera, Verilog HDL

4. Click 「Assignments > Pin Planner > Location」

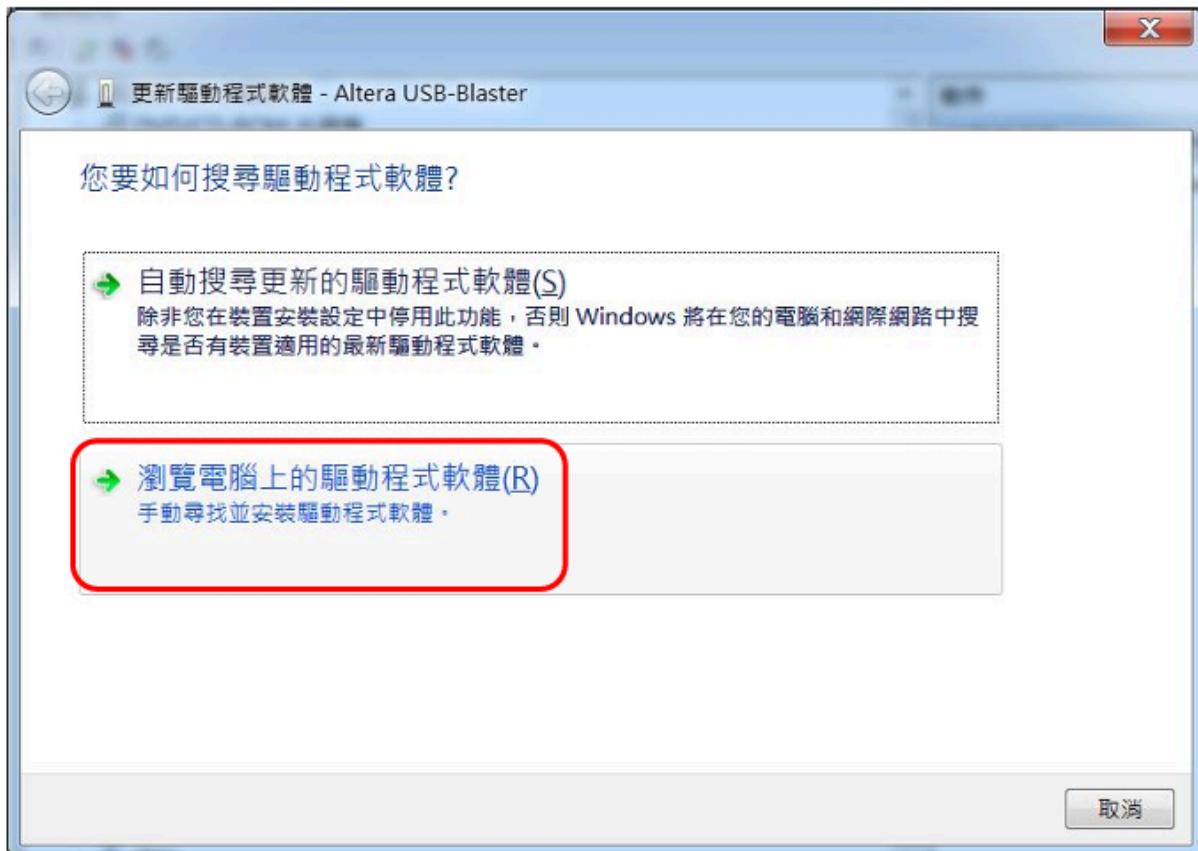




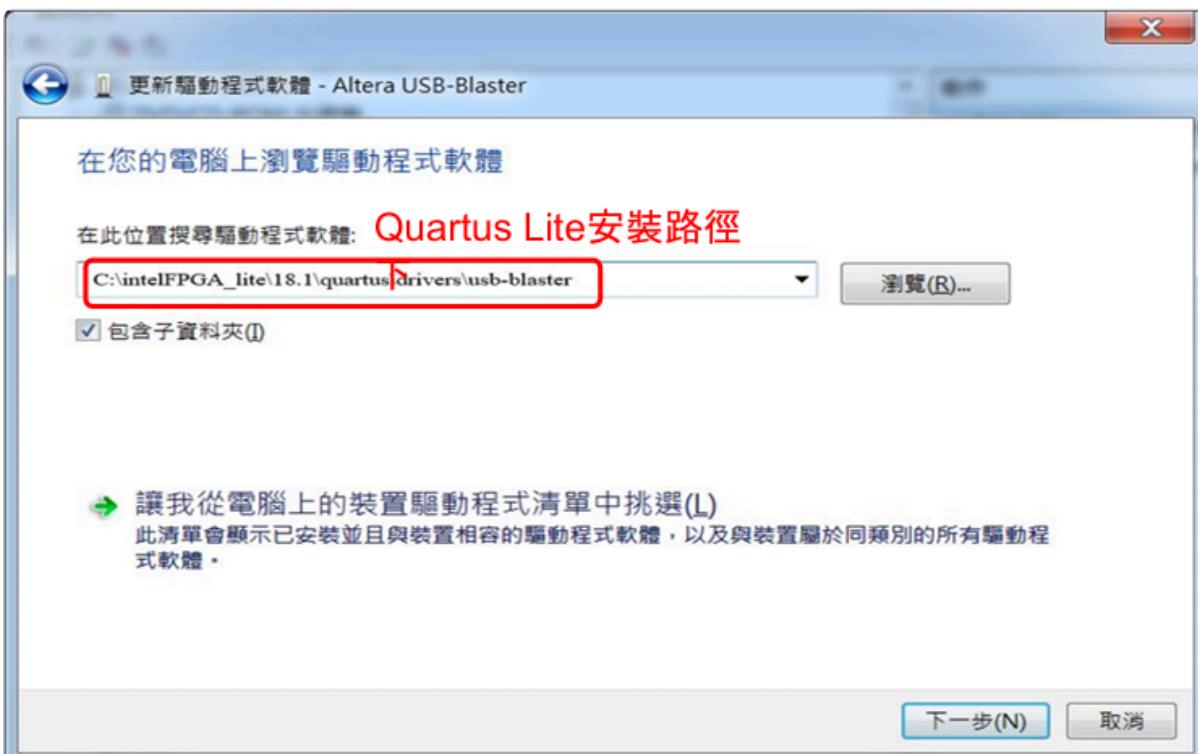
5. 到裝置管理員，「其他裝置 > USB-Blaster」，右鍵「更新驅動程式」



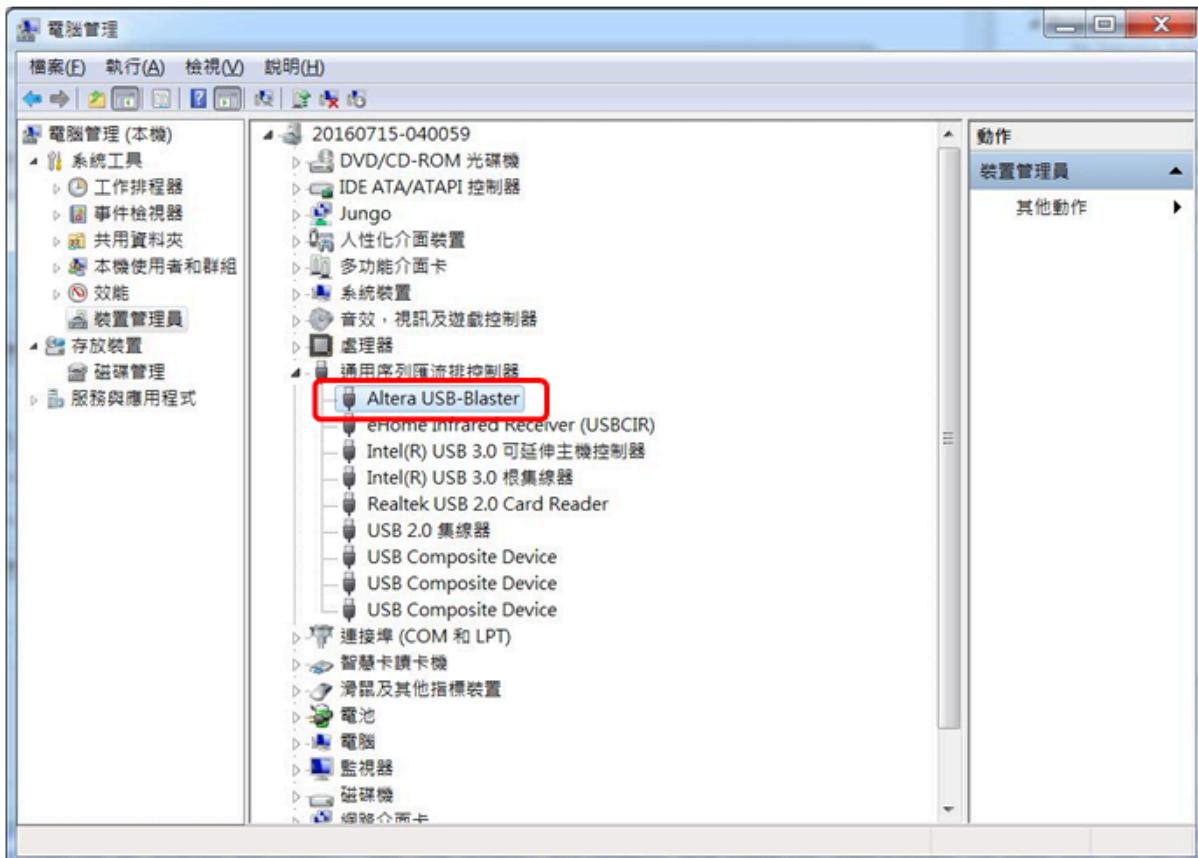
6. 濶覽電腦上的驅動程式



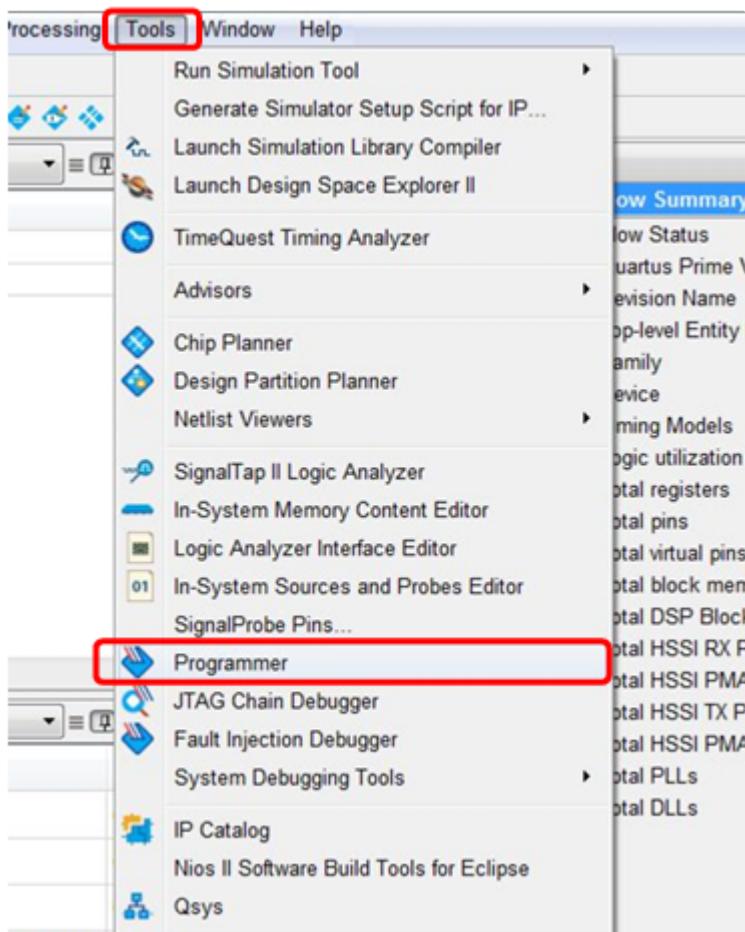
7. 路徑 C:\intelFPGA_lite\16.1\quartus\drivers\usb-blaster (16.1 或 18.1)



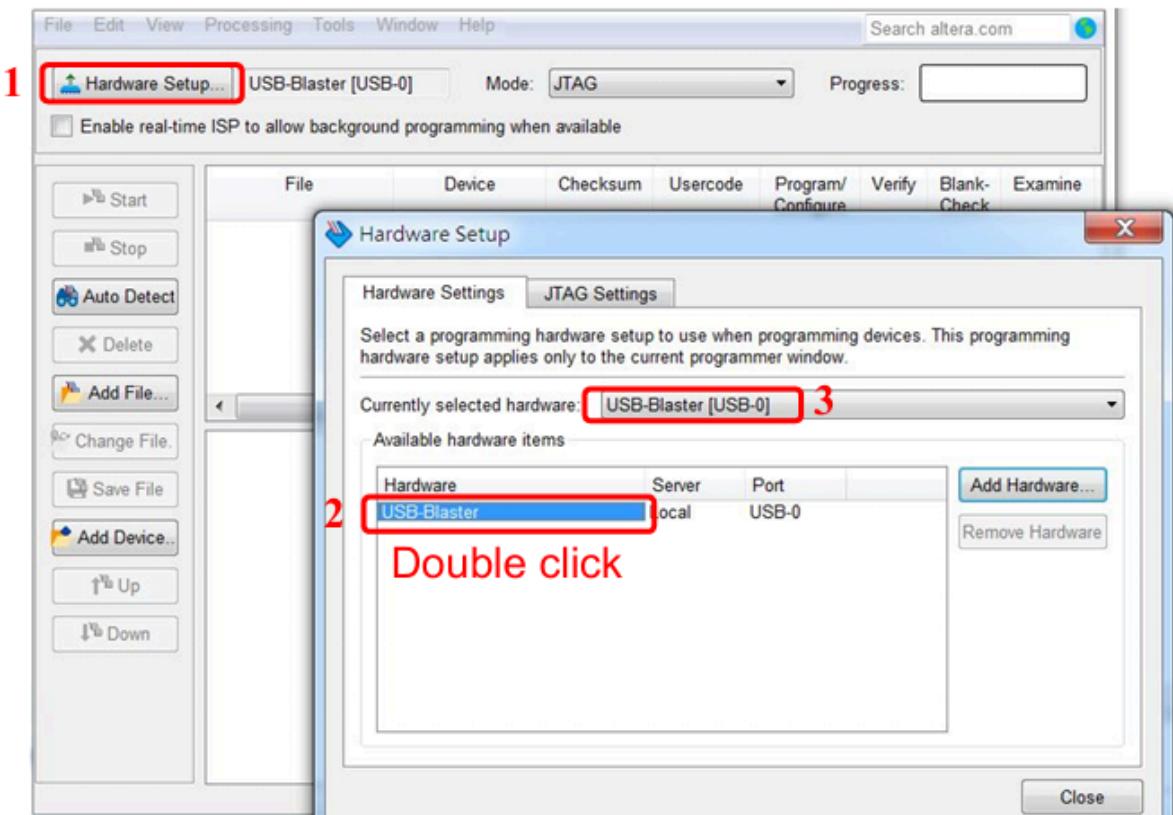
8. 到裝置管理員，「通用序列匯流排控制器 > Altera USB-Blaster」，有就代表成功



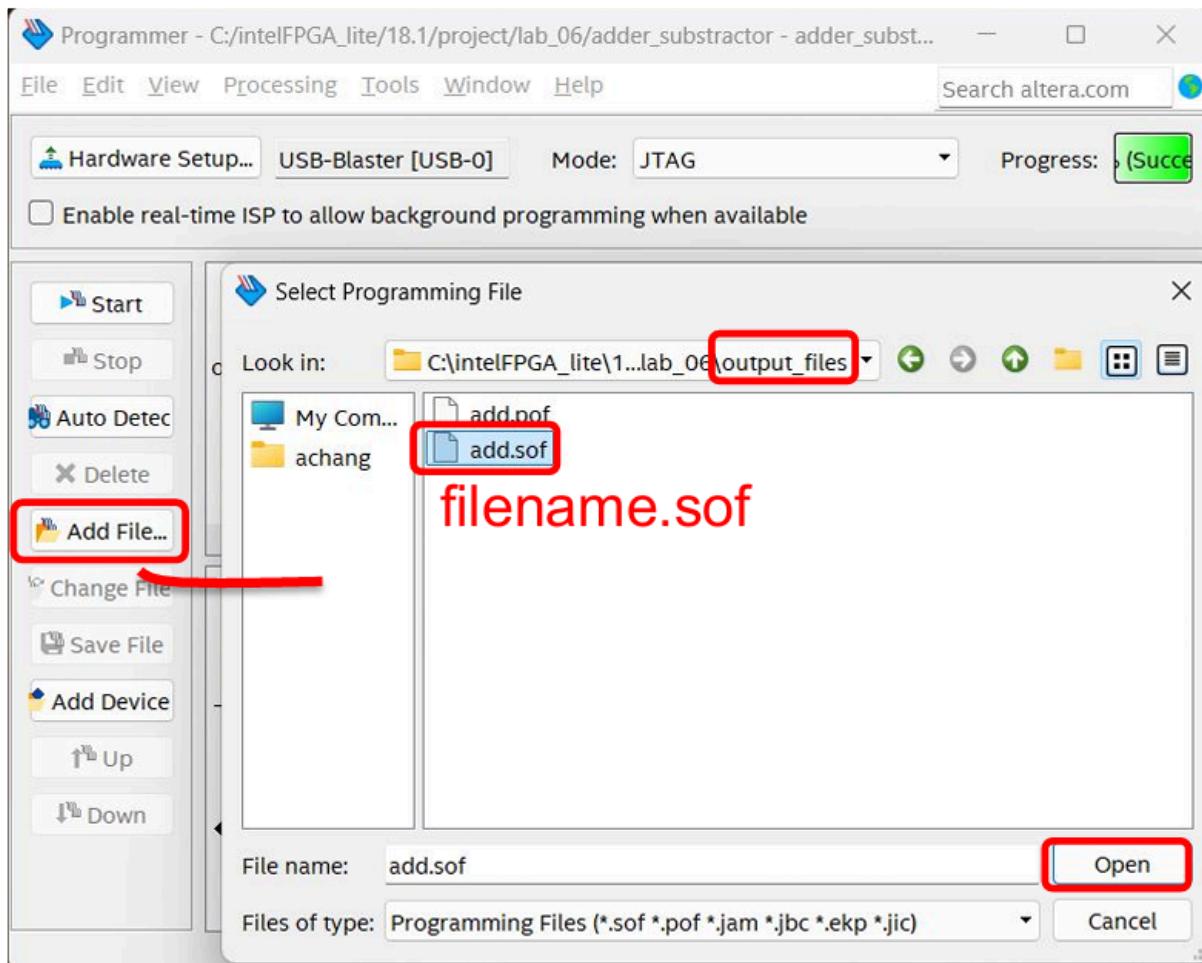
9. Click 「Tools > Programmer」



10. Click 「Hardware Setup > USB Blaster」，然後 Currently selected hardware 選「USB-Blaster [USB0]」



11. Click 「Add file > ~.sof > Open」 (注意是在 outputfiles 裡面)



12. Click 「Start」

