

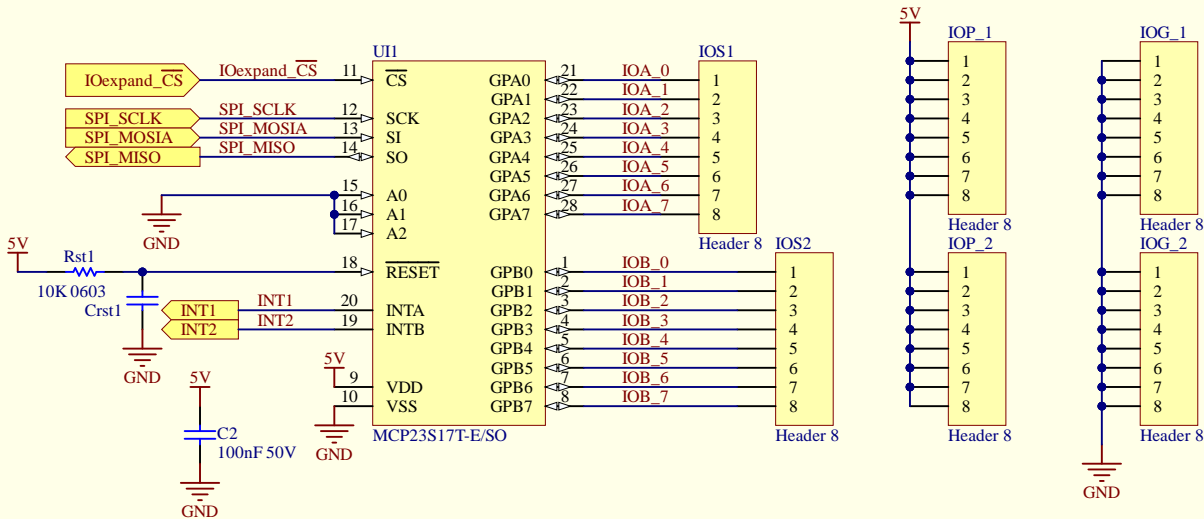
Title **Processor 40pin GPIO Interface**

Size: A	Project: Building a	Revision: 1.2c
Date: 19/10/2023	Time: 11:31:44	Sheet 1 of 5
File: Processor_Interface.SchDoc		

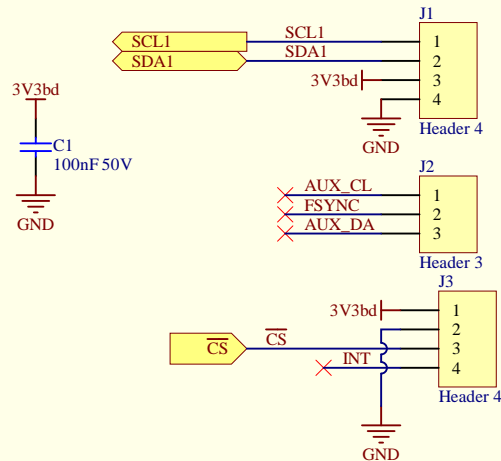
Engineer: **D Board**



Digital IO Expansion SPI to 16 IOs

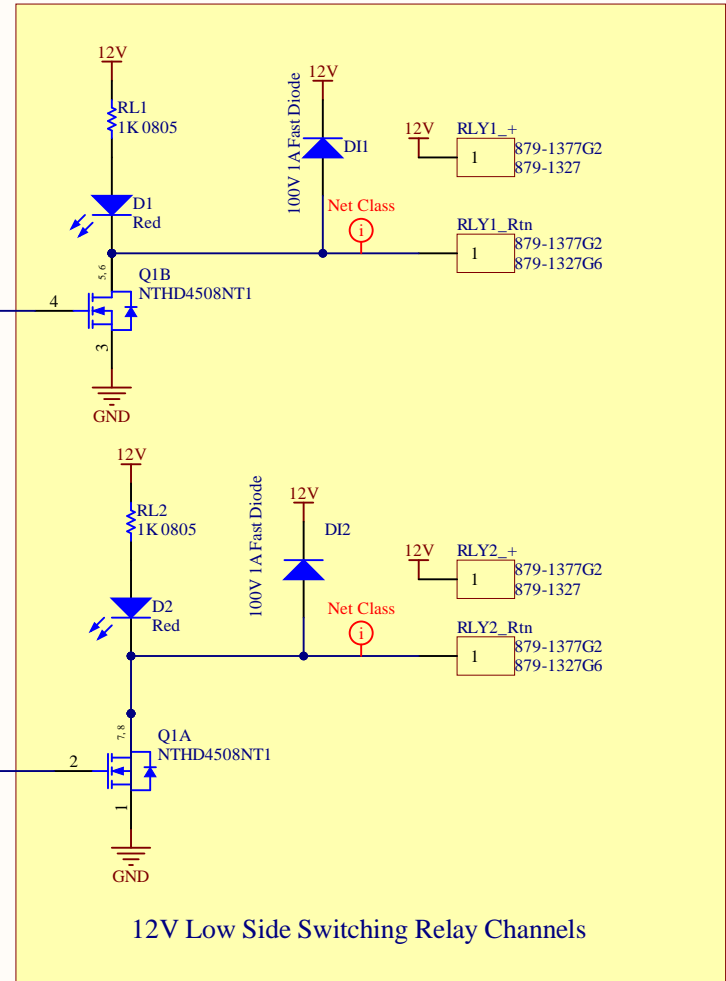
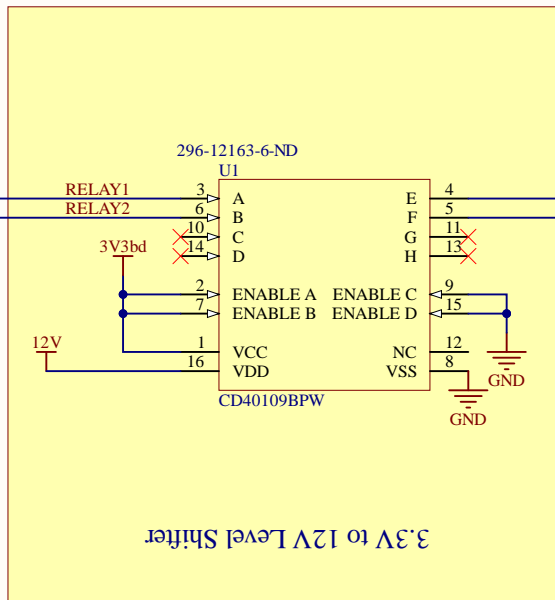


I2C addr 0b110100 0x68
MPU module interface - Accel, Gyro, Mag



Invensense MPU-9250 needed

RELAY[1..2]



Title 12V Solenoid Relay Logic		
Size: A	Project: suiddoL CM	Revision: 1.2
Date: 19/10/2023	Time: 11:31:45	Sheet 4 of 5
File: Relay_IO.SchDoc		

S Circuit Engineer:

