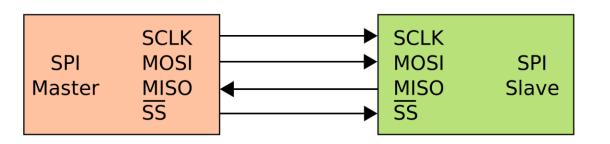
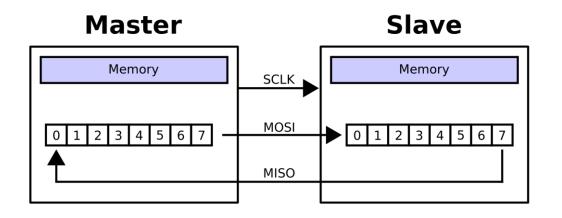
# Интерфейсы

#### SPI

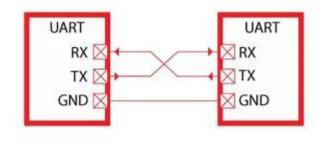
- SPI serial peripheral interface
- MOSI Master Out Slave In
- MISO Master In Slave Out
- SCLK синхросигнал.
- SS —Slave Select.
- Отсутствие официального стандарта

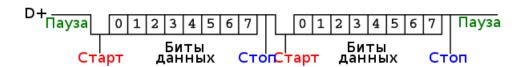




#### **UART**

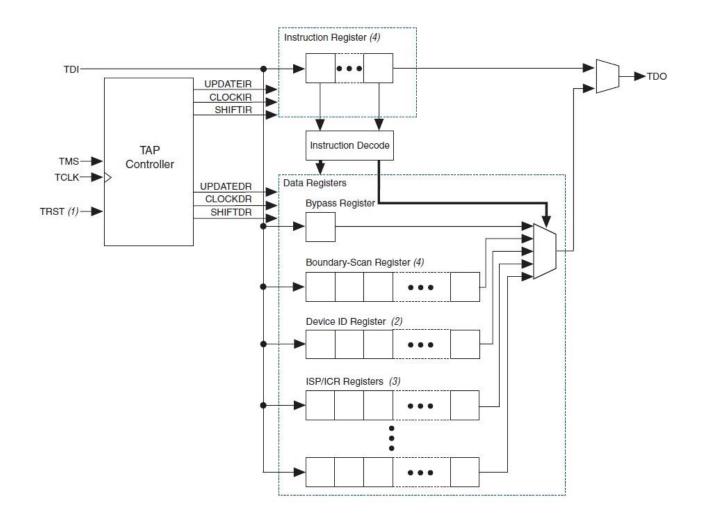
- UART universal asynchronous reciver-transmitter
- Возможен контроль четности
- Соединение типа точка-точка



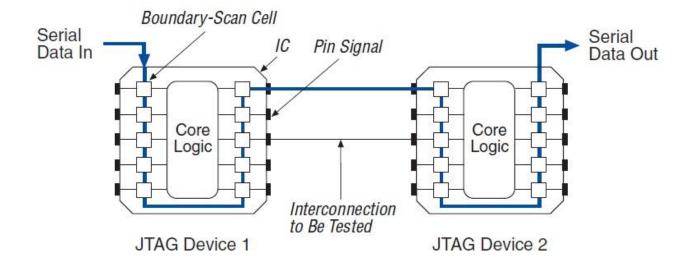


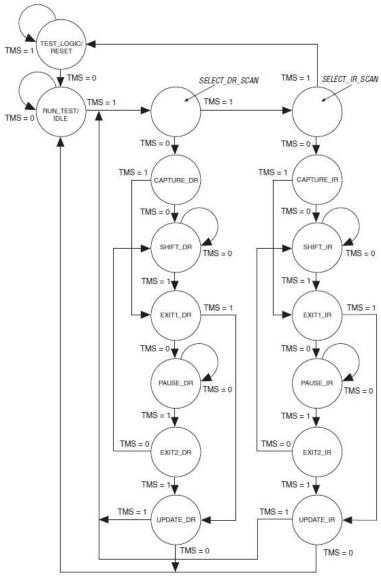
#### **JTAG**

- JTAG join test action group
- IEEE 1149
- Граничное тестирование
- Регистр данных и регистр инструкций



## **JTAG**





### 7SEG

