

# 《计算机体系结构-量化研究方法-第六版》

## 第四章：

### 1. PPT 内容

Page 5 Flynn's Taxonomy

Page 6-7 Advantages of SIMD architectures

Page 11 Example of vector architecture

Page 26 Optimizations

Page 28 1. A four lane vector unit

Page 38-39 Memory banks, 课本 page 298-299

Page 40-43

Page 58-59 Roofline Model 课本 Page 307-310

Page 60-61 C. Graphical Processing Unit GPU

Page 63-64 Threads, blocks, and grid (CUDA) 课本 Page 313-320

Page 65-70 Example: multiply two vectors of length 8192

Page 71-73 NVIDIA GPU memory structures 课本 Page 326-328

Page 74 Terminology (GPU)

Page 81-83 Conditional branching 课本 Page 323 – 326

Page 91-94,97 Loop level parallelism