Bryan Arnold

CSE 3666

Architecture Assignment 2

2/26/17

	Instruction Memory	Control	Registers	ALU	Data Memory	Branch Hardware	Jump Hardware
add	Yes	Yes	Yes	Yes	No	No	No
sub	Yes	Yes	Yes	Yes	No	No	No
addi	Yes	Yes	Yes	Yes	No	No	No
lw	Yes	Yes	Yes	Yes	Yes	No	No
sw	Yes	Yes	Yes	Yes	Yes	No	No
beq	Yes	Yes	Yes	Yes	No	Yes	No
j	Yes	Yes	No	No	No	No	Yes

Blocks with "Yes" in them indicate blocks of the single-cycle hardware that are useful for the given instruction, and "No" indicates that the single-cycle hardware is not useful for the given instruction.