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CSE 3666

3/14/16

Architecture Assignment 6

1a) Non-pipelined: 1,225 ps. Pipelined: 325 ps.

b) Non-pipelined: 350 ps. Pipelined: 90 ps.

c) Non-pipelined: 395 ps. Pipelined: 100 ps.

d) Non-pipelined: 375 ps. Pipelined: 100 ps.

e) Non-pipelined: 470 ps. Pipelined: 125 ps.

2a) Non-pipelined: 1,225 ps. Pipelined: 1,625 ps.

b) Non-pipelined: 350 ps. Pipelined: 450 ps.

c) Non-pipelined: 395 ps. Pipelined: 500 ps.

d) Non-pipelined: 375 ps. Pipelined: 500 ps.

e) Non-pipelined: 470 ps. Pipelined: 625 ps.

3a) Split the MEM stage and the new clock-cycle time of the processor will be 300 ps.

b) Split the MEM stage and the new clock-cycle time of the processor will be 80 ps.

c) Split the WB stage and the new clock-cycle time of the processor will be 90 ps.

d) Split the ID stage and the new clock-cycle time of the processor will be 90 ps.

e) Split the MEM stage and the new clock-cycle time of the processor will be 100 ps.