

Bryan Arnold

CSE 3666

Architecture Assignment 3

2/26/17

	ALUSrc	MemToReg	RegDst	ALU	RegWrite	MemRead	MemWrite	Branch	Jump
add	0	0	1	0010	1	0	0	0	0
sub	0	0	1	0110	1	0	0	0	0
addi	1	0	0	0010	1	0	0	0	0
lw	1	1	0	0010	1	1	0	0	0
sw	1	X	X	0010	0	0	1	0	0
beq	0	X	X	0110	0	0	0	1	0
j	X	X	X	X	0	0	0	X	1