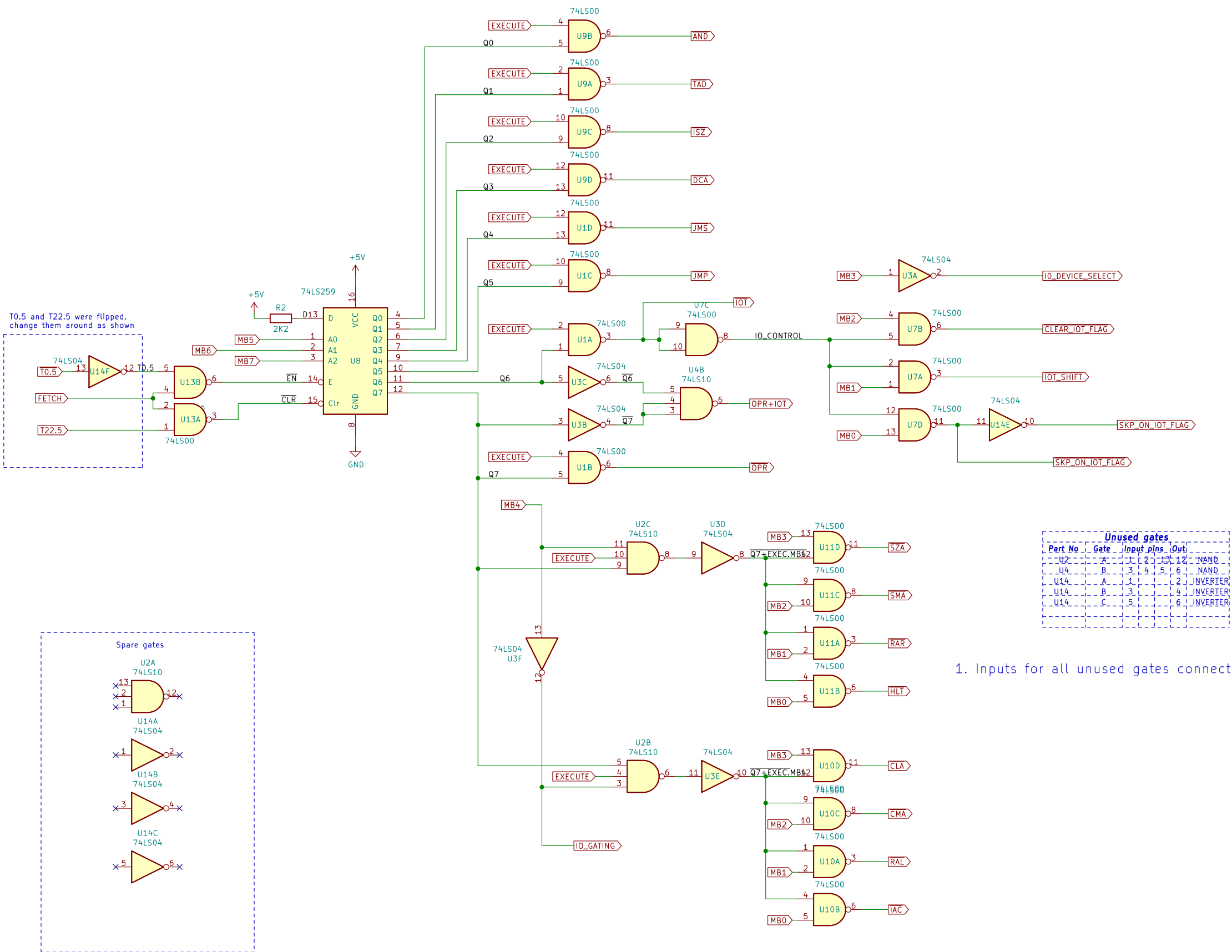
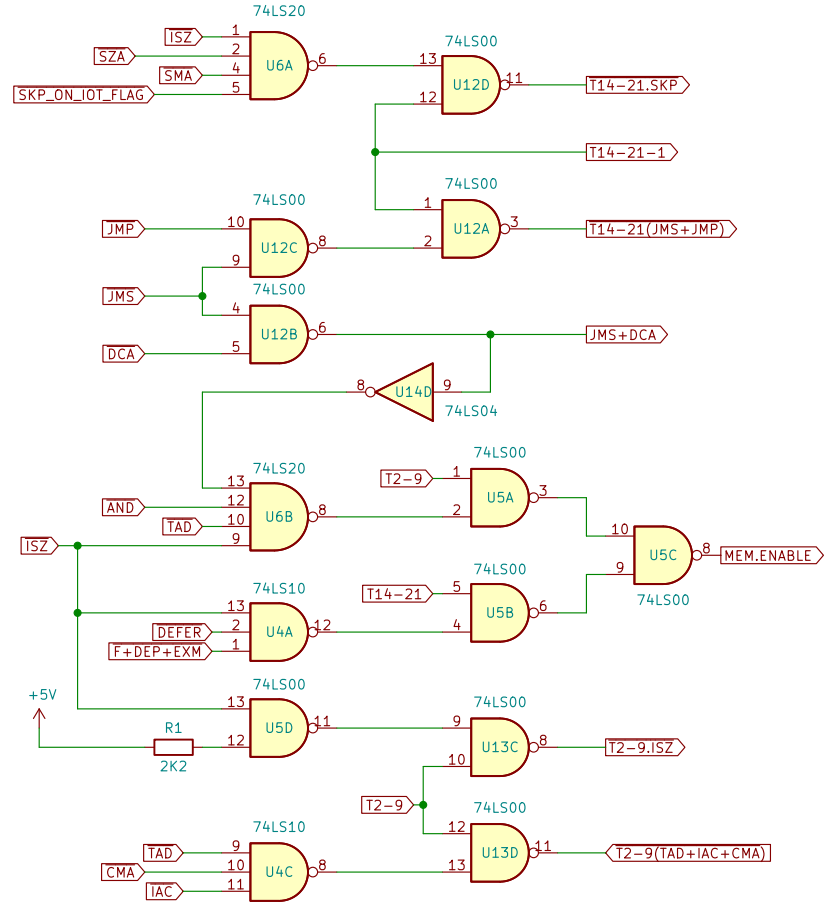


Every track and pad checked against the original artwork



Part No.	Gate	Input pins	Out
U2	A	1, 2, 12, 14	NAND
U4	B	3, 4, 5, 6	NAND
U14	A	1, 2	INVERTER
U14	B	3, 4	INVERTER
U14	C	5, 6	INVERTER

1. Inputs for all unused gates connected to GND

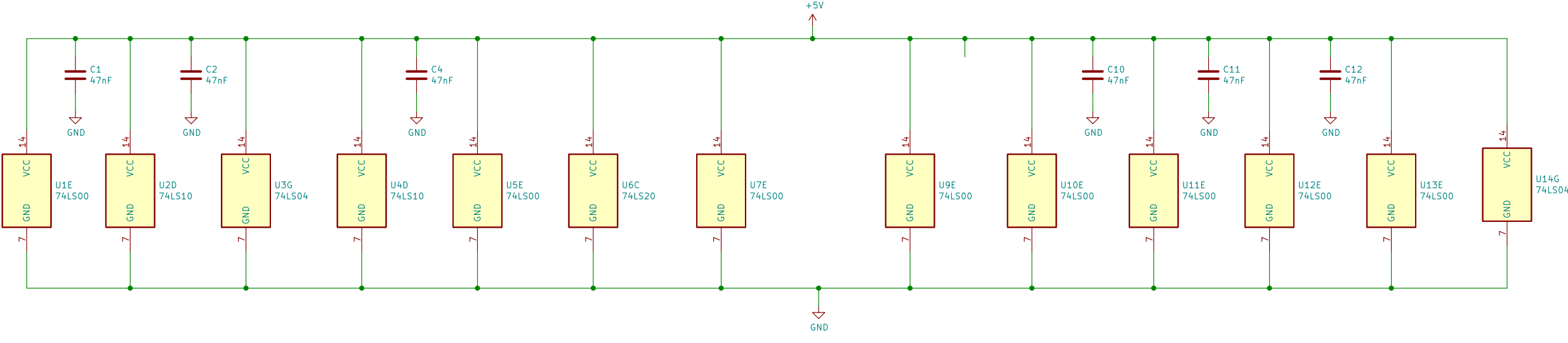
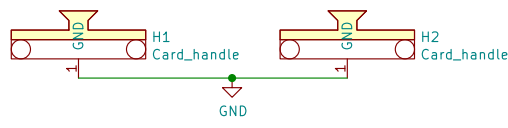


RefDes	Part No.	Supplier
U1	74LS00	
U2	74LS10	
U3	74LS10	
U4	74LS10	
U5	74LS00	
U6	74LS20	
U7	74LS00	
U8	74LS259	
U9	74LS00	
U10	74LS00	
U11	74LS00	
U12	74LS00	
U13	74LS00	
U14	74LS04	
R1	2.2K 1/4 Watt resistor	
C1	100nF 50v Polyester	

Part No.	Type	QTY
74LS00	Quad NAND	8
74LS10	Hex Inverter	2
74LS10	3 Input NAND	2
74LS20	6 Input NAND	1
74LS259	Addressable 8 bit latch	1
C1-C10	100nF 50v Polyester	14
C15-C16	100uF 35v electro	2
R1	2.2K 1/4 Watt resistor	1

Pin No	CARD EDGE 1	DB25
1	GND	1
2	+5V DC	14
3	IO_GATING	1
4	IO_DEVICE_SELECT	1
5	IO_DEVICE_SELECT	1
6	CLEAR_IOT_FLAG	1
7	SKIP_ON_IOT_FLAG	1
8	IO_SHIFT	1
9	IO_SHIFT	1
10	IO_SHIFT	1
11	IO_SHIFT	1
12	IO_SHIFT	1
13	IO_SHIFT	1
14	IO_SHIFT	1
15	IO_SHIFT	1
16	IO_SHIFT	1
17	IO_SHIFT	1
18	IO_SHIFT	1
19	IO_SHIFT	1
20	IO_SHIFT	1
21	IO_SHIFT	1
22	IO_SHIFT	1
23	IO_SHIFT	1
24	IO_SHIFT	1
25	IO_SHIFT	1

Pin No	CARD EDGE 2
1	AND
2	TAD
3	ISZ
4	DCA
5	JMS
6	JMS
7	JMP
8	IOI
9	OPR
10	MB0
11	MB1
12	MB2
13	MB3
14	MB4
15	MB5
16	MB6
17	MB7
18	+5V
19	GND



Part No.	Gate	Input pins	Out
U2	A	1, 2, 12, 14	NAND
U4	B	3, 4, 5, 6	NAND
U14	A	1, 2	INVERTER
U14	B	3, 4	INVERTER
U14	C	5, 6	INVERTER

INSTRUCTION DECODER