Name of the Experiment: To write a program for Unipolar Non-Return-to-Zeno (NRZ) line coding.

Objectives: To know the basics of line cooling technique and how it works and what are the advantages and disadvantages of Unipolan (Nez) line coding technique.

Theory: Line coding is the process of conventing digital darta into digital signal suitable for transmission over a communication channel, typically by encoding each bit on symbol with specific voltage levels frequencies on phases

Unipolar Non-Return-to-Zerio is a line cooling technique used in digital communication where one voltage level represents a binary '1' and another voltage level represents a binary 'O'. Unlike Return -to-Zeno encoding in Unipolan NRZ the signal does not neturn to zono between the bit intervals. This means that the voltage level is maintained during the entine duration of the bit peniod, nepresenting either a high on low state of each bit

In unipolar NRZ the signal is only in the positive side. That is shown as follows.

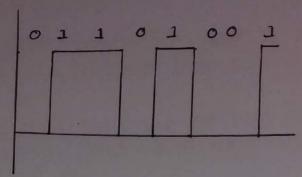


Figure: Unipolan Non-Return-to-Zeno

Advantages Unipolan NRZ

OIT is simple in implementation,

2 It has a better bandwidth efficiency.

3 It supports higher data mates.

Disadvantages of Unipolan NRZ

- 1) Presence of DC components leading to signal distorition.
- (2) Lack of inherent clock synchronization.

3) Vulnapable to base line wandering.

4) Susceptible to transmission enrons, especially in noisy environments.

Blimited ennon detection panticularly for long sequences of consecutive bits.

Appointus Required

- 1) Laptop/Desktop Computer.
- 2) Application software.

Expeniment Number: 02

Name of the Expeniment To write a program for Polan Non-neturn to zono Line roding.

Objectives: To know the basic of line coding technique and how it works and what are advantages and disadvantuges of it.

Theory:

Line cooling is the process of conventing digital data into digital signal suitable for transmission over a communication channel, typically by encoding each bit on symbol with specific voltage levels, friequencies on phases

Polan nonneturn-to-zeno is a line cooling scheme used in digital communication where each bit nepresented by a specific voltage level. In polan NRZ one voltage level represents a binary zero, while a different voltage level represents a binary 1'. Unlike neturn to zero encoding the signal does not neturn to zeno between each bit interval. Istead the voltage level is maintained for the duration of the bit period. making polan NRZ mone efficient in terms of bandwidth usage but potentially susceptible wander and clock necovery issues.

In Polan Retu: NRZ the negative side is acceptible the diagram of Polan NRZ is given follows.

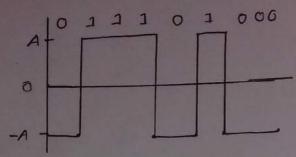


Figure: Polan Robn Return to Zerro

Advantages of Polan NRZ

- OIT is simple in implementation.
- 2) No low-frequency component are present.

Disadvantuges of Polan NRZ

- DNO annon connection is possible here.
- No clock is present.

Appantus Required

- 1) Desktop/Laptop Computer.
- 2) Application software

Name of the Expeniment: To write a program for Uni Polar Return to zona line Objectives: To know the concept of line coding and specially the Uni-Polan (02) specially the Uni-Polan (RZ) and how it works and What are advant What are advantages and disadvantages of it. Theory: Uni-Polan neturn to zeno encoding is a line Coding technique used in digital communication system In unipolar RZ encoding, each bit of digital data is represented by a signal that neturns to zero voltage level withing the bit dunation, regardless of whether the bit is a local of the bit is a local of the bit of the bit is a local of the bit is a l the bit is a logical 'O'on' J'. Here is more detailed explanation (F) Birany '1' is represented by a higher voltageled 1. Encodina Binary 'O' is represented by a neutral on zerro voltage level-2. Signal Representation During each bit peniod, if the data bit is 1, the signal maintance a high voltage level for the entire half time and then that neturns to zero and stays in zero for the remaining half time. 3. Synchronization Unipolan RZ encoding provides better synchonomization capabilities companed to some other line coding schemes such as Unipolan NRZ The Zerio voltage level between each bit interval helps in clock necovery and synchronization at the neceiven end

Unipolar RZ encoding typically nequines more bandwidth companed to all width companed to other line coding schemes like NRZ due to the presence of his like NRZ due to the presence of additional triansactions

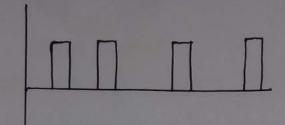


Figure: Unipolan return to Zeno

Advantages of Unipolan R7 encoding

- DImproved synchronization due to transitions between bits
- DEnhanced noise immunity companed to unipolan NEZ
- * Fasien clock necovery of the neceiving End.

Disadvantages of Unipolan RZ encoding

- @ Requires more bandwidth compared to some other line coding techniques.
- Required efficiency in terms of Bondwidth utilization

Appantus Required

- 1) Laptop/Desktop Computer,
- 2) Application Software,

Experiment Number: 04

Name of the Expeniment: To unite a program for Bi-Polari Return to-zono line of

Objectives: To know the fundamentals of Bi-polar roturn to zero line anding the to zeno line coding. How it works, and what are the advantagen and disadvanta advantages and disadvantages of it.

Theory: Bi-Polan Return-to-zeno (Bipolan RZ) is amline encoding technique used in digital communication System. In this encoding technique each bit of digital data is represented by a signal that triansitions between multiple voltage levels. Specifically, Bipolan RZ encoding envolves Three voltage levels: positive, negative and zeno.

Here is how bipolan RZ works

- 1. VOLTAGE LE VELS
 - (*) Positive level
 - (or) Negative level
 - (x) Zeno level
- 2. Representation of Data bits
 - @A positive value represents binary 1
- Anaptive voltage pluse nepriesents binary]
- A No voltage pluse represent a binary o
- 3. Signal Generation
 - From each bit of digital data a signal is generated from the first binary I the pluse is possitive and for
 - the second 1 pluse is negative vice vensa
- # If the data bit is zeno no pluse is generated

Ton each positive and negative pluse it neturns to Zerro.

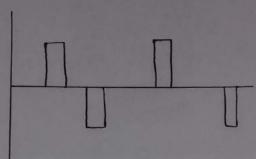


figure: Bipolan Return to Zero

Advantages of BRZ

DIT provides a better synchropization capabilities companed to the other encoding schemes

A) It helps in minimizing ennous during the data triansmission due to the clean definition of bit boundaries.

Disadvantages of Bipolan RZ

#It nequines more bandwidth compared to the other encoding techniques due to the use of three voltage levels

IT may len efficient in tenms of bandwicth otilization

Appantus Requined

- 1) Laptop/Dasktop
- 2) Application Software,

Name of the Expeniment, To Unite a program for Split-Phase on Manchastan Code

Objectives: To know the basic properties of marches ten code / Grand ten code / Ginay code and determine that how it works it works and what are the advantages and disadvantages of it.

Theory: Manchester encoding also known as marchester code is a line coding technique used in digital communication to nication to encode binary data for transmission out a communication channel. In Manchester cooling each bit peniod is divided into two halves and a transition accurs in the middle of each bit period. The presence on absence of than sition determines the value of the bit being transmitted.

Basic Principle: In Manchester encoding, both logical o and I is represented by transitions.

mansition Rules: for a logical O: The signal transitions from a high voltage to a low voltage in the middle of the penied.

for a logical 1: The signal transitions from a low voltage to high voltage in the middle of the bit peniod.

synchronization: Since transitions occurs at a regular intervals. Manchester encoding fucilitates clock necovery and synchronization between the sender and receiver. This makes it suitable for sender and receiver this makes it suitable for senden and relatible duty transmission

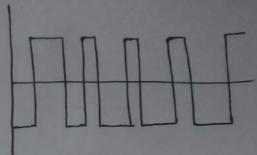


Figure: Manchester Encoding

Advantages of Manchesten Grooding

self clocking. The presence of transitions in the middle of each bit ensures that the hereivers can extract the clock signal from the dartu stream, adding synchronitution Oc Balance: The average voltage over a bit peniod is zerro, which helps in maintaining the integrety of the triansmission medium, particularly in systems suscepitible to signal degradation

Disadvantages of Manchesten Coding

- 1) It requires a higher bandwidth
- (2) It has a loven date plate

Appantus Required

- Dlaptop/Desistop Computer
- 2) Application software

Experiment Number: 06

Name of the Experiment: To write a program for binary Amplitude Shift keying (Ask) modulation and demodulation.

Objectives: To know the fundamentals of shif keying and know the process of Amplitude shift keying modulation and demodulation.

Theory: Shift keying is a digital modulation technique used in communication system to transmit digital data over a carnier wave by varying one or more of its characteristics.

Amplitude shift keying is a digital modulation technique where the amplitude of a cannier signal is vanied to represent digital data. In Ask, there are typically two discrete amplitude levels used to represent winary states

In Ask technique two types of amplitude are fixed for representing the binary 'O' and 'I'. We can determine any amplitude representing ang of the binary data. Suppose 'of we represent migh amplitude for I' the low amplitude means binary 'O' vice vensa.

For Modulation the amplitude of the cannier signal varinies according to the digital bit representation on the other hand while demodulating vigher amplitude means binary I and lower means of and the demodulation is done.

Expeniment Number: 07

Name of the Expeniment: To write a priogram for frequency shift keying (FSK) modulation and de modulation

Objective: To know the concept of Friedmenry shift weging (FSK) modulation and demodulation in digital data communication.

Theory: Friequency Shift keying (FSK) is a digital modulation technique in which there friequency of the contrien signal is varied to represent digital data. In FSK modulation two different friequencies are used to represent the two birary states (0 and 1).

In fsk, a high frequency carriien signal is generated the carriien signal typically has a mixed amplitude. The frequency of the carriien signal is modulated according to the binary data. One frequency is used to represent one binary and another frequency is used to represent the other binary state. The modulated signal then transmitted through communication channel At the neceiving end, the neceived signal is demodulated to extract the original binary data.

Experiment Number: 08

Name of the Experiment: To unite a priogram for Phase Shift keying modulation and demodulation.

Objectives: To know the basic concept of phase shift keying technique and how it can be used for digital communication.

Theory: Phase shift keying is a digital modulation technique in which the phase of the connier signal is varied to represent digital data. In PSK modulation the cannier signal's phase is shifted by specific angles to represent different binary states.

for PSK, A high frequency cannien signal is generated. This cannien signal typically has a fixed frequency and amplitude. The phase of the cannien signal is modulated according to the binary data. Different phase shifts are used to represent the different binary states, for example a phase shift of Odegnee might represent binary 1. The modulated cannier sign which now cannies the digital data through the communication channel. At the receiving end, the received signal is demodulated to extract the original binary data.

Name of the Expeniment: To write a program for Quadrature Phase Shift keying (ODC) Phase Shift keying (BPSK) modulation.

Objective: To know the general format of QPSK and how its works

Theory: Quadrature Phase Shift keying is a digital modulation scheme that extends the concept of Binary Phase Shift keying (BPSK) to transmit two bits of digital data pen symbol. In QPSK modulation, four different phase shifts are used to represent four different Combinations of two binary bits. The digital data to be transmitted is encoded into a binary format. Each pain of binary bits is mapped to one of four possible phase shifts. Two carnier signals, typically in quadrature (90° degree out of phase) are generated. These connier signals has fixed frequencies and amplitude. Each pair of the binary digits is maped to one of the four possible phase shifts (0°,90°,180° on 270°). These phase shifts are used to modulate in-phose and quandrature (Q) components of the commiers signals.

Expeniment Number: 10

Name of the Experiment: To write a program for pulse amplitude modulation.

Objective: To know the basic concept of Pulse Amplitude producation to be basic concept of Pulse Amplitude de modulation how it works and how PATA is used for modulation.

Theory: Pulse Amplitude Modulation (PAM) is a digital modulation technique in which the amplitude of a series of nectangular pulses is vanied to represent digital data. In PAM, each pulse nepnesents one on more bits of digital information.

The digital data to be transmitted is encoded into a binary format Each binary digit (bit) on group of bits is mapped to a specific amplitude level, A train of pluses is generated where each pulse represents one on mone bits of digital data. The amplitude of each pulse is modulated according to the binary data. Different amplitude levels and use to represent different birrary states.

At the neceiven end, the neceived pulse thain is demodulated to extract the original binary data.