Ann	nntena	7 RF FE (1950 MHz)	I/Q demodulator	In phase Quadrature	ADC	Receiver
Information	tion bits	Digital Signal Processing 1	Down	sampling M	FIR Filter	(Digital Down Converter) DDC1 (IF1=200 kHz)
Information	tion bits	Digital Signal Processing 2	Down	sampling M	FIR Filter	(Digital Down Converter) DDC2 (IF2=400 kHz)
Information	tion bits	Digital Signal Processing 3	Down	sampling M	FIR Filter	(Digital Down Converter) DDC3 (IF3=600 kHz)
Information	tion bits	Digital Signal Processing 4	Down	sampling M	FIR Filter	(Digital Down Converter) DDC4 (IF4=800 kHz)
Information	tion bits	Digital Signal Processing 5	Down	sampling M	FIR Filter	(Digital Down Converter) DDC5 (IF5=1000 kHz)
Information	tion bits	Digital Signal Processing 6	Down	sampling M	FIR Filter	(Digital Down Converter) DDC6 (IF6=1200 kHz)
Information	tion bits	Digital Signal Processing 7	Down	sampling M	FIR Filter	(Digital Down Converter) DDC7 (IF7=1400 kHz)
Information	tion bits	Digital Signal Processing 8	Down	sampling M	FIR Filter	(Digital Down Converter) DDC8 (IF8=1600 kHz)