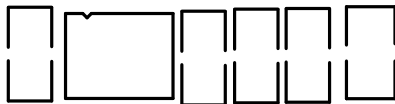


J2

C1

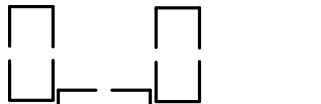


C4 C2 C3 R2

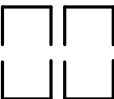


C7

R7

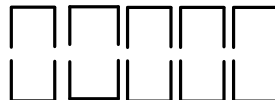


R1

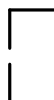


R4 R3

C10 R6 C8 C11 C9



R5



J3



## Design Rules Verification Report

Filename : C:\Users\asus\Desktop\Altium Kurs\Deneme 27 Aralik\Stm32f103\STM32F103

Warnings 0  
Rule Violations 0

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=5mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ( All )	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=7mil) (Max=393.701mil) (Preferred=10mil) (All)	0
Power Plane Connect Rule(Relief Connect )(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole Size Constraint (Min=0.1mil) (Max=100mil) (All)	0
Hole To Hole Clearance (Gap=10mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All)	0
Matched Lengths(Tolerance=1000mil) (InDifferentialPairClass('diff_usb'))	0
Height Constraint (Min=0mil) (Max=1000mil) (Preferred=500mil) (All)	0
Total	0