

BBM433 Microprocessor

Final Exam

E-posta *

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Questions



**Q1**

Draw the memory and input-output address when it is stated that an 8086 null commands in maximum mode with a total memory area of 64K RAM and 512K ROM with 32 Kbytes of ROM and RAM type memory units. (20)

```
MOV    AX, 2000H
MOV    8000H, BH
IN AL, 0AAH
INT    021H
CALL   2500H
```

Q2

Explain how to use it in addressing input/output units, (10)
and create an example with X86 operation commands.(10)

Q3

Discuss what the minimum and maximum mode mean in the 8088/86 processor and what this preference is for.(10)

Q4

In a system with an 8086 microprocessor with a clock frequency of 5 MHz, a peripheral unit with a response time of 500ns is connected via internal data bus.

- a) Create the time diagram for the reading process of this unit. (10)
- b) Find out what time it is for the system to transmit a value received from this unit to a different address of this unit. (10)
- c) Express how this value you find is evaluated as a metric.(10)
- d) What do you think the defined unit could be?(10)

Q5

Write briefly the benefits of creating address and data bus with latch and bus-transiver.(10)

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