

BBM436: Microprocessors Lab.
2020-2021 Autumn
Homework 2

İbrahim Burak Tanrıkulu, 21827852

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1 Phase: Creating workspace

- Firstly i used minecraft to create an 74181 ALU and i did half of it. But doing it in minecraft was time consuming. So i decided to use Proteus.
- I downloaded and installed Proteus and will use it.

2 Phase: Running Examples

- I ran examples and did my first 74181 ALU in Proteus 1 . I checked this ALU with random inputs.

3 Phase: Designing ALU and Register

- I decided to do 16 bit ALU and Registers for to use in next projects.
- I designed 16-bit ALU with 74181 ALU 2 and 74182 Lookahead Carry Generator 3 .
There are 4 piece of 74181 to make 16 bit ALU. Lookahead Carry Generator makes this ALUs faster.
- I designed 16-bit register file with open-collector D Flip-Flops 4 .
Every register is 16 bit length and there are 16 registers in this register file.
I used 16 piece of OR gate to multiplex these register BUSES.
- I checked these circuits with pattern generator 5 and logic analyzer 6 .

4 Phase: Sample 8086 compiler

- I used emu8086 application to simulate 8086 microprocessor. Also i installed MASM32 .
- There is a sample addition/substraction code and simulation of this code. 7
You can run it step by step or completely. While you stepping this commands, You can see assembly codes.

5 Phase: Machine Code Extraction

- I used another addition code to extract machine code. I will use WinHex to view these machine codes.
sample.asm 8
sample.obj 9
sample.exe 10

I can share my Proteus project and Minecraft map.

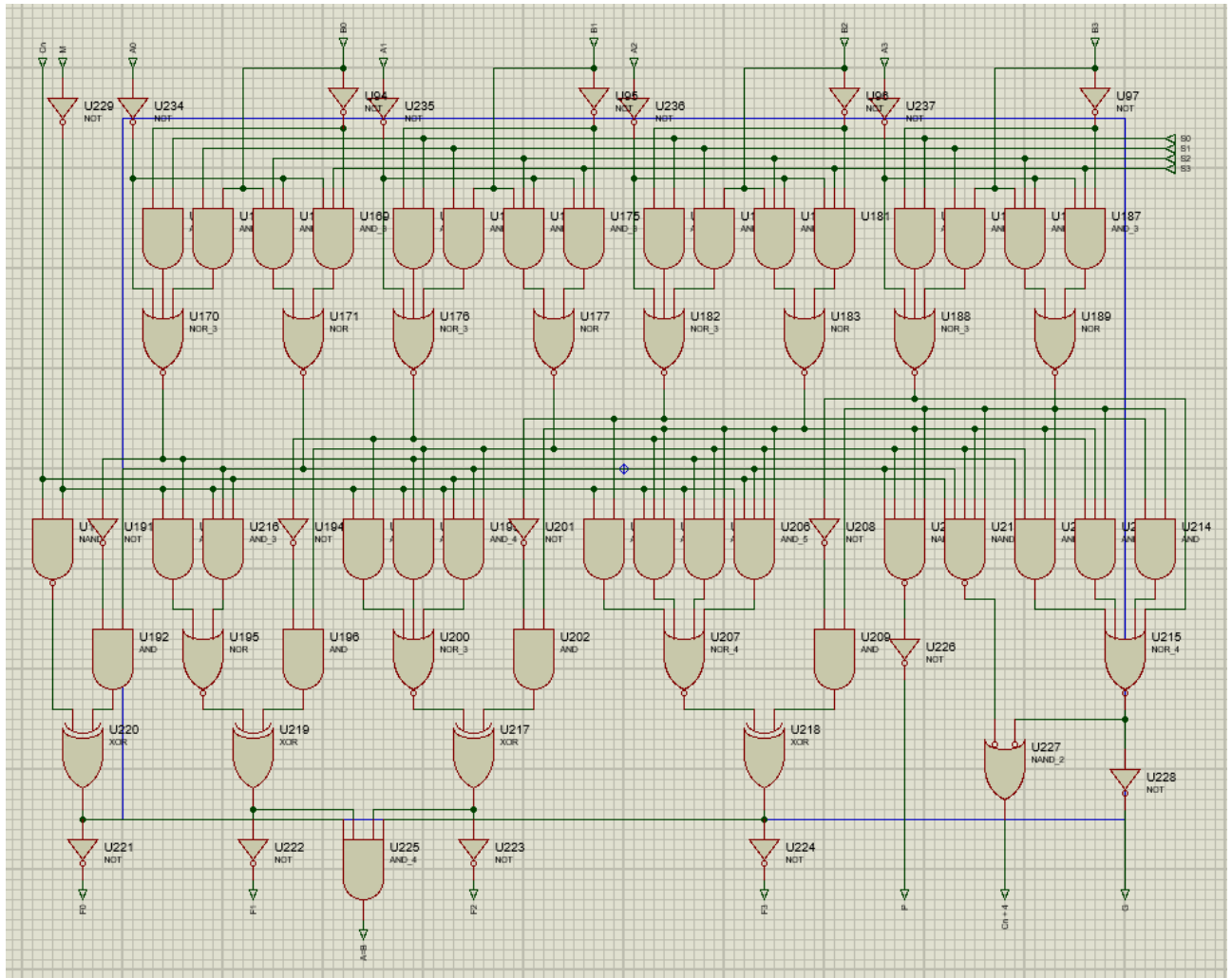


Figure 1: 74181 ALU

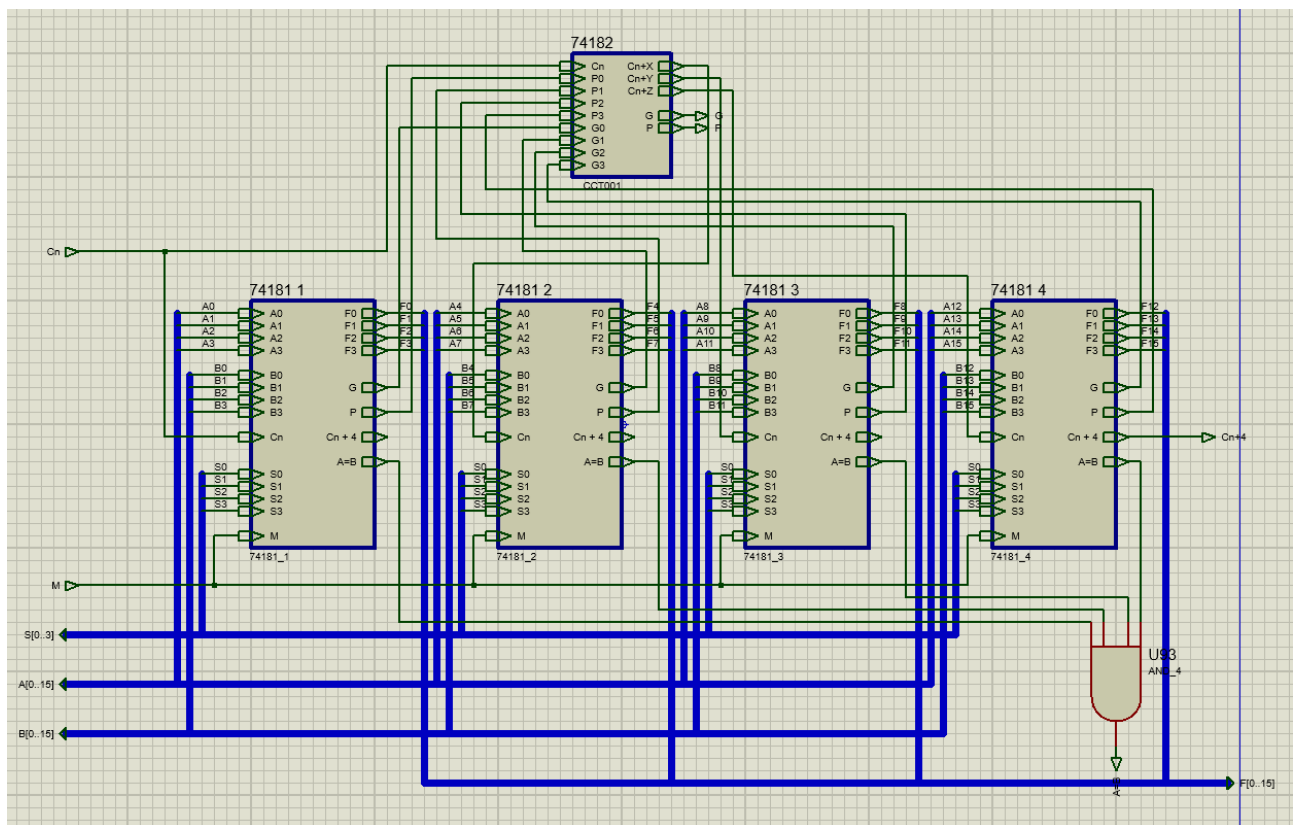


Figure 2: 16-bit ALU

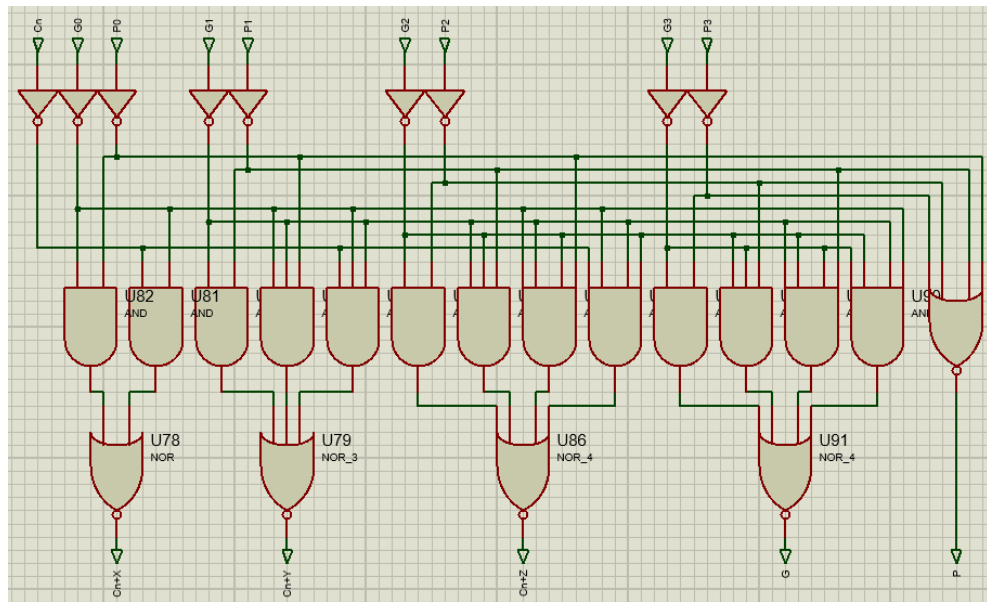


Figure 3: 74182 Lookahead Carry Generator

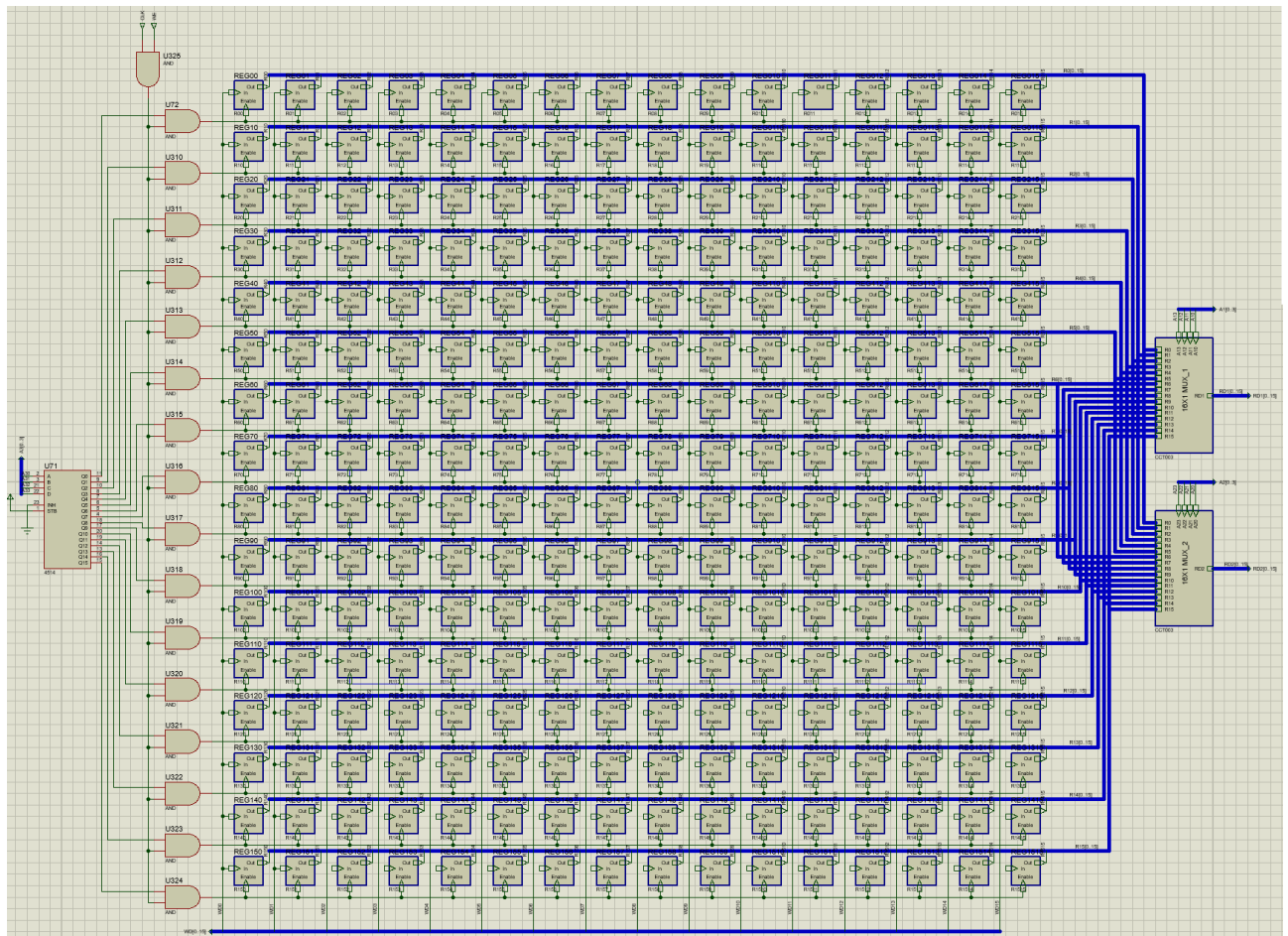


Figure 4: 16-bit Register File

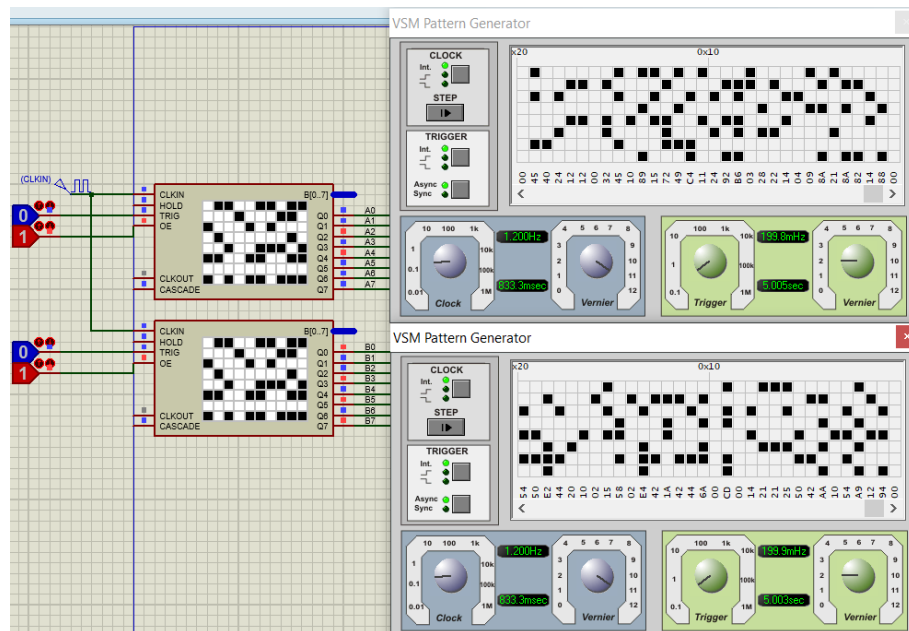


Figure 5: Pattern Generator

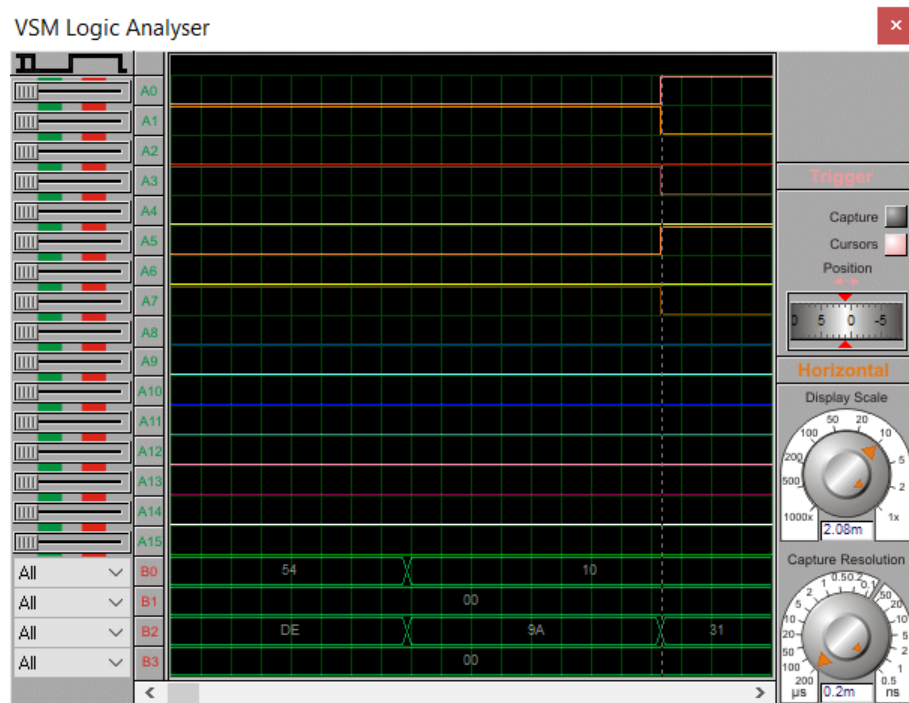


Figure 6: Logic Analyzer

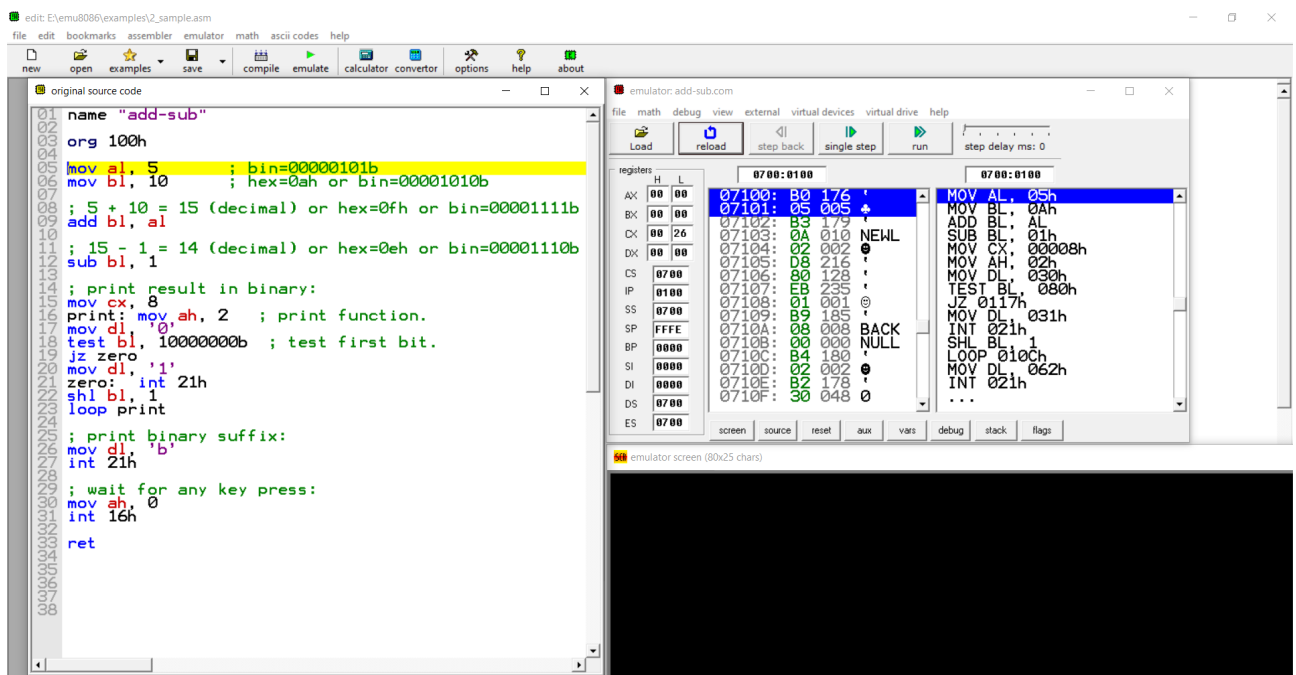


Figure 7: Sample Program in emu8086

2_sample.obj	2_sample.exe	2_sample.asm															ANSI ASCII	
Offset	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15		
00000000	20	20	20	20	2E	34	38	36	0D	0A	20	20	20	20	2E	6D		.486 .m
00000016	6F	64	65	6C	20	66	6C	61	74	2C	20	73	74	64	63	61		odel flat, stdca
00000032	6C	6C	20	20	20	20	20	20	20	20	20	20	20	20	20	20		ll
00000048	20	20	20	20	0D	0A	20	20	20	20	6F	70	74	69	6F	6E		option
00000064	20	63	61	73	65	6D	61	70	20	3A	6E	6F	6E	65	0D	0A		casemap :none
00000080	20	0D	0A	20	20	20	20	69	6E	63	6C	75	64	65	20	5C		include \
00000096	6D	61	73	6D	33	32	5C	69	6E	63	6C	75	64	65	5C	77		masm32\include\w
00000112	69	6E	64	6F	77	73	2E	69	6E	63	0D	0A	20	20	20	20		indows.inc
00000128	69	6E	63	6C	75	64	65	20	5C	6D	61	73	6D	33	32	5C		include \masm32\
00000144	6D	61	63	72	6F	73	5C	6D	61	63	72	6F	73	2E	61	73		macros\macros.as
00000160	6D	0D	0A	20	20	20	20	69	6E	63	6C	75	64	65	20	5C		m include \
00000176	6D	61	73	6D	33	32	5C	69	6E	63	6C	75	64	65	5C	6D		masm32\include\m
00000192	61	73	6D	33	32	2E	69	6E	63	0D	0A	20	20	20	20	69		asm32.inc i
00000208	6E	63	6C	75	64	65	20	5C	6D	61	73	6D	33	32	5C	69		nclude \masm32\i
00000224	6E	63	6C	75	64	65	5C	67	64	69	33	32	2E	69	6E	63		nclude\gdi32.inc
00000240	0D	0A	20	20	20	20	69	6E	63	6C	75	64	65	20	5C	6D		include \m
00000256	61	73	6D	33	32	5C	69	6E	63	6C	75	64	65	5C	75	73		asm32\include\us
00000272	65	72	33	32	2E	69	6E	63	0D	0A	20	20	20	20	69	6E		er32.inc in
00000288	63	6C	75	64	65	20	5C	6D	61	73	6D	33	32	5C	69	6E		clude \masm32\in
00000304	63	6C	75	64	65	5C	6B	65	72	6E	65	6C	33	32	2E	69		clude\kernel32.i
00000320	6E	63	0D	0A	20	20	20	20	69	6E	63	6C	75	64	65	6C		nc includel
00000336	69	62	20	5C	6D	61	73	6D	33	32	5C	6C	69	62	5C	6D		ib \masm32\lib\m
00000352	61	73	6D	33	32	2E	6C	69	62	0D	0A	20	20	20	20	69		asm32.lib i
00000368	6E	63	6C	75	64	65	6C	69	62	20	5C	6D	61	73	6D	33		ncludelib \masm3
00000384	32	5C	6C	69	62	5C	67	64	69	33	32	2E	6C	69	62	0D		2\lib\gdi32.lib
00000400	0A	20	20	20	20	69	6E	63	6C	75	64	65	6C	69	62	20		includelib
00000416	5C	6D	61	73	6D	33	32	5C	6C	69	62	5C	75	73	65	72		\masm32\lib\user
00000432	33	32	2E	6C	69	62	0D	0A	20	20	20	20	69	6E	63	6C		32.lib incl
00000448	75	64	65	6C	69	62	20	5C	6D	61	73	6D	33	32	5C	6C		udelib \masm32\l
00000464	69	62	5C	6B	65	72	6E	65	6C	33	32	2E	6C	69	62	0D		ib\kernel32.lib
00000480	0A	20	20	20	20	2E	63	6F	64	65	20	20	20	20	20	20		.code
00000496	20	20	20	20	20	20	20	20	20	20	20	20	20	20	20	20		
00000512	20	0D	0A	0D	0A	73	74	61	72	74	3A	20	20	20	20	20		start:
00000528	20	20	20	20	20	20	20	20	20	20	20	20	20	20	20	20		
00000544	20	20	20	20	20	0D	0A	20	20	20	20	63	61	6C	6C	20		call
00000560	6D	61	69	6E	20	0D	0A	0D	0A	6D	61	69	6E	20	70	72		main main pr
00000576	6F	63	0D	0A	20	20	20	20	6D	6F	76	20	65	61	78	2C		oc mov eax,
00000592	20	31	30	30	20	20	20	20	20	20	20	20	20	20	20	20		100
00000608	20	20	20	20	0D	0A	20	20	20	20	6D	6F	76	20	65	63		mov ec
00000624	78	2C	20	32	35	30	20	20	20	20	20	20	20	20	20	20		x, 250
00000640	20	20	20	20	20	20	0D	0A	20	20	20	20	61	64	64	20		add
00000656	65	63	78	2C	20	65	61	78	20	20	20	20	20	20	20	20		ecx, eax
00000672	20	20	20	20	20	20	20	20	0D	0A	20	20	20	20	70	72		pr
00000688	69	6E	74	20	73	74	72	24	28	65	63	78	29	20	20	20		int str\$(ecx)
00000704	20	20	20	20	20	20	20	20	0D	0A	0D	0A	6D	61	69	6E		main
00000720	20	65	6E	64	70	0D	0A	0D	0A	65	6E	64	20	73	74	61		endp end sta
00000736	72	74	0D	0A														rt

Figure 8: Assembly Code with WinHex

2_sample.obj	2_sample.exe	2_sample.asm																
Offset	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	ANSI	ASCII
00000000	4C	01	03	00	92	04	A3	5F	88	01	00	00	0E	00	00	00	L	' _ ^
00000016	00	00	00	00	2E	74	65	78	74	00	00	00	00	00	00	00		.text
00000032	00	00	00	00	26	00	00	00	8C	00	00	00	B2	00	00	00	&	& "
00000048	00	00	00	00	04	00	00	00	20	00	50	60	2E	64	61	74		P`.dat
00000064	61	00	00	00	26	00	00	00	00	00	00	00	14	00	00	00	a	&
00000080	DA	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	Ú	
00000096	40	00	50	C0	2E	64	72	65	63	74	76	65	3A	00	00	00	@ PÀ.drectve:	
00000112	00	00	00	00	9A	00	00	00	EE	00	00	00	00	00	00	00	š	î
00000128	00	00	00	00	00	00	00	00	00	0A	00	00	E8	00	00	00		è
00000144	00	B8	64	00	00	00	00	B9	FA	00	00	00	03	C8	68	00	,d	ú Èh
00000160	00	00	51	E8	00	00	00	00	68	00	00	00	00	E8	00	00	Qè	h è
00000176	00	00	12	00	00	00	0B	00	00	00	06	00	18	00	00	00		
00000192	09	00	00	00	14	00	1D	00	00	00	0B	00	00	00	00	06		
00000208	22	00	00	00	0A	00	00	00	14	00	00	00	00	00	00	00	"	
00000224	00	00	00	00	00	00	00	00	00	00	00	00	00	00	00	2D		-d
00000240	65	66	61	75	6C	74	6C	69	62	3A	5C	6D	61	73	6D	33	efaultlib:\masm3	
00000256	32	5C	6C	69	62	5C	6D	61	73	6D	33	32	2E	6C	69	62	2\lib\masm32.lib	
00000272	20	2D	64	65	66	61	75	6C	74	6C	69	62	3A	5C	6D	61	-defaultlib:\ma	
00000288	73	6D	33	32	5C	6C	69	62	5C	67	64	69	33	32	2E	6C	asm32\lib\gdi32.l	
00000304	69	62	20	2D	64	65	66	61	75	6C	74	6C	69	62	3A	5C	ib -defaultlib:\	
00000320	6D	61	73	6D	33	32	5C	6C	69	62	5C	75	73	65	72	33	masm32\lib\user3	
00000336	32	2E	6C	69	62	20	2D	64	65	66	61	75	6C	74	6C	69	2.lib -defaultli	
00000352	62	3A	5C	6D	61	73	6D	33	32	5C	6C	69	62	5C	6B	65	b:\masm32\lib\ke	
00000368	72	6E	65	6C	33	32	2E	6C	69	62	20	2D	65	6E	74	72	ernel32.lib -entr	
00000384	79	3A	73	74	61	72	74	20	2E	66	69	6C	65	00	00	00	y:start .file	
00000400	00	00	00	00	FE	FF	00	00	67	01	32	5F	73	61	6D	70	by g 2_samp	
00000416	6C	65	2E	61	73	6D	00	00	00	00	00	00	40	63	6F	6D	le.asm @ccm	
00000432	70	2E	69	64	FC	20	12	00	FF	FF	00	00	03	00	2E	74	p.idü yy .t	
00000448	65	78	74	00	00	00	00	00	00	00	01	00	00	00	03	01	ext	
00000464	26	00	00	00	04	00	00	00	00	00	00	00	00	00	00	00	&	
00000480	00	00	2E	64	61	74	61	00	00	00	00	00	00	00	02	00	.data	
00000496	00	00	03	01	14	00	00	00	00	00	00	00	00	00	00	00		
00000512	00	00	00	00	00	00	2E	64	72	65	63	74	76	65	00	00	.drectve	
00000528	00	00	03	00	00	00	03	01	9A	00	00	00	00	00	00	00	š	
00000544	00	00	00	00	00	00	00	00	00	00	5F	64	77	74	6F	61	_dwtoa	
00000560	40	38	00	00	00	00	00	00	20	00	02	00	00	00	00	00	@8	
00000576	04	00	00	00	00	00	00	00	00	00	20	00	02	00	3F	3F		??
00000592	30	30	31	39	00	00	00	00	00	00	02	00	00	00	03	00	0019	
00000608	5F	73	74	61	72	74	00	00	00	00	00	00	01	00	20	00	_start	
00000624	02	00	5F	6D	61	69	6E	40	30	00	05	00	00	00	01	00	_main@0	
00000640	20	00	02	00	0E	00	00	00	5F	53	74	64	4F	75	74	40	_StdOut@	
00000656	34	00															4	

Figure 9: Object Code with WinHex

