

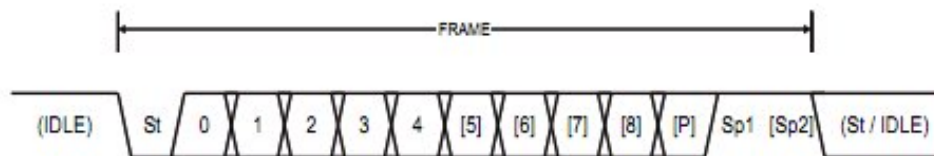
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ASSIGNMENT 6

Topic

Asynchronous Serial Communication with Bit Banging



St Start bit, always low.

(n) Data bits (0 to 8).

P Parity bit. Can be odd or even.

Sp Stop bit, always high.

IDLE No transfers on the communication line (RxDn or TxDn). An IDLE line must be high.

Hardware and software that will create the serial communication model given in the picture over a one bit output will be realized.

Submit your work file by attaching it to your report.

Phase 1

Perform the system on the proteus with one side of the serial terminal. Set the communication speed as 9600 Baud, 8 bit, Non Parity, 1 Stop Bit taking into account the features given in the picture.

Phase 2

Create the circuit in the first phase with interruption. Build its implementation as software and hardware.

Phase 3

Briefly write your comments on the application and usage between Bit Banging and interrupt based implementations in your report.