

LM124/LM224/LM324/LM2902 Low Power Quad Operational Amplifiers

General Description

The LM124 series consists of four independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, DC gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM124 series can be directly operated off of the standard $\pm5\mathrm{V}$ power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional $\pm\,15\mathrm{V}$ power supplies.

Unique Characteristics

- In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage
- The unity gain cross frequency is temperature compensated
- The input bias current is also temperature compensated

Advantages

- Eliminates need for dual supplies
- Four internally compensated op amps in a single package
- Allows directly sensing near GND and V_{OUT} also goes to GND
- Compatible with all forms of logic
- Power drain suitable for battery operation

Features

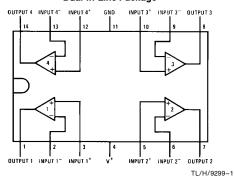
- Internally frequency compensated for unity gain
- Large DC voltage gain 100 dB ■ Wide bandwidth (unity gain) 1 MHz
- Wide bandwidth (unity gain) (temperature compensated)
- Wide power supply range:

Single supply 3V to 32V or dual supplies ± 1.5 V to ± 16 V

- Very low supply current drain (700 μA)—essentially independent of supply voltage
- Low input biasing current (temperature compensated) 45
- Low input offset voltage 2 mV and offset current 5 nA
- Input common-mode voltage range includes ground
- Differential input voltage range equal to the power supply voltage
- Large output voltage swing $0V \text{ to } V^+ 1.5V$

Connection Diagram

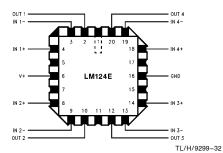
Dual-In-Line Package



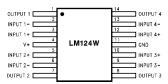
Top View

Order Number LM124J, LM124AJ, LM124J/883**, LM124AJ/883*, LM224J, LM224AJ, LM324J, LM324M, LM324AM, LM2902M, LM324N, LM324AN or LM2902N See NS Package Number J14A, M14A or N14A

*LM124A available per JM38510/11006 **LM124 available per JM38510/11005



Order Number LM124AE/883 or LM124E/883 See NS Package Number E20A



TL/H/9299-33

Order Number LM124AW/883 or LM124W/883 See NS Package Number W14B

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Absolute Maximum Ratings If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 9)

(2,2,2,2)					
	LM124/LM224/LM324 LM124A/LM224A/LM324A	LM2902		LM124/LM224/LM324 LM124A/LM224A/LM324A	LM2902
Supply Voltage, V ⁺	32V	26V	Storage Temperature Range	-65°C to +150°C	-65°C to +150°C
Differential Input Voltage	32V	26V	Lead Temperature (Soldering, 10 seconds) 260°C	seconds) 260°C	260°C
Input Voltage	-0.3V to $+32V$	-0.3V to $+26V$	Soldering Information		
Input Current			Dual-In-Line Package		
(V _{IN} < -0.3V) (Note 3)	50 mA	50 mA	Soldering (10 seconds)	260°C	260°C
Power Dissipation (Note 1)			Small Outline Package		Ü
Molded DIP	1130 mW	1130 mW	vapor Pnase (ou seconds)) 5000 0000	ار ان ان
Cavity DIP	1260 mW	1260 mW	Infrared (15 seconds)	22022	220-52
Small Outline Package	800 mW	800 mW	See AN-450 "Surface Mounting Methods and Their Effect on Product Reliability" for	Methods and Their Effect on Prod	luct Reliability" for
OND of timering Short-Circuit to GND			other methods of soldering surface mount devices.	se mount devices.	
(One Amplifier) (Note 2)			ESD Tolerance (Note 10)	250V	250V
$\dot{ m V}^+ \le 15 \dot{ m V}$ and $\dot{ m T}_{ m A} = 25^\circ m C$	Continuous	Continuous			
Operating Temperature Range		-40°C to +85°C			
LM324/LM324A	0°C to +70°C				
LM224/LM224A	-25°C to +85°C				
LM124/LM124A	-55°C to +125°C				

Electrical Characteristics $V^+ = +5.0V$, (Note 4), unless otherwise stated

2																		
	Darameter	Conditions	LM	LM124A	_	LM224A		LM324A	1A	LM1	LM124/LM224	4	LM324	24		LM2902		l it
			Min Typ	Мах	Min Typ	yp Max		Min Typ	Max	Min T	Min Typ Max		Min Typ	Max	Min Typ		Мах	8
lub	Input Offset Voltage	(Note 5) T _A = 25°C	-	2		1 3		2	3		2 5		2	7		2	7	m/
Inpu	Input Bias Current (Note 6)	$I_{IN(+)}$ or $I_{IN(-)}$, $V_{CM} = 0V$, $T_A = 25^{\circ}C$	20	90	,	40 80		45	100	,	45 150	0	45	250	,	45	250	nA
Inpu	Input Offset Current	$I_{IN(+)} - I_{IN(-)}, V_{CM} = 0V,$ $T_A = 25^{\circ}C$	2	10		2 15	·	5	30		3 30		2	20		5	50	nA
Volt	Input Common-Mode Voltage Range (Note 7)	$V^{+} = 30V$, (LM2902, $V^{+} = 26V$), $T_{A} = 25^{\circ}C$	0	V ⁺ -1.5	0	V ⁺ -1.5	.5	_	V ⁺ -1.5	0	V ⁺ -1.5	1.5 0		V ⁺ -1.5	0	>	V ⁺ -1.5	>
dns	Supply Current	Over Full Temperature Range $R_L = \infty$ On All Op Amps $V^+ = 30V$ (LM2902 $V^+ = 26V$) $V^+ = 5V$	1.5	e <u>†</u>		1.5 3 0.7 1.2		1.5	3 1.2	+ 0	1.5 3 0.7 1.2		1.5	8 1.2		1.5	s 1.	μ
Larg	Large Signal Voltage Gain	$V^+ = 15V, R_L \ge 2 k\Omega, \\ (V_O = 1V to 11V), T_A = 25^\circ C$	50 100		50 1	100	25	100		50 100	00	Ö	25 100		25 1	100		V/m/V
Cor	Common-Mode Rejection Ratio	DC, $V_{CM} = 0V \text{ to } V^+ - 1.5V$, $T_A = 25^{\circ}C$	70 85		20	85	65	85		3 02	85	9	65 85		. 09	70		фВ
Pow	Power Supply Rejection Ratio	$V^{+} = 5V \text{ to } 30V$ (LM2902, $V^{+} = 5V \text{ to } 26V$), $T_{A} = 25^{\circ}C$	65 100		65 1	100	65	100		65 100	00	9	65 100		50 100	001		ф

	LM2902 Units	Typ Max	-120 dB	40	50	50 μA	40 60 mA	10 mV		7 μV/°C	200	200	200	200 500 V + -2	200 200 200	200 200 000	200 200 000 000
	_	Ā	I	700 7	10	12 6	,		_					0	0 2 15	22 15	22 23 23
	LM324	Тур Мах	-120	40	20	50	40 60	6		7	150			150	150	150	150
	_	Min	Ì	20 4	10 2	12 5	7						1 4	0	0 0 15	0 0 15 26	0 0 15 26 26 27
	LM224	Мах	0				09	7			100	100	100	100 300 V ⁺ -2			
	LM124/LM224	Min Typ	-120	20 40	10 20	12 50	40		^	,	,	10	10 10 440	, 10 10 0			
f		Max				<u> </u>	09	2	30	_	75	300	300	N	10 0 0 1	10 0 0 1	0 0 0
	LM324A	Min Typ	-120	20 40	10 20	12 50	40		7			10	10 40		0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	0 0 0	10 10 40 28
ntinued)	Ą	Max M		7	-	-	09	4	20		30	30	30 200 100		0 0 0	0 0 0	0 0 0
ated (Cor	LM224A	n Typ	-120	0 40) 20	5 50	40		7			10	10 40	0	0 4 40	01 04	10 10 40 28
erwise sta		Max Min		20	10	12	09	4	20	_	30	30	30 200 100	200 100 + -2 0	0 00 0	0 00 00	0 00 00 -
less othe	LM124A	Тур	-120	40	20	50	40		7			10 2		>			
te 4) un		Ā		20	10	12								0	0 0 52	56 25 0	26 25 27 27
$\mathbf{S} \mathbf{V}^+ = +5.0 \mathrm{V} (\mathrm{Note} 4) \mathrm{unless} \mathrm{otherwise} \mathrm{stated} (\mathrm{Continued})$	Conditions		- 1z, T _A = 25°C	$= 0V,$ $2V, T_A = 25^{\circ}C$	= 0V, $2V, T_A = 25^{\circ}C$	$= 0V,$ 200 mV, $T_A = 25^{\circ}C$	$= 15V, T_A = 25^{\circ}C$				M = 0	V0 = M0	NO = WO), V _{CM} = 0V	CM = 0V 26V)	CM = 0V 26V) 0 11V) R _L = 2 kΩ	
aracteristic	S		$f=1\mathrm{kHz}$ to 20 kHz, T_A (Input Referred)	$V_{IN}^{+} = 1V, V_{IN}^{-}$ $V_{+}^{+} = 15V, V_{O} =$	$V_{IN}^{-} = 1V, V_{IN}^{+} = V_{V}^{+}$	$V_{IN}^{-} = 1V, V_{IN}^{+}$ $V^{+} = 15V, V_{O} =$	(Note 2) $V^+ = 15$	(Note 5)	$R_S=0\Omega$		$l_{IN(+)} - l_{IN(-)}$, $V_{CM} = 0V$	$\frac{\ln(+) - \ln(-)}{R_S} = 0\Omega$	$I_{IN(+)} - I_{IN(-)}, V_I$ $R_S = 0.0$ $I_{IN(+)} \text{ or } I_{IN(-)}$	$\ln(+) - \ln(-)$, V, $\ln(+) = 0$ $\ln(+) \text{ or } \ln(-)$ $V^{+} = +30V$ (LM2902, V ⁺ = 2	$\begin{split} I_{N(+)} - I_{IN(-)}, V_{CA} \\ R_S &= 0 \Omega \\ I_{IN(+)} \text{ or } I_{IN(-)} \\ V^+ &= +30 V \\ (LM2902, V^+ &= 26 \\ V^+ &= +15 V \\ (V_O Swing = 1V \text{ to} \\ R_L &\geq 2 \text{ k} \Omega \end{split}$	$R_{S} = 0\Omega$ $R_{S} = 0\Omega$ $V^{+} = +30V$ $V^{+} = +30V$ $V^{+} = +15V$ $V^{+} = +15V$ $V^{+} = +15V$ $V^{+} = 30V$ $V^{+} = 30V$	$R_{S} = 0\Omega$ $R_{S} = 0\Omega$ $V = +30V$ $V^{+} = +30V$ $V^{+} = +15V$ $V^{-} = +15V$ $V^{-} = +15V$ $V^{+} = 30V$
Electrical Characteristics	Parameter		Amplifier-to-Amplifier Coupling (Note 8)	Output Current Source $V_{IN}^+ = 1V, V_{IN}^-$ $V^+ = 15V, V_O =$	Sink		Short Circuit to Ground	Input Offset Voltage	Input Offset Voltage Drift		Input Offset Current	Input Offset Current Input Offset Current Drift	Input Offset Current Input Offset Current Drift Input Bias Current	Input Offset Current Input Offset Current Drift Input Bias Current Input Common-Mode Voltage Range (Note 7)	Input Offset Current Input Offset Current Drift Input Bias Current Input Common-Mode Voltage Range (Note 7) Large Signal Voltage Gain		Input Offset Current Input Offset Current Drift Input Bias Current Input Common-Mode Voltage Range (Note 7) Large Signal Voltage Gain Output Voltage Swing

Electrical Characteristics $V^+ = +5.0V$ (Note 4) unless otherwise stated (Continued)

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902	Min Typ Max				
LM2902	Ι Tyl	10 20	ω		
	Mir	10	.CJ		
54	Ma				
LM324	Тур	10 20	80		
	Min	10	2		
LM124/LM224	Max				
124/L	Тур	10 20	ω		
LM	Min	10	Ω		
۷	Мах				
LM324A	Тур	20	80		
_	Min	10 20	2		
۷	Max				
LM224A	Тур	20	80		
1	Min	10 20	2		
۷	Мах				
LM124A	Тур	20	15		
_	Min	10 20	10 15		
suo	2	$^{+} = +1V,$ $^{-} = 0V, V^{+} = 15V$	= +1V, = 0V, $V^+ = 15V$		
Conditions		+ _ N			
		Current Source V _O = 2V			
ī	5	Source	Sink		
Parametel	5	Output Current			

Note 1: For operating at high temperatures, the LM324/LM324A/LM2902 must be derated based on a + 125°C maximum junction temperature and a thermal resistance of 88°C/W which applies for the device soldered in a printed circuit board, operating in a still air ambient. The LM224/LM224A and LM124A can be derated based on a + 150°C maximum junction temperature. The dissipation is the total of all four amplifiers—use external resistors, where possible, to allow the amplifier to saturate of to reduce the power which is dissipated in the integrated circuit. Note 2: Short circuits from the output to V ⁺ can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of V⁺. At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers. Note 3: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input the properties of the op amps to go to the V⁺ voltage level (or to ground for a large clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the op amps to go to the V⁺ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3V (at 25°C). **Note 4:** These specifications are limited to -55° C $\leq T_{A} \leq +125^{\circ}$ C for the LM124/LM124A. With the LM224/LM224A, all temperature specifications are limited to -25° C $\leq T_{A} \leq +85^{\circ}$ C, the LM324/LM324A temperature specifications are limited to 0° C \leq T_A \leq + 70 $^{\circ}$ C, and the LM2902 specifications are limited to -40° C \leq T_A \leq + 85 $^{\circ}$ C.

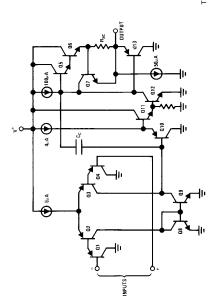
Note 5: $V_0 \approx 1.4V$, $R_S = 0.0$ with V⁺ from 5V to 30V; and over the full input common-mode range (0V to V⁺ - 1.5V) for LM2902, V⁺ from 5V to 26V.

Note 7: The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.30 (at 25°C). The upper end of the common-mode voltage range is V + - 1.5V (at 28°C), but either or both Note 6: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines. inputs can go to $+32 {\sf V}$ without damage ($+26 {\sf V}$ for LM2902), independent of the magnitude of ${\sf V}^+$

Note 8: Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies. Note 9: Refer to RETS124AX for LM124A military specifications and refer to RETS124X for LM124 military specifications.

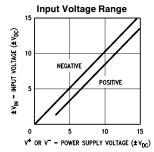
Note 10: Human body model, 1.5 k Ω in series with 100 pF.

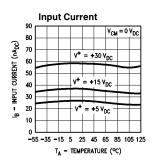
Schematic Diagram (Each Amplifier)

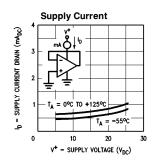


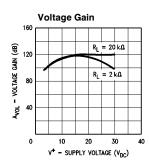
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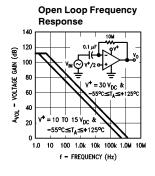


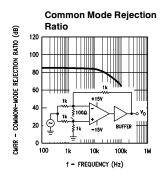


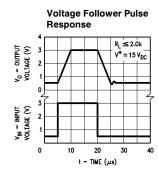


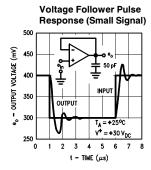


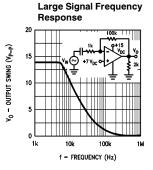


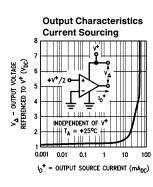


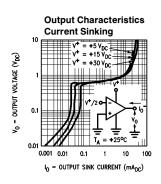


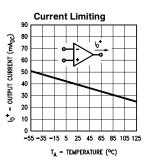




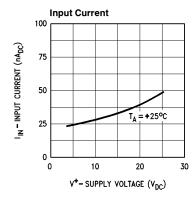


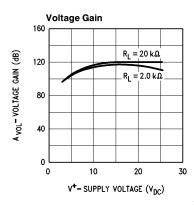






Typical Performance Characteristics (LM2902 only)





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Application Hints

The LM124 series are op amps which operate with only a single power supply voltage, have true-differential inputs, and remain in the linear mode with an input common-mode voltage of 0 V $_{DC}$. These amplifiers operate over a wide range of power supply voltage with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 2.3 V $_{DC}$.

The pinouts of the package have been designed to simplify PC board layouts. Inverting inputs are adjacent to outputs for all of the amplifiers and the outputs have also been placed at the corners of the package (pins 1, 7, 8, and 14). Precautions should be taken to insure that the power supply

for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a test socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.

Large differential input voltages can be easily accommodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger than V $^+$ without damaging the device. Protection should be provided to prevent the input voltages from going negative more than $-0.3~V_{DC}$ (at 25°C). An input clamp diode with a resistor to the IC input terminal can be used.

To reduce the power supply drain, the amplifiers have a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents. Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

For ac applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and prevent crossover distortion.

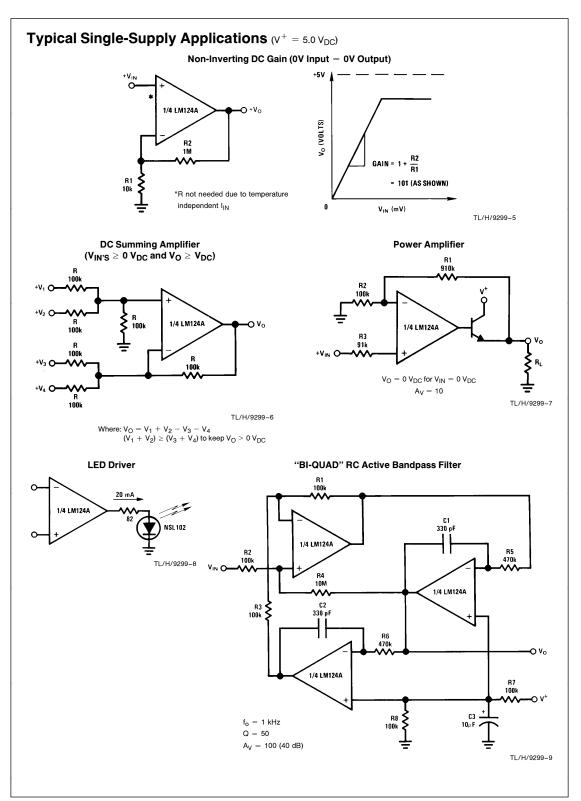
Where the load is directly coupled, as in dc applications, there is no crossover distortion.

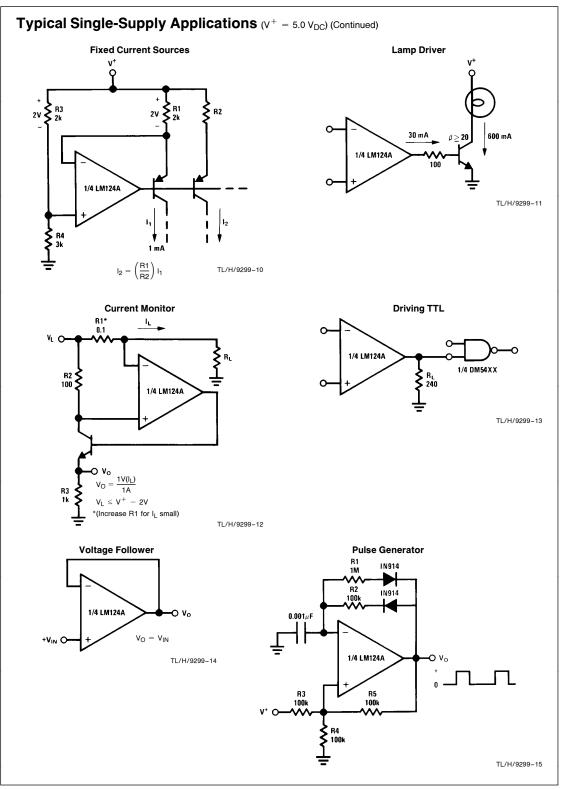
Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50 pF can be accommodated using the worst-case non-inverting unity gain connection. Large closed loop gains or resistive isolation should be used if larger load capacitance must be driven by the amplifier.

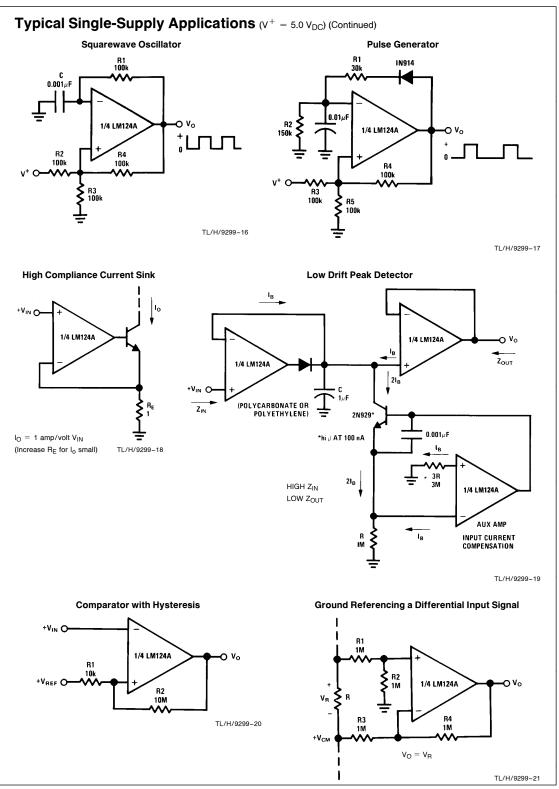
The bias network of the LM124 establishes a drain current which is independent of the magnitude of the power supply voltage over the range of from 3 V_{DC} to 30 $V_{DC}.$

Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive junction temperatures. Putting direct short-circuits on more than one amplifier at a time will increase the total IC power dissipation to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers. The larger value of output source current which is available at 25°C provides a larger output current capability at elevated temperatures (see typical performance characteristics) than a standard IC op amp.

The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used. In general, introducing a pseudo-ground (a bias voltage reference of V + /2) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.

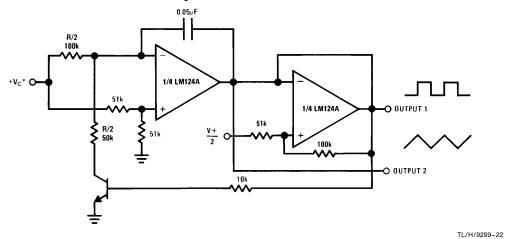






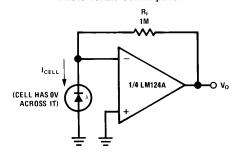
Typical Single-Supply Applications ($V^+ = 5.0 V_{DC}$) (Continued)

Voltage Controlled Oscillator Circuit



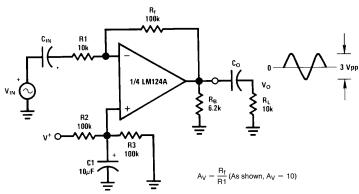
*Wide control voltage range: 0 $V_{DC} \leq V_{C} \leq$ 2 (V $^{+}~-1.5~V_{DC})$

Photo Voltaic-Cell Amplifier



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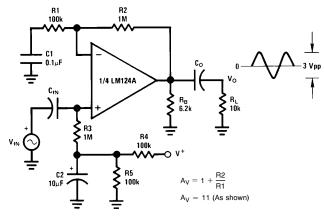
AC Coupled Inverting Amplifier



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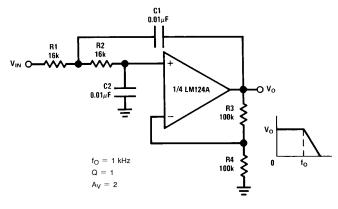
Typical Single-Supply Applications ($V^+ = 5.0 V_{DC}$) (Continued)

AC Coupled Non-Inverting Amplifier



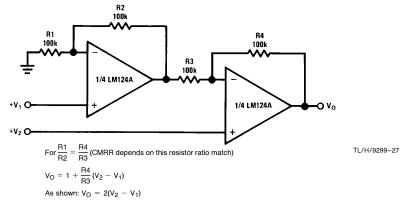
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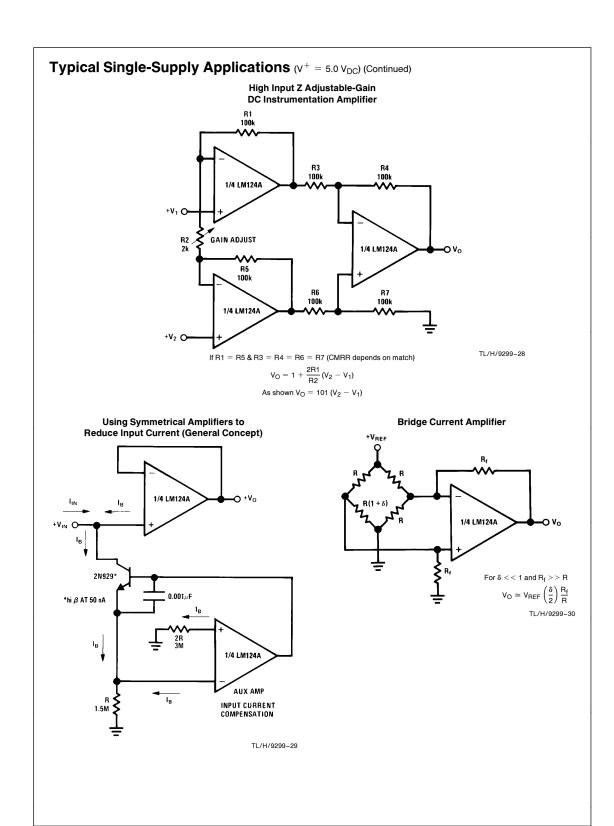
DC Coupled Low-Pass RC Active Filter



TL/H/9299-26

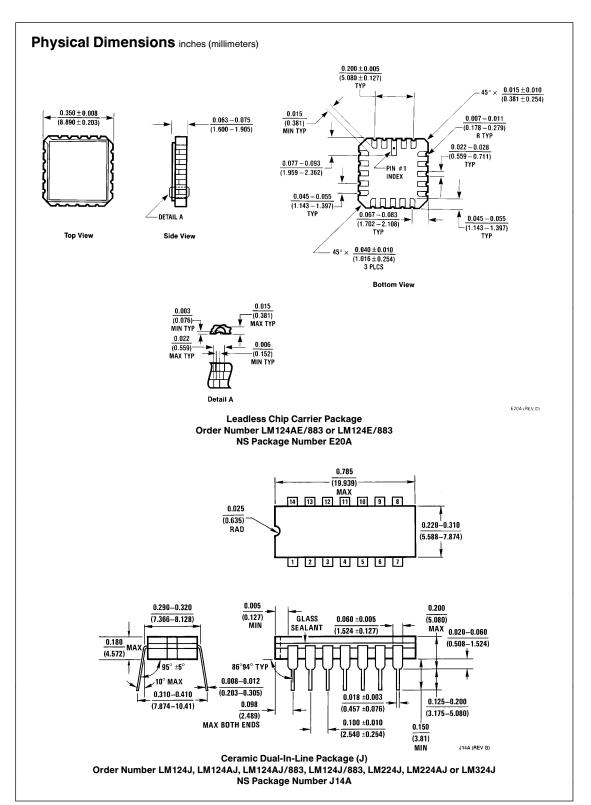
High Input Z, DC Differential Amplifier

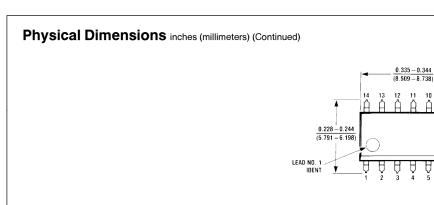


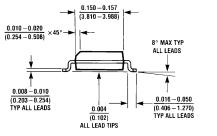


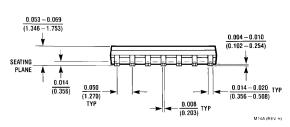
Typical Single-Supply Applications (v⁺ = 5.0 V_{DC}) (Continued) Bandpass Active Filter C1 0.01 R2 0.01 R4 620 R5 1/4 LM124A R6 1/4 LM124A Vo Vo Vin C3 R7 100 R8 100 Vin C3 R7 100 R8 100 Vin C3 R8 100 Vin C4 R8 100 Vin C5 R8 100 Vin C5 R8 100 Vin C6 R8 100 Vin C7 R8 Vin C7 Vin C7 R8 Vin C7 Vin C7 R8 Vin C7 Vin C7 R8 Vin C7 Vin C7

TL/H/9299-31



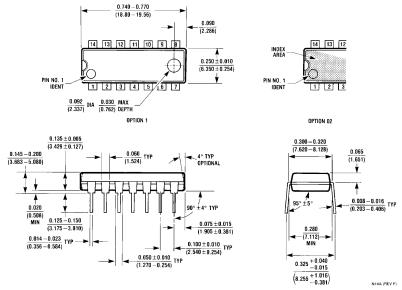






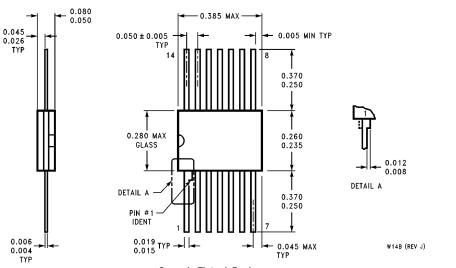
0.010 (0.254) MAX

S.O. Package (M) Order Number LM324M, LM324AM or LM2902M NS Package Number M14A



Molded Dual-In-Line Package (N) Order Number LM324N, LM324AN or LM2902N NS Package Number N14A

Physical Dimensions inches (millimeters) (Continued)



Ceramic Flatpak Package Order Number LM124AW/883 or LM124W/883 NS Package Number W14B

LIFE SUPPORT POLICY

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- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and whose failure to perform, when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.



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