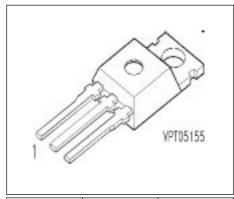
SIPMOS ® Power Transistor

- N channel
- Enhancement mode
- Avalanche-rated



Pin 1	Pin 2	Pin 3		
G	D	S		

Туре	V _{DS}	I _D	R _{DS(on)}	Package	Ordering Code
BUZ 21	100 V	21 A	$0.085~\Omega$	TO-220 AB	C67078-S1308-A2

Maximum Ratings

Parameter	Symbol	Values	Unit
Continuous drain current	I _D		А
$T_{\rm C}$ = 25 °C		21	
Pulsed drain current	/ _{Dpuls}		
$T_{\rm C}$ = 25 °C		84	
Avalanche current, limited by T_{jmax}	I _{AR}	21	
Avalanche energy, periodic limited by T_{jmax}	E _{AR}	11	mJ
Avalanche energy, single pulse	E _{AS}		
$I_{\rm D} = 21 \text{ A}, \ V_{\rm DD} = 25 \text{ V}, \ R_{\rm GS} = 25 \ \Omega$			
$L = 340 \mu H, T_j = 25 °C$		100	
Gate source voltage	V _{GS}	± 20	V
Power dissipation	P _{tot}		W
$T_{\rm C} = 25~{\rm ^{\circ}C}$		75	
Operating temperature	T _j	-55 + 150	°C
Storage temperature	T _{stg}	-55 + 150	
Thermal resistance, chip case	R _{thJC}	≤ 1.67	K/W
Thermal resistance, chip to ambient	R _{thJA}	75	
DIN humidity category, DIN 40 040		E	
IEC climatic category, DIN IEC 68-1		55 / 150 / 56]



Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain- source breakdown voltage	V _{(BR)DSS}				V
$V_{\rm GS}$ = 0 V, $I_{\rm D}$ = 0.25 mA, $T_{\rm j}$ = 25 °C		100	-	-	
Gate threshold voltage	V _{GS(th)}				
$V_{\text{GS}} = V_{\text{DS}}$, $I_{\text{D}} = 1 \text{ mA}$		2.1	3	4	
Zero gate voltage drain current	I _{DSS}				μA
$V_{\rm DS}$ = 100 V, $V_{\rm GS}$ = 0 V, $T_{\rm j}$ = 25 °C		-	0.1	1	
$V_{\rm DS} = 100 \text{ V}, \ V_{\rm GS} = 0 \text{ V}, \ T_{\rm j} = 125 \text{ °C}$		-	10	100	
Gate-source leakage current	I _{GSS}				nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$		-	10	100	
Drain-Source on-resistance	R _{DS(on)}				Ω
$V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$		-	0.065	0.085	



Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Dynamic Characteristics					
Transconductance	g _{fs}				S
$V_{DS} \ge 2 * I_D * R_{DS(on)max}, I_D = 13 A$		8	11	-	
Input capacitance	C_{iss}				pF
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	1000	1300	
Output capacitance	C_{oss}				
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	300	530	
Reverse transfer capacitance	C_{rss}				
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	150	240	
Turn-on delay time	$t_{d(on)}$				ns
$V_{\rm DD} = 30 \; \rm V, \; V_{\rm GS} = 10 \; \rm V, \; I_{\rm D} = 3 \; \rm A$					
$R_{\rm GS} = 50 \ \Omega$		-	25	40	
Rise time	t_{r}				
$V_{\rm DD} = 30 \; \rm V, \; V_{\rm GS} = 10 \; \rm V, \; I_{\rm D} = 3 \; \rm A$					
$R_{\rm GS} = 50 \ \Omega$		-	50	75	
Turn-off delay time	$t_{d(off)}$				
$V_{\rm DD} = 30 \; \rm V, \; V_{\rm GS} = 10 \; \rm V, \; I_{\rm D} = 3 \; \rm A$					
$R_{\rm GS} = 50 \ \Omega$		-	160	210	
Fall time	$t_{\rm f}$				
$V_{\rm DD} = 30 \; {\rm V}, \; V_{\rm GS} = 10 \; {\rm V}, \; I_{\rm D} = 3 \; {\rm A}$					
$R_{\rm GS}$ = 50 Ω		-	80	110	

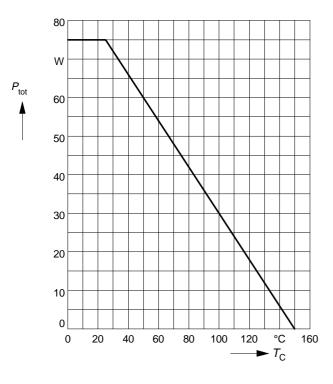


Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Reverse Diode					
Inverse diode continuous forward current	Is				А
<i>T</i> _C = 25 °C		-	-	21	
Inverse diode direct current,pulsed	/ _{SM}				
<i>T</i> _C = 25 °C		-	-	84	
Inverse diode forward voltage	V_{SD}				V
$V_{GS} = 0 \text{ V}, I_{F} = 42 \text{ A}$		-	1.3	1.7	
Reverse recovery time	t _{rr}				ns
$V_{R} = 30 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$		-	150	-	
Reverse recovery charge	Q _{rr}				μC
$V_{R} = 30 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$		-	0.48	-	

Power dissipation

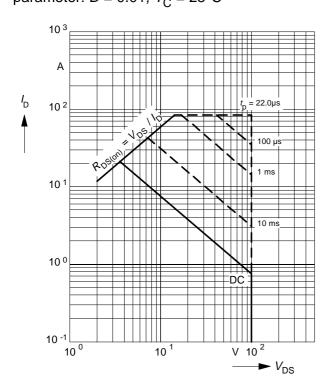
$$P_{\mathsf{tot}} = f(T_{\mathsf{C}})$$



Safe operating area

$$I_{\mathsf{D}} = f(V_{\mathsf{DS}})$$

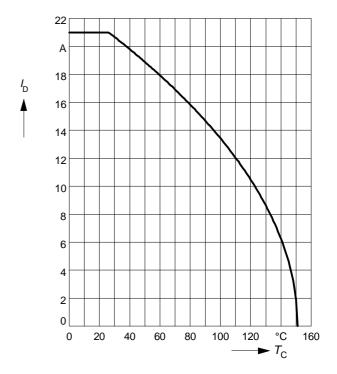
parameter: D = 0.01, $T_C = 25$ °C



Drain current

 $I_{\mathsf{D}} = f(T_{\mathsf{C}})$

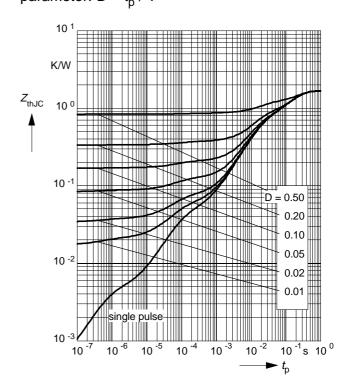
parameter: $V_{GS} \ge 10 \text{ V}$



Transient thermal impedance

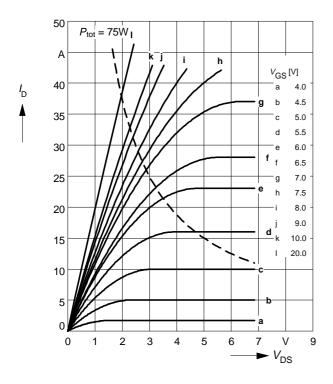
$$Z_{\mathsf{th\ JC}} = f(t_{\mathsf{p}})$$

parameter: $D = t_p / T$



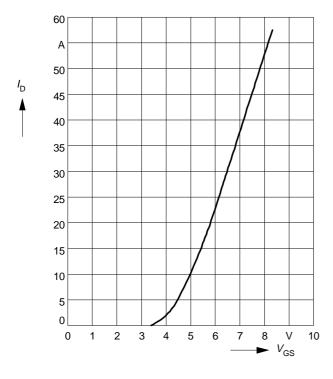
Typ. output characteristics

 $I_{\rm D} = f(V_{\rm DS})$ parameter: $t_{\rm p} = 80~\mu{\rm s}$



Typ. transfer characteristics $I_D = f(V_{GS})$

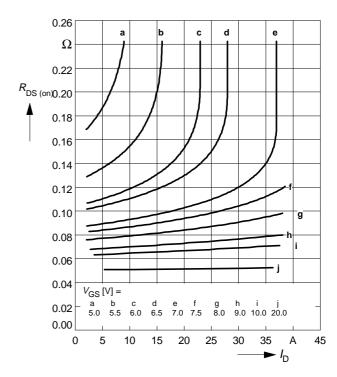
parameter: $t_p = 80 \mu s$ $V_{DS} \ge 2 \times I_D \times R_{DS(on)max}$



Typ. drain-source on-resistance

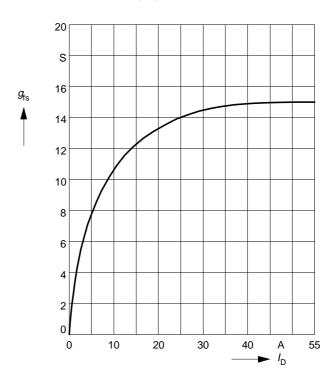
 $R_{\mathrm{DS}\;(\mathrm{on})} = f(I_{\mathrm{D}})$

parameter: $t_p = 80 \mu s$, $T_j = 25 °C$



Typ. forward transconductance $g_{fS} = f(I_D)$

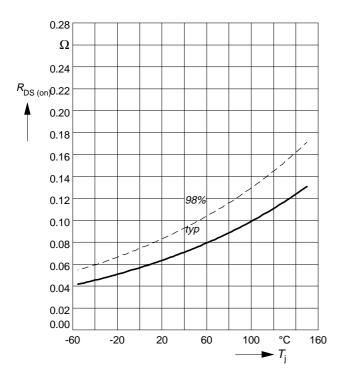
parameter: $t_p = 80 \mu s$, $V_{DS} \ge 2 \times I_D \times R_{DS(on)max}$



Drain-source on-resistance

 $R_{\text{DS (on)}} = f(T_{\text{j}})$

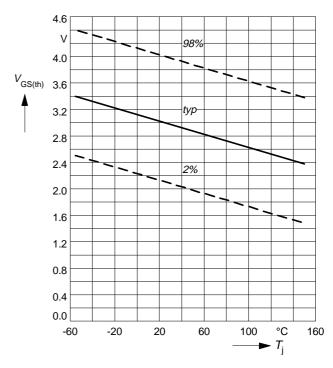
parameter: $I_D = 13 \text{ A}$, $V_{GS} = 10 \text{ V}$



Gate threshold voltage

 $V_{GS (th)} = f(T_j)$

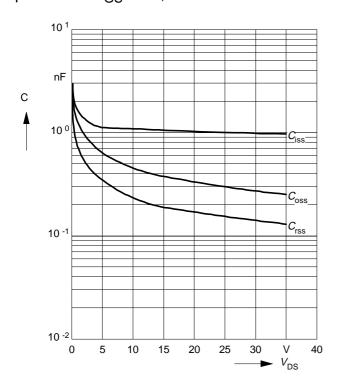
parameter: $V_{GS} = V_{DS}$, $I_{D} = 1 \text{ mA}$



Typ. capacitances

 $C = f(V_{DS})$

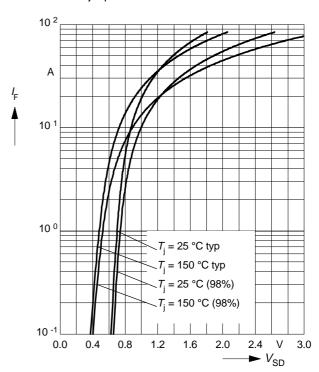
parameter: $V_{GS} = 0V$, f = 1MHz



Forward characteristics of reverse diode

 $I_{\mathsf{F}} = f(V_{\mathsf{SD}})$

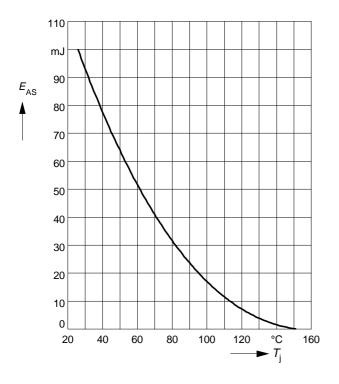
parameter: T_i , $t_p = 80 \mu s$



Avalanche energy $E_{AS} = f(T_j)$

parameter: $I_D = 21 \text{ A}$, $V_{DD} = 25 \text{ V}$

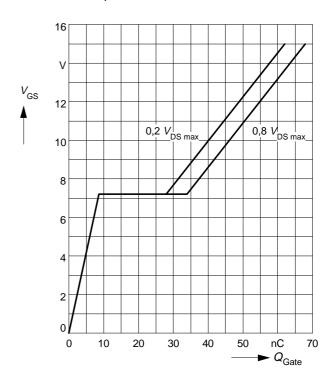
 $R_{\rm GS}$ = 25 Ω , L = 340 μH



Typ. gate charge

 $V_{\mathsf{GS}} = f(Q_{\mathsf{Gate}})$

parameter: $I_{D \text{ puls}} = 36 \text{ A}$



Drain-source breakdown voltage

$$V_{(BR)DSS} = f(T_i)$$

