**Recommended Land Patterns** 

KC2520Z

KC2016Z

KC3225Z





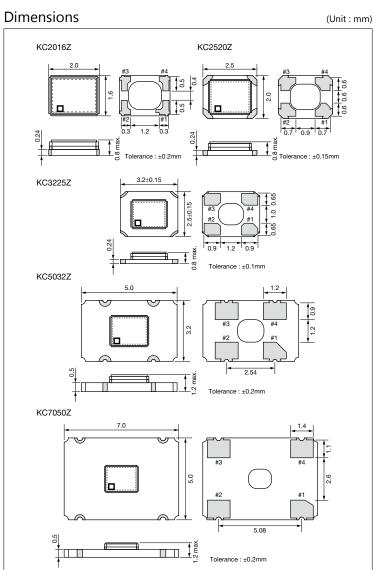


(Unit:mm)

# CMOS/ 1.8V, 2.5V, 3.3V/ 2.0×1.6, 2.5×2.0, 3.2×2.5, 5.0×3.2, 7.0×5.0mm



RoHS Compliant



#3 #4 #4 CC #1 #1 Tolerance : ±0.2mm	KC5032Z
#3 #4 #4 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0 \$0	KC7050Z 5.08
Stand-by Function Pad1 Pad3 (Output) Open Active	

Pad Connections				
#1	Stand-by Function			
#2	Case GND			
#3	Output			
#4	Vcc			

Stand-by Function				
Pad1	Pad3 (Output)			
Open	Active			
"H" Level	Active			
"L" Level	High Z (No-Oscillation)			

# Clock Oscillators Surface Mount Type Clock Z-Series "X" type (Short LT type)







## CMOS/ 1.8V, 2.5V, 3.3V / 2.0×1.6, 2.5×2.0, 3.2×2.5, 5.0×3.2, 7.0×5.0mm



#### **Features**

- Frequency Range 0.5 to 170 MHz
- CMOS Output
- Short Lead Time
- Heat resistant up to +125°C

#### **Applications**

• Consumer/ Networking/ Industrial/ Amuse Table 1

Freq. Tol. Code × 10 <sup>-6</sup>		Operating	Note				
		Temperature Range (°C)	ivote				
S	± 30						
U	± 25	-10 to +70					
W	± 20						
G	± 50						
Н	± 30		-40 to +85 For additional				
J	± 25			For additional			
K	± 20		stabillity, please				
L	± 15		contact us.				
6	± 50	-40 to +105					
5	± 30	-40 10 + 103					
X	± 100						
Z	± 50	-40 to +125	-40 to +125	-40 to +125	-40 to +125	-40 to +125	
9	± 30						

#### How to Order

<u>KC</u> □□□□ <u>Z</u>	<u>25.0000</u>	<u>C</u>	1		X	<u>00</u>
1	2	3	4	(5)	6	7

#### **OSeries**

KC2016Z	2016 Size	KC2520Z	2520 Size		
KC3225Z	3225 Size	KC5032Z	5032 Size		
KC7050Z	7050 Size				

②Output Frequency (25.0000: 25MHz)

3 Output Type (C: CMOS)

**@Supply Voltage** 

(1: 1.8V/ 2.5V/ 3.3V Compatible) ⑤Frequency Tolerance (See Table 1) ©Symmetry/ INH Function

45/55%

**10** Individual Specification (STD Specification is "00".)

#### Packaging Tape&Reel

KC7050Z/ KC5032Z	1000 pcs./ reel
KC3225Z/ KC2520Z/ KC2016Z	2000 pcs./ reel

### **Specifications**

Item	Symbol	Conditions	Min.	Max.	Unit	
Output Frequency Range	fo	Conditions	0.5	170	MHz	
Frequency Tolerance	f_tol	Initial tolerance, Operating temperature range, Rated power supply voltage change, Load change, Aging (1 year @25°C), Shock and vibration		See Table 1.		
Storage Temperature Range	T_stg	Change, Load Change, Aging (1 year @25 C), Shock and Visitation	-55	150	°C	
Operating Temperature Range	T use			See Table 1.		
Max. Supply Voltage			-0.3	4.5	V	
Supply Voltage	Vcc		1.71	3.63	V	
		0.5≤fo<5MHz	_	5.2		
		5≤fo<15MHz	_	5.8		
		15≤fo<30MHz	_	6.2		
		30≤fo<50MHz		6.8		
Current Consumption	Icc	50≤fo≤60MHz		6.8		
(Noload/ 1.71≤Vcc≤2.25)	ICC	60 <fo<75mhz< td=""><td>_</td><td>9</td><td></td></fo<75mhz<>	_	9		
		75≤fo<105MHz	_	10		
		105≤fo<130MHz	_	10.5		
		130≤fo<160MHz	_	11.5		
		160≤fo≤170MHz	_	12.5		
		0.5≤fo<5MHz	_	5.5		
		5≤fo<15MHz	_	6		
		15≤fo<30MHz	_	6.5	5	
		30≤fo<50MHz	_	7.2		
Current Consumption	Icc	50≤fo≤60MHz	_	7.4	mA	
(Noload/ 2.25 < Vcc ≤ 2.8)	ICC	60 <fo<75mhz< td=""><td>_</td><td>10</td><td>ША</td></fo<75mhz<>	_	10	ША	
		75≤fo<105MHz	_	11.5		
		105≤fo<130MHz	_	12.5		
		130≤fo<160MHz	_	14		
		160≤fo≤170MHz	_	15		
		0.5≤fo<5MHz	_	5.8		
		5≤fo<15MHz	_	6.5		
		15≤fo<30MHz	_	7.3		
		30≤fo<50MHz	_	8		
Current Consumption	Icc	50≤fo≤60MHz	_	8.5		
(Noload/ 2.8 <vcc≤3.63)< td=""><td rowspan="3">icc</td><td>60<fo<75mhz< td=""><td>_</td><td>12.5</td></fo<75mhz<></td></vcc≤3.63)<>	icc	60 <fo<75mhz< td=""><td>_</td><td>12.5</td></fo<75mhz<>	_	12.5		
		75≤fo<105MHz	_	14.5		
		105≤fo<130MHz	_	15.5		
		130≤fo<160MHz	_	18		
		160≤fo≤170MHz		19.5		
Stand-by Current	I_std	0.500(1)		5	μΑ	
Symmetry	SYM	@50% Vcc	45	55	%	
		Loaded/ 1.71≤Vcc≤2.25		4		
Di / F-II Ti		0.5≤fo≤60MHz Loaded/ 2.25 <vcc≤2.8< td=""><td></td><td>3</td><td>1</td></vcc≤2.8<>		3	1	
Rise/ Fall Time	Tr/ Tf	Loaded/ 2.8 < Vcc ≤ 3.63	_	2.5	ns	
(20% to 80% Output Level)	·	Loaded/ 1.71≤Vcc≤2.25 60 <fo≤170mhz 2.25<vcc≤2.8<="" loaded="" td=""><td></td><td>1.5</td><td></td></fo≤170mhz>		1.5		
		60 <fo≤170mhz< td=""><td></td><td>1.3 1</td><td></td></fo≤170mhz<>		1.3 1		
Low Level Output Voltage	Vol		+ =	10% Vcc	V	
High Level Output Voltage	VOL	lol = 5mA   loн = -5mA	90% Vcc	10% VCC	V 	
Output Load (CMOS)		IOH JINA		15		
Low Level Input Voltage	L_CMOS		+=-	30% Vcc	pF V	
High Level Input Voltage	VIL VIH		70% Vcc	30% VCC	V	
Disable Time			70% VCC	200		
Enable Time	t_dis		<del>-</del>	5	ns	
Start-up Time	t_ena t str	@Minimum operating voltage to be 0 sec.	+ =	5	ms	
Start-up Tillie	L_str	www.minimum operating voitage to be o sec.		<u> </u>	ms	