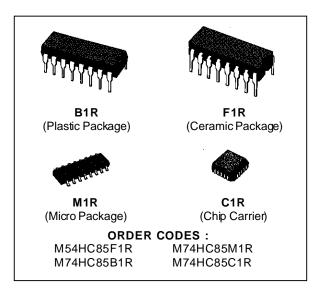


4-BIT MAGNITUDE COMPARATOR

- HIGH SPEED
 - $t_{PD} = 22 \text{ ns} (TYP.) \text{ at } V_{CC} = 5 \text{ V}$
- LOW POWER DISSIPATION $I_{CC} = 4 \mu A \text{ (MAX.)}$ at $T_A = 25 \text{ °C}$
- HIGH NOISE IMMUNITY

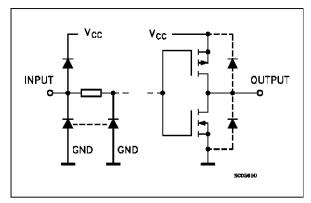
 VNIH = VNIL = 28 % VCC (MIN.)
- OUTPUT DRIVE CAPABILITÝ
 10 LSTTL LOADS
- SYMMETRICAL OUTPUT IMPEDANCE ||I_{OH}| = I_{OL} = 4 mA (MIN.)
- BALANCED PROPAGATION DELAYS tplh = tphl
- WIDE OPERATING VOLTAGE RANGE Vcc (OPR) = 2 V to 6 V
- PIN AND FUNCTION COMPATIBLE WITH 54/74LS85

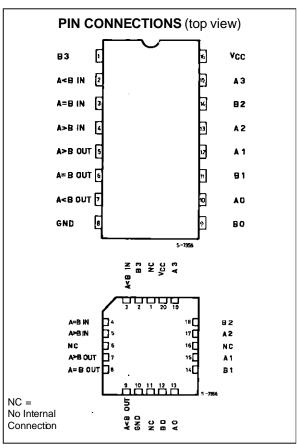


DESCRIPTION

The M54/74HC85 is a high speed CMOS 4-BIT MAGNITUDE COMPARATOR fabricated in silicon gate C^2 MOS technology. It has the same high speed performance of LSTTL combined with true CMOS low power consumption. This comparator compares two 4-bit words and provides a high voltage level on one of the A > B out, A = B out and A < B out outputs. The comparing bit number is easily expanded by cascading several devices as shown in the typical application. All inputs are equipped with protection circuits against static discharge and transient excess voltage.

INPUT AND OUTPUT EQUIVALENT CIRCUIT



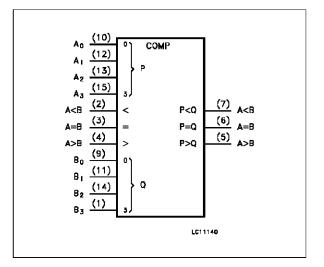


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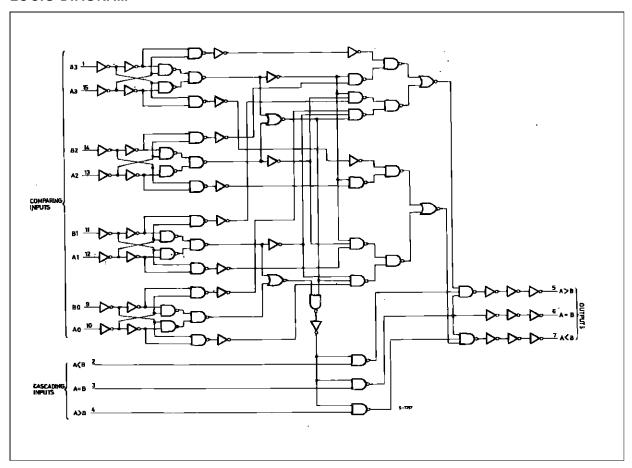
PIN DESCRIPTION

PIN No	SYMBOL	NAME AND FUNCTION			
2	IN _{A<b< sub=""></b<>}	A <b expansion="" input<="" td="">			
3	IN _{A=B}	A=B Expansion Input			
4	IN _{A>B}	A>B Expansion Input			
5	OUT _{A>B}	A>B Expansion Output			
6	OUT _{A=B}	A=B Expansion Output			
7	OUT _{A<b< sub=""></b<>}	A <b expansion="" output<="" td="">			
9, 11, 14, 1	B ₀ to B ₃	Word B Inputs			
10, 12, 13, 15	A_0 to A_3	Word A Inputs			
8	GND	Ground (0V)			
16	V _{CC}	Positive Supply Voltage			

IEC LOGIC SYMBOL



LOCIG DIAGRAM



TRUTH TABLE

	COMPARIA	NG INPUTS		CASC	ADING I	NPUTS	C	UTPUT	S
	COWIFARII	NG INPUTS		A>B	A <b< th=""><th>A=B</th><th>A>B</th><th>A<b< th=""><th>A=B</th></b<></th></b<>	A=B	A>B	A <b< th=""><th>A=B</th></b<>	A=B
A3>B3	X	X	Х	Х	Х	Х	Н	L	L
A3=B3	A2>B2	X	Х	Х	X	X	Н	L	L
A3=B3	A2=B2	A1>B1	Х	Х	Х	Х	Н	L	L
A3=B3	A2=B2	A1=B1	A0>B0	Х	X	Х	Н	L	L
			A0=B0	L	L	L	Н	Н	L
				Х	Х	Н	L	L	Н
A3=B3	A2=B2	A1=B1		L	Н	L	L	Н	L
				Н	L	L	Н	L	L
				Н	Н	L	L	L	L
A3=B3	A2=B2	A1=B1	A0 <b0< td=""><td>Х</td><td>Х</td><td>Х</td><td>L</td><td>Н</td><td>L</td></b0<>	Х	Х	Х	L	Н	L
A3=B3	A2=B2	A1 <b1< td=""><td>Х</td><td>Х</td><td>Х</td><td>Х</td><td>L</td><td>Н</td><td>L</td></b1<>	Х	Х	Х	Х	L	Н	L
A3=B3	A2 <b2< td=""><td>Х</td><td>Х</td><td>Х</td><td>Х</td><td>Х</td><td>L</td><td>Н</td><td>L</td></b2<>	Х	Х	Х	Х	Х	L	Н	L
A3 <b3< td=""><td>Х</td><td>X</td><td>Х</td><td>Х</td><td>Х</td><td>Х</td><td>L</td><td>Н</td><td>L</td></b3<>	Х	X	Х	Х	Х	Х	L	Н	L

X: DON'T CARE

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vcc	Supply Voltage	-0.5 to +7	V
Vı	DC Input Voltage	-0.5 to Vcc + 0.5	V
Vo	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
lıĸ	DC Input Diode Current	± 20	mA
lok	DC Output Diode Current	± 20	mA
Io	DC Output Source Sink Current Per Output Pin	± 25	mA
Icc or I _{GND}	DC V _{CC} or Ground Current	± 50	mA
P_{D}	Power Dissipation	500 (*)	mW
T _{stg}	Storage Temperature	-65 to +150	°C
TL	Lead Temperature (10 sec)	300	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied. (*) 500 mW: \cong 65 °C derate to 300 mW by 10mW/°C: 65 °C to 85 °C



RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter		Value	Unit
Vcc	Supply Voltage		2 to 6	V
VI	Input Voltage		0 to V _{CC}	V
Vo	Output Voltage		0 to V _{CC}	V
Тор	Operating Temperature: M54HC Series M74HC Series		-55 to +125 -40 to +85	°C
t _r , t _f	Input Rise and Fall Time	$V_{CC} = 2 V$	0 to 1000	ns
		$V_{CC} = 4.5 \text{ V}$	0 to 500	
		V _{CC} = 6 V	0 to 400	

DC SPECIFICATIONS

		Te	est Co	nditions				Value				
Symbol	Parameter	V _{CC}			T _A = 25 °C 54HC and 74HC		-40 to 85 °C 74HC		-55 to 125 °C 54HC		Unit	
		(V)			Min.	Тур.	Max.	Min.	Max.	Min.	Max.	
V_{IH}	High Level Input	2.0			1.5			1.5		1.5		
Voltage	4.5			3.15			3.15		3.15		V	
		6.0			4.2			4.2		4.2		
V_{IL}	Low Level Input	2.0					0.5		0.5		0.5	
	Voltage	4.5					1.35		1.35		1.35	V
		6.0					1.8		1.8		1.8	
V_{OH}	Output Voltage	2.0	V _I =		1.9	2.0		1.9		1.9		
Output Voltage		VI =	I _O =-20 μA	4.4	4.5		4.4		4.4		.,	
		6.0	or		5.9	6.0		5.9		5.9		V
		4.5	VIL	I _O =-4.0 mA	4.18	4.31		4.13		4.10		
		6.0		I _O =-5.2 mA	5.68	5.8		5.63		5.60		
V_{OL}	Low Level Output	2.0	Vı =			0.0	0.1		0.1		0.1	
	Voltage	4.5	VI =	I _O = 20 μA		0.0	0.1		0.1		0.1	
		6.0	or			0.0	0.1		0.1		0.1	V
		4.5	V_{IL}	I _O = 4.0 mA		0.17	0.26		0.33		0.40	
		6.0		I _O = 5.2 mA		0.18	0.26		0.33		0.40	
II	Input Leakage Current	6.0	V _I = '	V _{CC} or GND			±0.1		±1		±1	μΑ
Icc	Quiescent Supply Current	6.0	V _I = '	V _{CC} or GND			4		40		80	μΑ

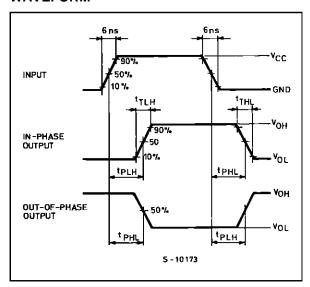


AC ELECTRICAL CHARACTERISTICS ($C_L = 50 \text{ pF}$, Input $t_f = t_f = 6 \text{ ns}$)

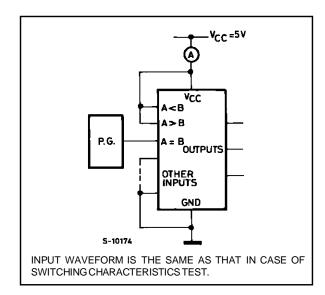
		Te	st Conditions				Value				
Symbol	Parameter	Vcc			T _A = 25 °C 54HC and 74HC		-40 to 85 °C 74HC		-55 to 125 °C 54HC		Unit
	(V)		Min.	Тур.	Max.	Min.	Max.	Min.	Max.		
t _{TLH}	Output Transition	2.0			30	75		95		110	
t _{THL}	Time	4.5			8	15		19		22	ns
		6.0			7	13		16		19	
t _{PLH}	t _{PLH} Propagation	2.0			96	185		230		280	
t _{PHL}	Delay Time	4.5			24	37		46		56	ns
	(A, B-OUT)	6.0			20	31		39		48	
t _{PLH}	Propagation	2.0			48	95		120		145	
t _{PHL}	Delay Time	4.5			12	19		24		29	ns
	(CASCADE-OUT)	6.0			10	16		20		25	
C _{IN}	Input Capacitance				5	10		10		10	pF
C _{PD} (*)	Power Dissipation Capacitance				23						pF

^(*) C_{PD} is defined as the value of the IC's internal equivalent capacitance which is calculated from the operating current consumption without load. (Refer to Test Circuit). Average operating current can be obtained by the following equation. I_{CC}(opr) = C_{PD} • V_{CC} • f_{IN} + I_{CC}

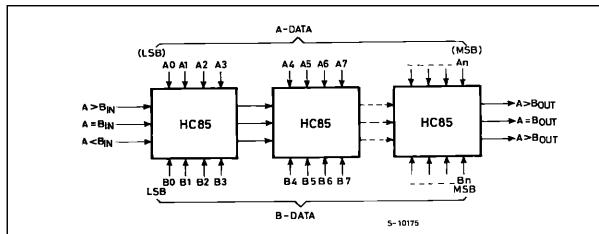
SWITCHING CHARACTERISTICS TEST WAVEFORM



TEST CIRCUIT Icc (Opr.)



TYPICAL APPLICATION



LSB = lowest significant bit

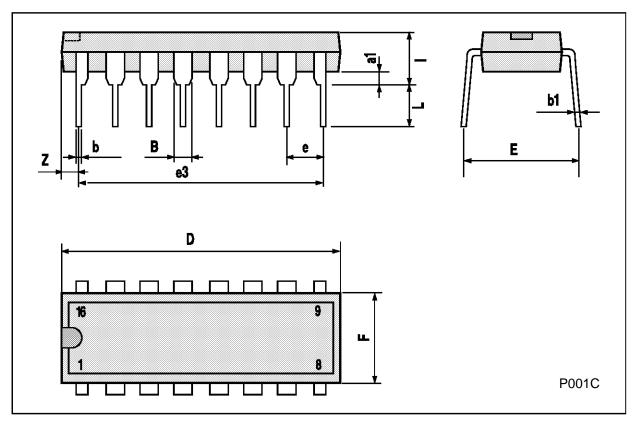
MSB = MOST SIGNIFICANT BIT

COMPARING INPUTS	CASC	ADING II	NPUTS	OUTPUTS			
COMPARING INFOTS	A>B	A=B	A <b< th=""><th>A>B</th><th>A=B</th><th>A<b< th=""></b<></th></b<>	A>B	A=B	A <b< th=""></b<>	
(A)>(B)	Х	Х	Х	Н	L	Ш	
	Н	L	L	Н	L	L	
(A)=(B)	Х	Н	Х	L	Н	L	
	L	L	Н	L	L	Н	
(A)<(B)	Х	Х	Х	L	L	Н	

X: DON'T CARE

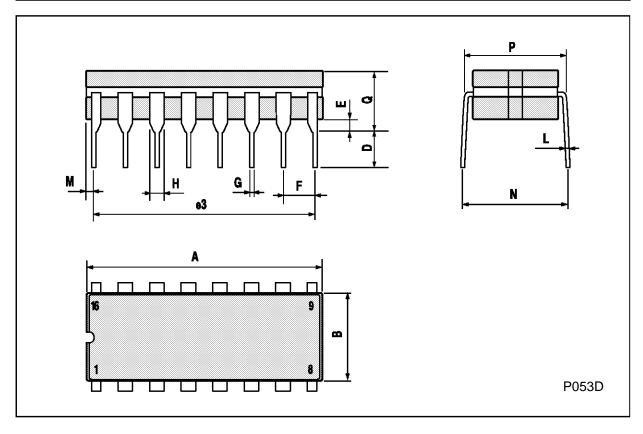
Plastic DIP16 (0.25) MECHANICAL DATA

DIM.		mm		inch				
Diwi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
a1	0.51			0.020				
В	0.77		1.65	0.030		0.065		
b		0.5			0.020			
b1		0.25			0.010			
D			20			0.787		
E		8.5			0.335			
е		2.54			0.100			
e3		17.78			0.700			
F			7.1			0.280		
I			5.1			0.201		
L		3.3			0.130			
Z			1.27			0.050		



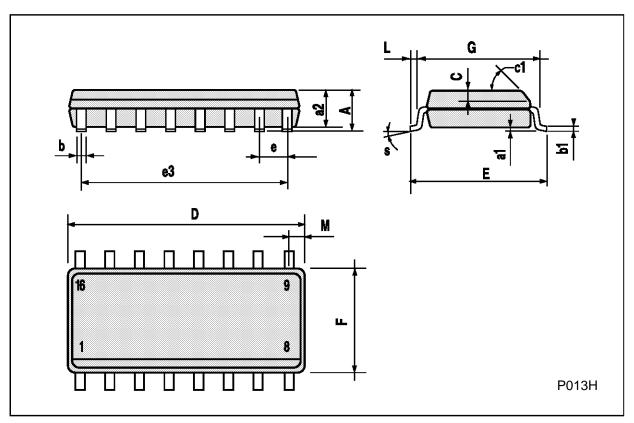
Ceramic DIP16/1 MECHANICAL DATA

DIM.		mm		inch				
Diwi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А			20			0.787		
В			7			0.276		
D		3.3			0.130			
Е	0.38			0.015				
e3		17.78			0.700			
F	2.29		2.79	0.090		0.110		
G	0.4		0.55	0.016		0.022		
Н	1.17		1.52	0.046		0.060		
L	0.22		0.31	0.009		0.012		
М	0.51		1.27	0.020		0.050		
N			10.3			0.406		
Р	7.8		8.05	0.307		0.317		
Q			5.08			0.200		



SO16 (Narrow) MECHANICAL DATA

DIM.		mm		inch				
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А			1.75			0.068		
a1	0.1		0.2	0.004		0.007		
a2			1.65			0.064		
b	0.35		0.46	0.013		0.018		
b1	0.19		0.25	0.007		0.010		
С		0.5			0.019			
c1			45° ((typ.)		•		
D	9.8		10	0.385		0.393		
E	5.8		6.2	0.228		0.244		
е		1.27			0.050			
e3		8.89			0.350			
F	3.8		4.0	0.149		0.157		
G	4.6		5.3	0.181		0.208		
L	0.5		1.27	0.019		0.050		
М			0.62			0.024		
S			8° (r	nax.)				



PLCC20 MECHANICAL DATA

DIM.		mm		inch				
Diiii.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А	9.78		10.03	0.385		0.395		
В	8.89		9.04	0.350		0.356		
D	4.2		4.57	0.165		0.180		
d1		2.54			0.100			
d2		0.56			0.022			
E	7.37		8.38	0.290		0.330		
е		1.27			0.050			
e3		5.08			0.200			
F		0.38			0.015			
G			0.101			0.004		
М		1.27			0.050			
M1		1.14			0.045			



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