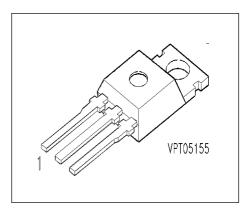
SIPMOS ® Power Transistor

- N channel
- Enhancement mode
- Avalanche-rated
- dv/dt rated
- Low on-resistance
- 175°C operating temperature
- also in TO-220 SMD available



Pin 1	Pin 2	Pin 3
G	D	S

Туре	V _{DS}	I _D	R _{DS(on)}	Package	Ordering Code
BUZ 103	50 V	40 A	0.04 Ω	TO-220 AB	C67078-S1352-A2

Maximum Ratings

Parameter	Symbol	Values	Unit
Continuous drain current	I _D		Α
<i>T</i> _C = 23 °C		40	
Pulsed drain current	/ _{Dpuls}		
$T_{\rm C}$ = 25 °C		160	
Avalanche energy, single pulse	E _{AS}		mJ
$I_{\rm D} = 40 \; {\rm A}, \; V_{\rm DD} = 25 \; {\rm V}, \; R_{\rm GS} = 25 \; {\rm \Omega}$			
$L = 63 \mu H, T_j = 25 °C$		100	
Reverse diode dv/dt	d <i>v</i> /d <i>t</i>		kV/μs
$I_{S} = 40 \text{ A}, \ V_{DS} = 40 \text{ V}, \ di_{F}/dt = 200 \text{ A/}\mu\text{s}$			
<i>T</i> _{jmax} = 175 °C		6	
Gate source voltage	V_{GS}	± 20	V
Power dissipation	P _{tot}		W
$T_{\rm C}$ = 25 °C		120	
Operating temperature	T _j	-55 + 175	°C
Storage temperature	T _{stg}	-55 + 175	
Thermal resistance, chip case	R _{thJC}	≤ 1.25	K/W
Thermal resistance, chip to ambient	R _{thJA}	≤ 75	
DIN humidity category, DIN 40 040		Е	
IEC climatic category, DIN IEC 68-1		55 / 175 / 56	



Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Static Characteristics					
Drain- source breakdown voltage	V _{(BR)DSS}				V
$V_{\rm GS}$ = 0 V, $I_{\rm D_i}$ $T_{\rm j}$ = -40 °C		50	-	-	
Gate threshold voltage	V _{GS(th)}				
$V_{GS}=V_{DS}$, $I_{D}=1$ mA		2.1	3	4	
Zero gate voltage drain current	I _{DSS}				
$V_{\rm DS} = 50 \; \rm V, \; V_{\rm GS} = 0 \; \rm V, \; T_{\rm j} = 25 \; ^{\circ} \rm C$		-	0.1	1	μA
$V_{\rm DS} = 50 \; {\rm V}, \; V_{\rm GS} = 0 \; {\rm V}, \; T_{\rm j} = -40 \; {\rm ^{\circ}C}$		-	1	100	nA
$V_{\rm DS} = 50 \; \rm V, \; V_{\rm GS} = 0 \; \rm V, \; T_{\rm j} = 150 \; ^{\circ}\rm C$		-	10	100	μA
Gate-source leakage current	I _{GSS}				nA
$V_{GS} = 20 \text{ V}, \ V_{DS} = 0 \text{ V}$		-	10	100	
Drain-Source on-resistance	R _{DS(on)}				Ω
$V_{GS} = 10 \text{ V}, I_{D} = 28 \text{ A}$		-	0.03	0.04	



Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Dynamic Characteristics					
Transconductance	<i>g</i> fs				S
$V_{DS} \ge 2 * I_D * R_{DS(on)max}, I_D = 28 A$		10	18	-	
Input capacitance	C_{iss}				pF
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	900	1200	
Output capacitance	C_{oss}				
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	330	500	
Reverse transfer capacitance	C_{rss}				
$V_{GS} = 0 \text{ V}, \ V_{DS} = 25 \text{ V}, \ f = 1 \text{ MHz}$		-	140	210	
Turn-on delay time	$t_{d(on)}$				ns
$V_{\rm DD} = 30 \ {\rm V}, \ V_{\rm GS} = 10 \ {\rm V}, \ I_{\rm D} = 3 \ {\rm A}$					
$R_{\rm GS} = 50~\Omega$		-	20	30	
Rise time	t_{r}				
$V_{\rm DD} = 30 \ {\rm V}, \ V_{\rm GS} = 10 \ {\rm V}, \ I_{\rm D} = 3 \ {\rm A}$					
$R_{\text{GS}} = 50 \ \Omega$		-	70	105	
Turn-off delay time	$t_{d(off)}$				
$V_{\rm DD} = 30 \ {\rm V}, \ V_{\rm GS} = 10 \ {\rm V}, \ I_{\rm D} = 3 \ {\rm A}$					
$R_{\rm GS} = 50 \ \Omega$		-	150	200	
Fall time	$t_{\rm f}$				
$V_{\rm DD} = 30 \ {\rm V}, \ V_{\rm GS} = 10 \ {\rm V}, \ I_{\rm D} = 3 \ {\rm A}$					
$R_{\rm GS} = 50 \ \Omega$		-	95	130	

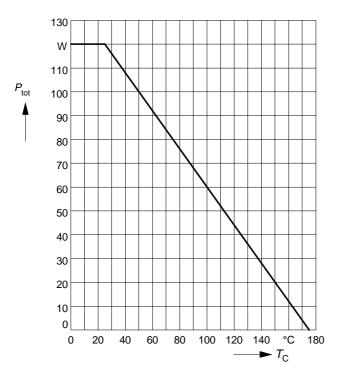


Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
Reverse Diode					
Inverse diode continuous forward current	Is				А
<i>T</i> _C = 25 °C		-	-	40	
Inverse diode direct current,pulsed	I _{SM}				
<i>T</i> _C = 25 °C		-	-	160	
Inverse diode forward voltage	V_{SD}				V
$V_{GS} = 0 \text{ V}, I_{F} = 80 \text{ A}$		-	1.2	1.8	
Reverse recovery time	t _{rr}				ns
$V_{R} = 30 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$		-	60		
Reverse recovery charge	Q _{rr}				μC
$V_{R} = 30 \text{ V}, I_{F} = I_{S}, di_{F}/dt = 100 \text{ A/}\mu\text{s}$		-	0.08	-	

Power dissipation

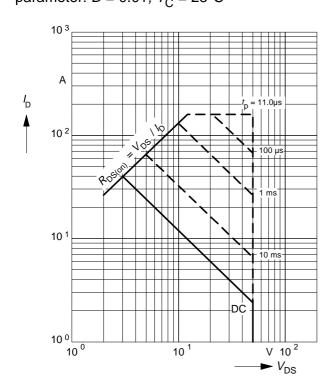
$$P_{\mathsf{tot}} = f(T_{\mathsf{C}})$$



Safe operating area

$$I_{\mathsf{D}} = f(V_{\mathsf{DS}})$$

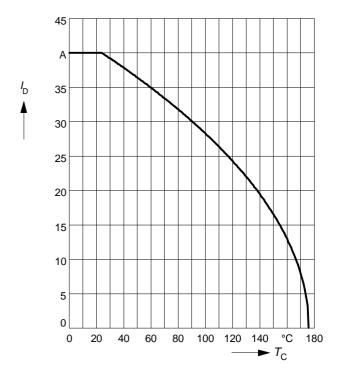
parameter: D = 0.01, $T_C = 25$ °C



Drain current

 $I_{\mathsf{D}} = f(T_{\mathsf{C}})$

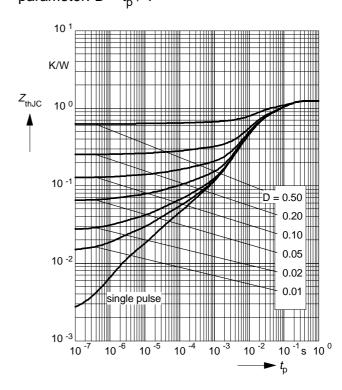
parameter: $V_{GS} \ge 10 \text{ V}$



Transient thermal impedance

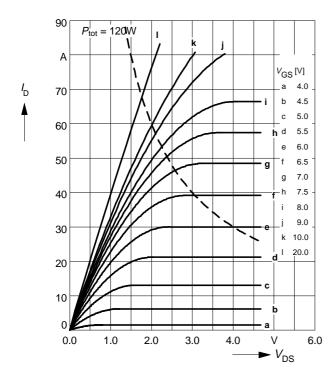
$$Z_{\mathsf{th\ JC}} = f(t_{\mathsf{p}})$$

parameter: $D = t_p / T$



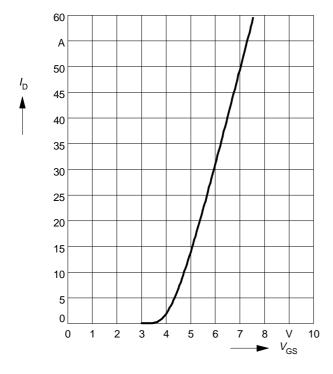
Typ. output characteristics

 $I_{\rm D} = f(V_{\rm DS})$ parameter: $t_{\rm p} = 80~\mu {\rm s}$



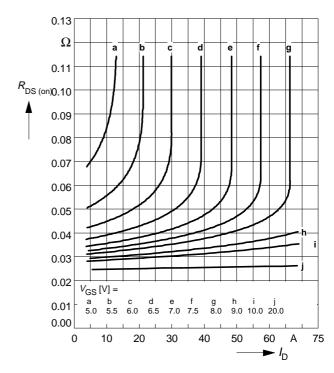
Typ. transfer characteristics $I_D = f(V_{GS})$

parameter: $t_p = 80 \mu s$ $V_{DS} \ge 2 \times I_D \times R_{DS(on)max}$



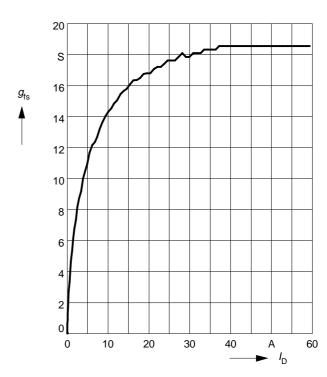
Typ. drain-source on-resistance

 $R_{\text{DS (on)}} = f(I_{\text{D}})$ parameter: V_{GS}



Typ. forward transconductance $g_{fS} = f(I_D)$

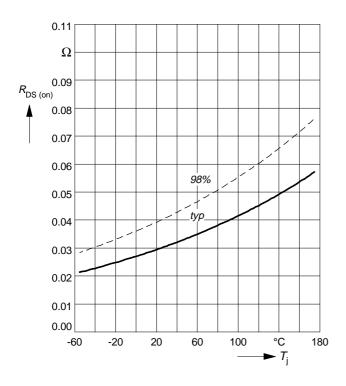
parameter: $t_p = 80 \mu s$, $V_{DS} \ge 2 \times I_D \times R_{DS(on)max}$



Drain-source on-resistance

 $R_{\text{DS (on)}} = f(T_{\text{j}})$

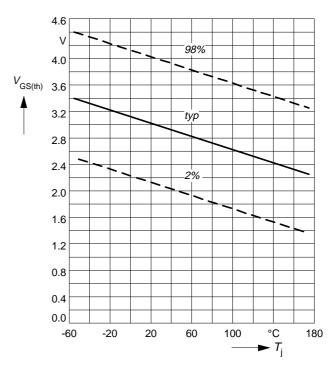
parameter: $I_D = 28 \text{ A}$, $V_{GS} = 10 \text{ V}$



Gate threshold voltage

 $V_{GS (th)} = f(T_j)$

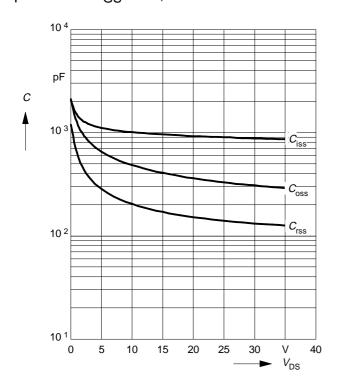
parameter: $V_{GS} = V_{DS}$, $I_{D} = 1 \text{ mA}$



Typ. capacitances

 $C = f(V_{DS})$

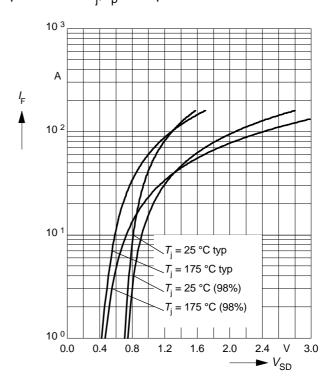
parameter: $V_{GS} = 0V$, f = 1MHz



Forward characteristics of reverse diode

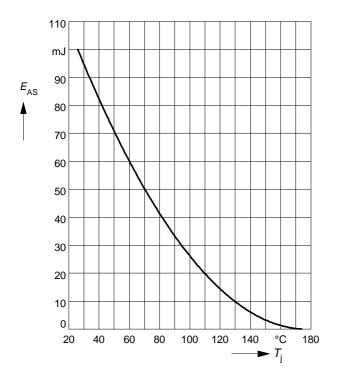
 $I_{\mathsf{F}} = f(V_{\mathsf{SD}})$

parameter: T_i , $t_p = 80 \mu s$



Avalanche energy $E_{AS} = f(T_j)$ parameter: $I_D = 40 \text{ A}, V_{DD} = 25 \text{ V}$

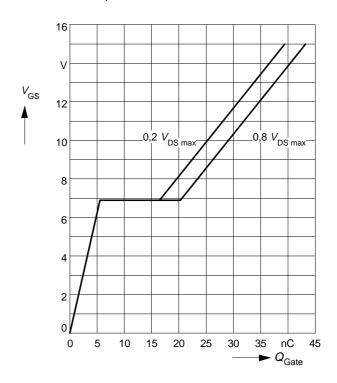
 $R_{\rm GS}$ = 25 Ω , L = 63 μH



Typ. gate charge

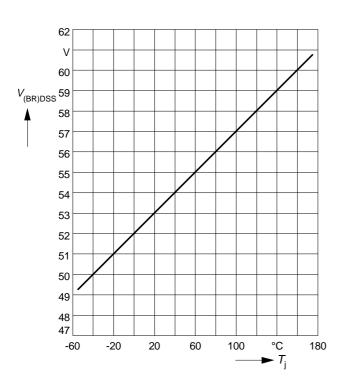
 $V_{\mathsf{GS}} = f(Q_{\mathsf{Gate}})$

parameter: $I_{D \text{ puls}} = 60 \text{ A}$



Drain-source breakdown voltage

$$V_{(BR)DSS} = f(T_i)$$

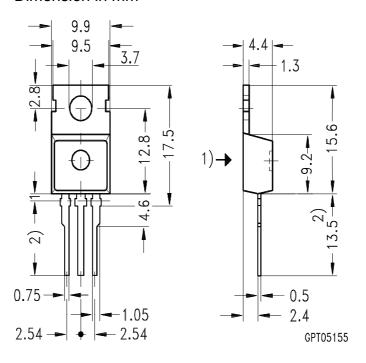




Package Outlines

TO-220 AB

Dimension in mm



- 1) punch direction, burr max. 0.04
- 2) dip tinning
- 3) max. 14.5 by dip tinning press burr max. 0.05