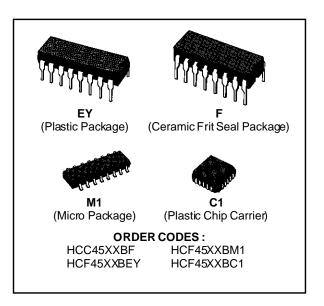


HCC/HCF4518B HCC/HCF4520B

DUAL UP-COUNTERS

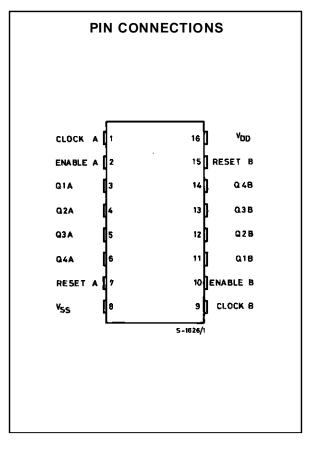
HCC/HCF4518B DUAL BCD UP-COUNTER HCC/HCF4520B DUAL BINARY UPCOUNTER

- MEDIUM-SPEED OPERATION 6MHz TYP. CLOCK FREQUENCY AT 10V
- POSITIVE OR NEGATIVE EDGE TRIGGER-ING
- SYNCHRONOUS INTERNAL CARRY PROPA-GATION
- QUIESCENT CURRENT SPECIFIED TO 20V FOR HCC DEVICE
- 5V, 10V, AND 15V PARAMETRIC RATINGS
- INPUT CURRENT OF 100nA AT 18V AND 25°C FOR HCC DEVICE
- 100% TESTED FOR QUIESCENT CURRENT
- MEETS ALL REQUIREMENTS OF JEDECTEN-TATIVE STANDARD N° 13A, "STANDARD SPE-CIFICATIONS FOR DESCRIPTION OF "B" SERIES CMOS DEVICES"



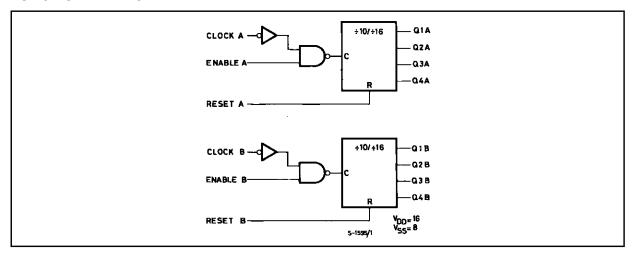
DESCRIPTION

The HCC4518B/4520B (extended temperature range) and HCF4518B/4520B (intermediate temperature range) are monolithic integrated circuits, available in 16-lead dual in-line plastic or ceramic package and plastic micro package. The HCC/HCF4518B Dual BCD Up Counter and HCC/HCF4520B Dual Binary Up Counter each consist of two identical, internally synchronous 4-stage counters. The counter stages are D-type flip-flops having interchangeable Clock and Enable lines for incrementing on either the positive-going or negative-going transition. For single-unit operation the Enable input is maintained "high" and the counter advances on each positive-going transition of the Clock. The counters are cleared by high levels on their Reset lines. The counter can be cascaded in the ripple mode by connecting Q4 to the enable input of the subsequent counter while the clock input of the latter is held low.



June 1989 1/14

FUNCTIONAL DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{DD} *	Supply Voltage : HCC Types HCF Types	- 0.5 to + 20 - 0.5 to + 18	V V
Vi	Input Voltage	- 0.5 to V _{DD} + 0.5	V
I _I	DC Input Current (any one input)	± 10	mA
P _{tot}	Total Power Dissipation (per package) Dissipation per Output Transistor for Top = Full Package-temperature Range	200 100	mW mW
Тор	Operating Temperature : HCC Types HCF Types	- 55 to + 125 - 40 to + 85	°C °C
T _{stg}	Storage Temperature	- 65 to + 150	°C

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for external periods may affect device reliability.

* All voltage values are referred to V_{SS} pin voltage.

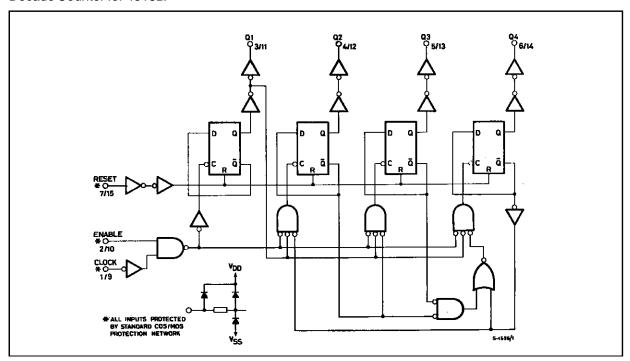
RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Value	Unit
V_{DD}	Supply Voltage: HCC Types	3 to 18	V
	HCF Types	3 to 15	V
V_{I}	Input Voltage	0 to V _{DD}	V
Top	Operating Temperature : HCC Types	– 55 to + 125	°C
	HCF Types	- 40 to + 85	°C

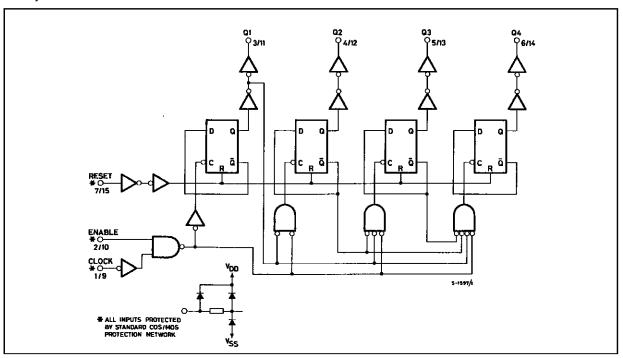


LOGIC DIAGRAMS (for one of two identical counter)

Decade Counter for 4518B.



Binary Counter for 4520B.



TRUTH TABLE

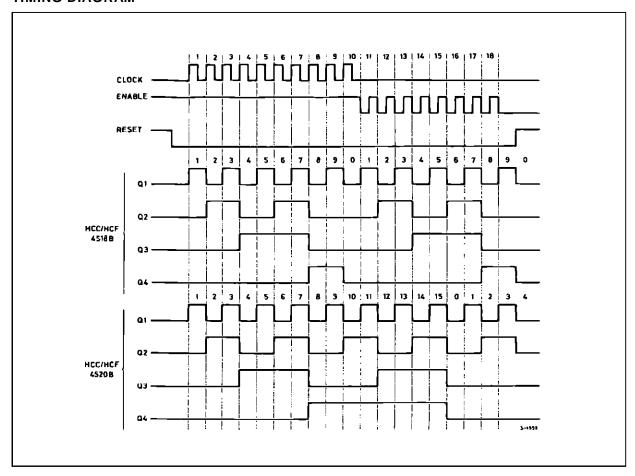
Clock	Clock Enable		Action
	_/ 1		Increment Counter
0	0 _/_		Increment Counter
	Х	0	No Change
Х	Х/		No Change
_厂	_/ 0		No Change
1	1 __		No Change
Х	х х		Q1 Thru Q4 = 0

X = Don't Care

Logic 1 = High State

Logic 0 = Low

TIMING DIAGRAM



STATIC ELECTRICAL CHARACTERISTICS (over recommended operating conditions)

			Т	est Con	dition	s	Value					Unit		
Symbol	Parame	Parameter		V _I V _O IO V		V _{DD}	T _{Low} * 25°C T _{High} *						iah*	
			(V)	(V)	(μA)	(V)	Min.	Max.	Min.	Тур.	Max.	Min.	Max.	
١L	Quiescent		0/ 5			5		5		0.04	5		150	
	Current	нсс	0/10			10		10		0.04	10		300	
		Types	0/15			15		20		0.04	20		600	
			0/20			20		100		0.08	100		3000	μΑ
			0/ 5			5		20		0.04	20		150	
		HCF Types	0/10			10		40		0.04	40		300	
		Турсз	0/15			15		80		0.04	80		600	
V _{OH}	Output High	ı	0/ 5		< 1	5	4.95		4.95			4.95		
	Voltage		0/10		< 1	10	9.95		9.95			9.95		V
			0/15		< 1	15	14.95		14.95			14.95		
V_{OL}	Output Low	1	5/0		< 1	5		0.05			0.05		0.05	
	Voltage		10/0		< 1	10		0.05			0.05		0.05	V
			15/0		< 1	15		0.05			0.05		0.05	1
VıH	Input High			0.5/4.5	< 1	5	3.5		3.5			3.5		V
	Voltage			1/9	< 1	10	7		7			7		
				1.5/13.5	< 1	15	11		11			11		
V_{IL}	Input Low			4.5/0.5	< 1	5		1.5			1.5		1.5	V
	Voltage			9/1	< 1	10		3			3		3	
				13.5/1.5	< 1	15		4			4		4	
I _{OH}	Output		0/ 5	2.5		5	- 2		- 1.6	- 3.2		- 1.15		
	Drive Current	HCC	0/ 5	4.6		5	- 0.64		- 0.51	- 1		- 0.36		
	Odnone	Types	0/10	9.5		10	- 1.6		- 1.3	- 2.6		- 0.9		
			0/15	13.5		15	- 4.2		- 3.4	- 6.8		- 2.4		mA
			0/ 5	2.5		5	- 1.53		- 1.36	- 3.2		- 1.1		
		HCF	0/ 5	4.6		5	- 0.52		- 0.44			- 0.36		
		Types		9.5		10	- 1.3		- 1.1	- 2.6		- 0.9		
			0/15	13.5		15	- 3.6		- 3.0	- 6.8		- 2.4		
I_{OL}	Output	HCC	0/ 5	0.4		5	0.64		0.51	1		0.36		
	Sink Current	Types	0/10	0.5		10	1.6		1.3	2.6		0.9		
			0/15	1.5		15	4.2		3.4	6.8		2.4		mA
		HCF	0/ 5	0.4		5	0.52		0.44	1		0.36		
		Types	0/10	0.5		10	1.3		1.1	2.6		0.9		
			0/15	1.5		15	3.6		3.0	6.8		2.4		
I _{IH} , I _{IL}	Input HCC Leakage Types		0/18	Any In	put	18		± 0.1		±10 ⁻⁵	± 0.1		± 1	μА
C	Current	Current HCF Types		,		15		± 0.3		±10 ⁻⁵	± 0.3		± 1	μΑ
Cı	Input Capa	citance		Any In	put					5	7.5			pF

^{*} $T_{Low} = -55^{\circ}\text{C}$ for HCC device : -40°C for HCF device. * $T_{High} = +125^{\circ}\text{C}$ for HCC device : $+85^{\circ}\text{C}$ for HCF device. The Noise Margin for both "1" and "0" level is : 1V min. with $V_{DD} = 5V$, 2V min. with $V_{DD} = 10V$, 2.5V min. with $V_{DD} = 15V$.

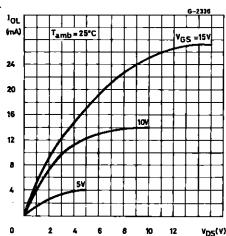


DYNAMIC ELECTRICAL CHARACTERISTICS ($T_{amb} = 25^{\circ}C$, $C_{L} = 50 pF$, $R_{L} = 200 k\Omega$, typical temperature coefficient for all $V_{DD} = 0.3\%$ °C values, all input rise and fall time = 20ns)

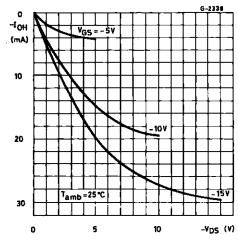
Symbol	Donomotor	Test Conditions			Unit		
Symbol	Parameter	V _{DE}	_D (V)	Min.	Тур.	Max.	Unit
t _{PLH} ,	Propagation Delay Time		5		280	560	
t _{PHL}	(clock or enable to output)	1	10		115	230	ns
		1	15		80	160	
t _{PLH} ,	Propagation Delay Time		5		330	650	
t _{PHL} ,	(reset to output)	1	10		130	225	ns
			15		90	170	
t _{TLH} ,t _{THL}	Transition Time		5		100	200	
		1	10		50	100	ns
		1	15		40	80	
t _W	Clock Pulse Width		5	200	100		
		1	10	100	50		ns
		1	15	70	35		
t _W	Reset Pulse Width		5	250	125		
		1	10	110	55		ns
		1	15	80	40		
t _W	Enable Pulse Width		5	400	200		
		1	10	200	100		ns
		1	15	140	70		
t _r ,	Clock or Enable Rise and Fall	5	5			15	
t _f	Time	1	10			15	μs
		1	15			5	
f _{max}	Maximum Clock Frequency		5	1.5	3		
		1	10	3	6		MHz
			15	4	8		
t _r ,	Clock Input Rise or Fall Time		5			15	
t _f			10			5	μs
		1	15			5	

Typical Output Low (sink) Current Characteristics.

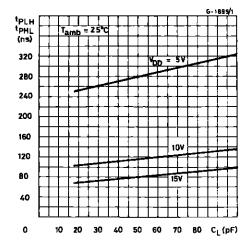
Mini-



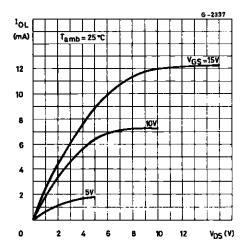
Typical Output High (source) Current Characteristics.



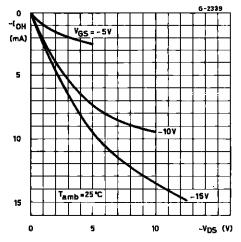
Typical Propagation Delay vs. Load Capacitance, Reset to output.



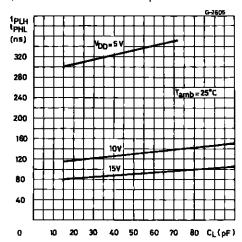
mum Output Low (sink) Current Characteristics.



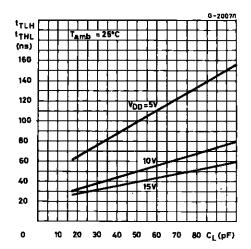
Minimum output High (source) Current Characteristics.



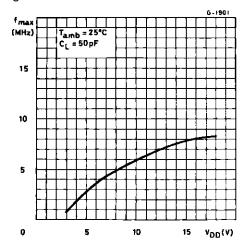
Typical Propagation Delay Time vs. Load Capacitance, Clock or Enable to Output.



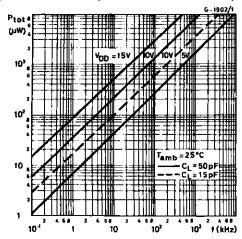
Typical Transition Time vs. load Capacitance.



Typical Maximum-clock Frequency vs, Supply Voltage.

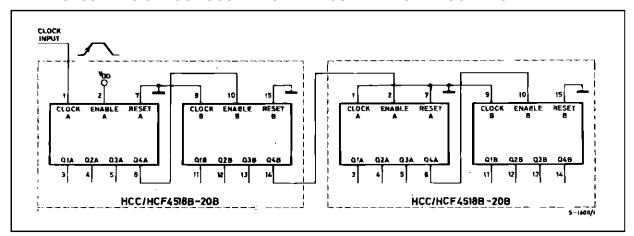


Typical Power Dissipation/Counter vs. Frequency.

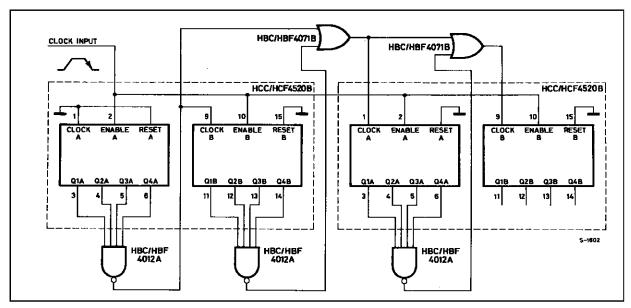


TYPICAL APPLICATIONS

RIPPLE CASCADING OF FOUR COUNTERS WITH POSITIVE-EDGE TRIGGERING.

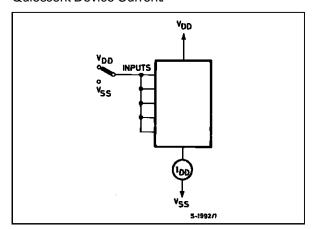




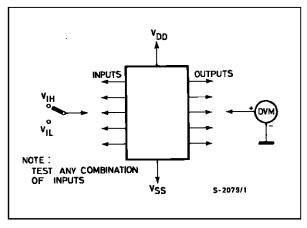


TEST CIRCUITS

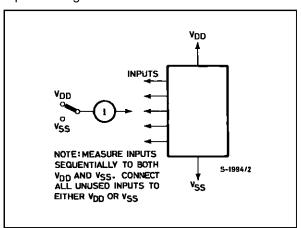
Quiescent Device Current.



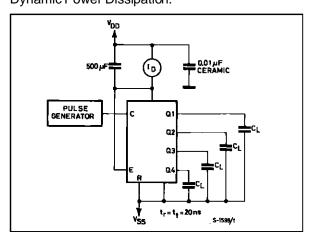
Noise Immunity.



Input Leakage Current.

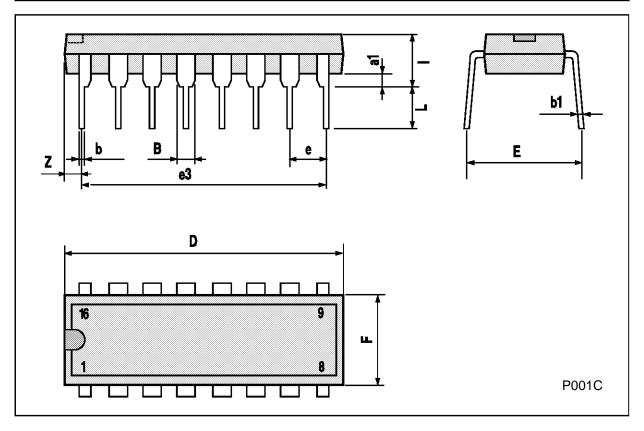


Dynamic Power Dissipation.



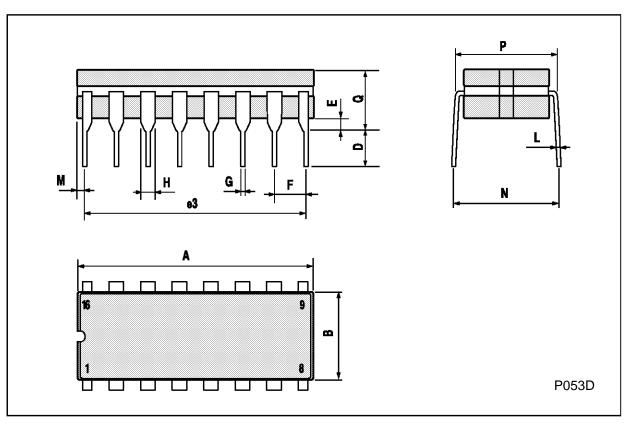
Plastic DIP16 (0.25) MECHANICAL DATA

DIM.		mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
a1	0.51			0.020			
В	0.77		1.65	0.030		0.065	
b		0.5			0.020		
b1		0.25			0.010		
D			20			0.787	
E		8.5			0.335		
е		2.54			0.100		
e3		17.78			0.700		
F			7.1			0.280	
I			5.1			0.201	
L		3.3			0.130		
Z			1.27			0.050	



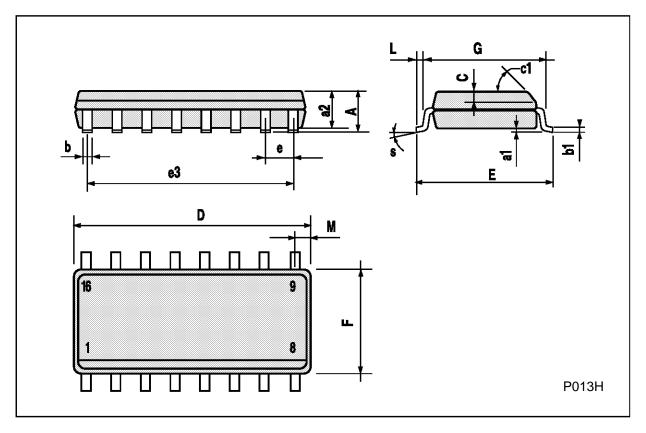
Ceramic DIP16/1 MECHANICAL DATA

DIM.		mm		inch			
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α			20			0.787	
В			7			0.276	
D		3.3			0.130		
Е	0.38			0.015			
e3		17.78			0.700		
F	2.29		2.79	0.090		0.110	
G	0.4		0.55	0.016		0.022	
Н	1.17		1.52	0.046		0.060	
L	0.22		0.31	0.009		0.012	
M	0.51		1.27	0.020		0.050	
N			10.3			0.406	
Р	7.8		8.05	0.307		0.317	
Q			5.08			0.200	



SO16 (Narrow) MECHANICAL DATA

DIM.		mm		inch			
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
Α			1.75			0.068	
a1	0.1		0.2	0.004		0.007	
a2			1.65			0.064	
b	0.35		0.46	0.013		0.018	
b1	0.19		0.25	0.007		0.010	
С		0.5			0.019		
c1			45°	(typ.)			
D	9.8		10	0.385		0.393	
E	5.8		6.2	0.228		0.244	
е		1.27			0.050		
e3		8.89			0.350		
F	3.8		4.0	0.149		0.157	
G	4.6		5.3	0.181		0.208	
L	0.5		1.27	0.019		0.050	
М			0.62			0.024	
S			8° (r	nax.)			



PLCC20 MECHANICAL DATA

DIM.		mm		inch				
Dii#i.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.		
А	9.78		10.03	0.385		0.395		
В	8.89		9.04	0.350		0.356		
D	4.2		4.57	0.165		0.180		
d1		2.54			0.100			
d2		0.56			0.022			
E	7.37		8.38	0.290		0.330		
е		1.27			0.050			
e3		5.08			0.200			
F		0.38			0.015			
G			0.101			0.004		
М		1.27			0.050			
M1		1.14			0.045			



Information furnished is believed to be accurate and reliable. However, SGS-THOMSON Microelectronics assumes no responsability for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may results from its use. No license is granted by implication or otherwise under any patent or patent rights of SGS-THOMSON Microelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. SGS-THOMSON Microelectronics products are not authorized for use as critical components in life support devices or systems without express written approval of SGS-THOMSON Microelectonics.

© 1994 SGS-THOMSON Microelectronics - All Rights Reserved

SGS-THOMSON Microelectronics GROUP OF COMPANIES

Australia - Brazil - France - Germany - Hong Kong - Italy - Japan - Korea - Malaysia - Malta - Morocco - The Netherlands - Singapore - Spain - Sweden - Switzerland - Taiwan - Thailand - United Kingdom - U.S.A

