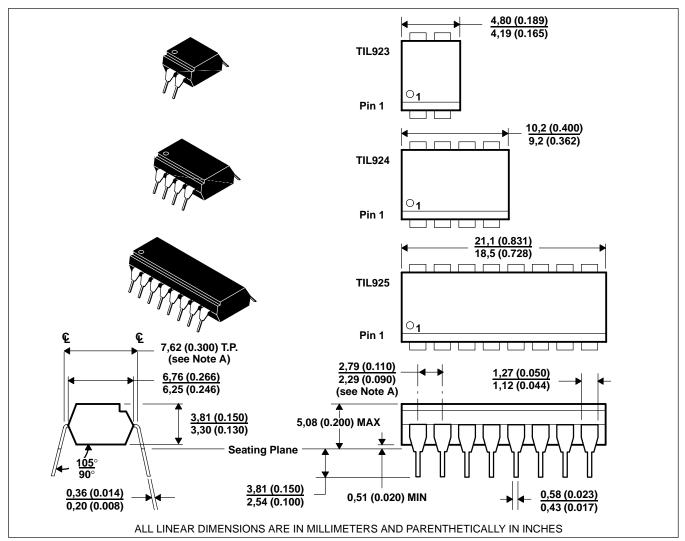
SOOS031 - OCTOBER 1991

- Gallium-Arsenide Diode Infrared Source
- Source Is Optically Coupled to Silicon N-P-N Darlington Phototransistor
- Choice of One, Two, or Four Channels
- Choice of Two Current-Transfer Ratios
- High-Voltage Electrical Isolation . . . 7.5 kV Peak (5.3 kV rms)
- Plastic Dual-In-Line Packages
- UL Listed File No. E65085

description

These optocouplers consist of a gallium-arsenide light-emitting diode and a silicon n-p-n Darlington phototransistor per channel. The TIL923 has one channel in a 4-pin package, the TIL924 has two channels in a 8-pin package, and the TIL925 has four channels in a 16-pin package. The standard devices, TIL923, TIL924, and TIL925, are tested for a current-transfer ratio of 500% minimum. Devices selected for a current-transfer ratio of 1000% are designated with the suffix.

mechanical data

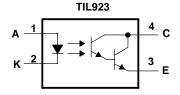


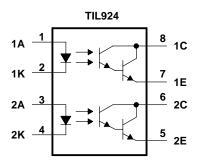
NOTE A: Each pin centerline is located 0,25 (0.010) of its true longitudinal position.

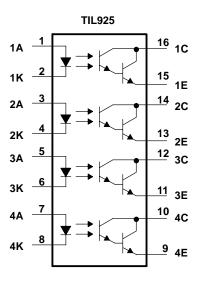


SOOS031 - OCTOBER 1991

schematic diagrams







absolute maximum ratings at 25°C free-air temperature (unless otherwise noted)

Input-to-output voltage (see Note 1)±7.5 kV peak or dc (±5.3 kV	√ rms)
Collector-emitter voltage (see Note 2)	. 35 V
Emitter-collector voltage	7 V
Input diode reverse voltage	5 V
Input diode continuous forward current at (or below) 25°C free-air temperature (see Note 3) 5	50 mA
Continuous power dissipation at (or below) 25°C free-air temperature:	
Phototransistor (see Note 4)15	0 mW
Input diode plus phototransistor per channel (see Note 5)	0 mW
Operating free-air temperature, T _A –55°C to	100°C
Storage temperature range55°C to	125°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

NOTES: 1. This rating applies for sine-wave operation at 50 or 60 Hz. Service capability is verified by testing in accordance with UL requirements.

- 2. This value applies when the base-emitter diode is open circuited.
- 3. Derate linearly to 100°C free-air temperature at the rate of 0.67 mA/°C.
- 4. Derate linearly to 100°C free-air temperature at the rate of 2 mW/°C.
- 5. Derate linearly to 100°C free-air temperature at the rate of 2.67 mW/°C.

electrical characteristics, T_A = 25°C (unless otherwise noted)

PARAMETER			TEST	MIN	TYP	MAX	UNIT		
V _(BR) CEO	O Collector-emitter breakdown voltage		$I_C = 0.5 \text{ mA},$	I _F = 0		35			V
V(BR)ECO	Emitter-collector breakdown voltage		$I_C = 100 \mu A$,	lF = 0		7			V
I _R	Input diode static reverse current		V _R = 5 V					10	μΑ
I _{C(off)}	Off-state collector current		V _{CE} = 10 V,	IF = 0				100	nA
CTR	Current transfer ratio	TIL923, TIL924, TIL925	I _F = 2 mA,	V _{CE} = 1 V		500%			
		TIL923A, TIL924A, TIL925A				1000%			
٧F	Input diode static forward voltage		IF = 20 mA					1.4	V
VCE(sat)	Collector-emitter saturation voltage		IF = 10 mA,	$I_C = 50 \text{ mA}$				1	V
C _{io}	Input-to-output capacitance		$V_{in-out} = 0$,	f = 1 MHz,	See Note 6		1		pF
r _{io}	Input-to-output internal resistance		$V_{in-out} = \pm 1 \text{ kV},$	See Note 6			1011		Ω

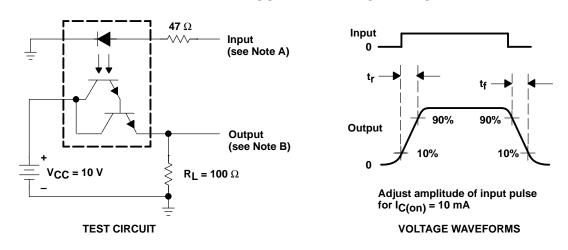
NOTE 6. These parameters are measured between all input-diode leads shorted together and all phototransistor leads shorted together.



switching characteristics at 25°C free-air temperature

	PARAMETER	TEST CONDITIONS				TYP	MAX	UNIT
t _r	Rise time	V _{CC} = 10 V,	lar > = 10 mA	Ω, See Figure 1	100			
t _f	Fall time		$I_{C(on)} = 10 \text{ mA}, R_L = 100 \Omega$	sz, see rigure i		100		μs

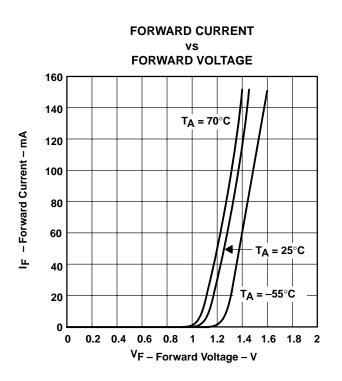
PARAMETER MEASUREMENT INFORMATION

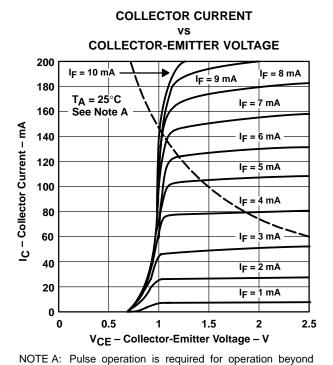


NOTES: A. The input waveform is supplied by a generator with the following characteristics: $Z_0 = 50 \,\Omega$, $t_r \le 15 \,\text{ns}$, duty cycle = 1%, $t_W = 500 \,\mu\text{s}$. B. The output waveform is monitored on an oscilloscope with the following characteristics: $t_r \le 12 \,\text{ns}$, $R_{in} \ge 1 \,\text{M}\Omega$, $C_{in} \le 20 \,\text{pF}$.

Figure 1. Switching Times

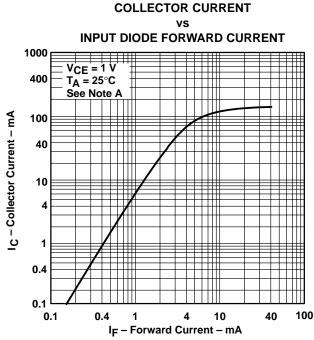
TYPICAL CHARACTERISTICS





limits shown by the dashed line.

Figure 2 Figure 3



NOTE A: These parameters are measured using pulse techniques $t_W = 1 \text{ ms}$, duty cycle $\leq 2\%$.

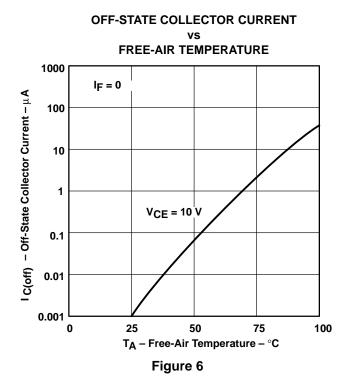
Figure 4



TYPICAL CHARACTERISTICS

RELATIVE ON-STATE COLLECTOR CURRENT FREE-AIR TEMPERATURE 1.2 Collector Current Relative to Value at TA = 25 $^{\circ}\text{C}$ V_{CE} = 1 V IF = 10 mA lF = 2 mA 0.8 I_F = 10 mA 0.6 0.4 IF = 2 mA 0.2 -75 -50 -25 25 75 100 $T_{\mbox{A}}$ – Free-Air Temperature – $^{\circ}\mbox{C}$

Figure 5



IMPORTANT NOTICE

Texas Instruments (TI) reserves the right to make changes to its products or to discontinue any semiconductor product or service without notice, and advises its customers to obtain the latest version of relevant information to verify, before placing orders, that the information being relied on is current.

TI warrants performance of its semiconductor products and related software to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

Certain applications using semiconductor products may involve potential risks of death, personal injury, or severe property or environmental damage ("Critical Applications").

TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, INTENDED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT APPLICATIONS, DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS.

Inclusion of TI products in such applications is understood to be fully at the risk of the customer. Use of TI products in such applications requires the written approval of an appropriate TI officer. Questions concerning potential risk applications should be directed to TI through a local SC sales office.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards should be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance, customer product design, software performance, or infringement of patents or services described herein. Nor does TI warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used.

Copyright © 1996, Texas Instruments Incorporated