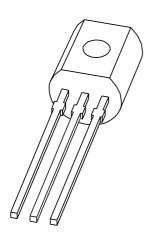
DISCRETE SEMICONDUCTORS

DATA SHEET



BC559; BC560 PNP general purpose transistors

Product specification
Supersedes data of 1997 Mar 14
File under Discrete Semiconductors, SC04

1997 Jun 03





PNP general purpose transistors

BC559; BC560

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 45 V).

APPLICATIONS

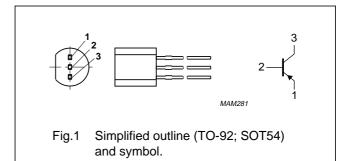
• General purpose switching and amplification.

DESCRIPTION

PNP transistor in a TO-92; SOT54 plastic package. NPN complements: BC549 and BC550.

PINNING

PIN	DESCRIPTION	
1	emitter	
2	base	
3	collector	



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC559		_	-30	V
	BC560		_	-50	V
V _{CEO}	collector-emitter voltage	open base			
	BC559		_	-30	V
	BC560		_	-45	V
I _{CM}	peak collector current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	500	mW
h _{FE}	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	125	800	
f _T	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	MHz

PNP general purpose transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter			
	BC559		_	-30	V
	BC560		_	-50	V
V _{CEO}	collector-emitter voltage	open base			
	BC559		_	-30	V
	BC560		_	-45	V
V _{EBO}	emitter-base voltage	open collector	_	- 5	V
Ic	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	500	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	250	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0; V_{CB} = -30 \text{ V}$	_	-1	-15	nA
		$I_E = 0$; $V_{CB} = -30 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	_	-4	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	_	-100	nA
h _{FE}	DC current gain	$I_C = -2 \text{ mA}$; $V_{CE} = -5 \text{ V}$; see Figs 2, 3 and 4	125	_	800	
h _{FE}	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V};$				
	BC559A	see Figs 2, 3 and 4	125	_	250	
	BC559B; BC560B		220	_	475	
	BC559C; BC560C		420	_	800	
V _{CEsat}	collector-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-60	-300	mV
		$I_C = -100 \text{ mA}; I_B = -5 \text{ mA}$	_	-180	-650	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -10 \text{ mA}$; $I_B = -0.5 \text{ mA}$; note 1	_	-750	_	mV
		$I_C = -100 \text{ mA}$; $I_B = -5 \text{ mA}$; note 1	_	-930	_	mV

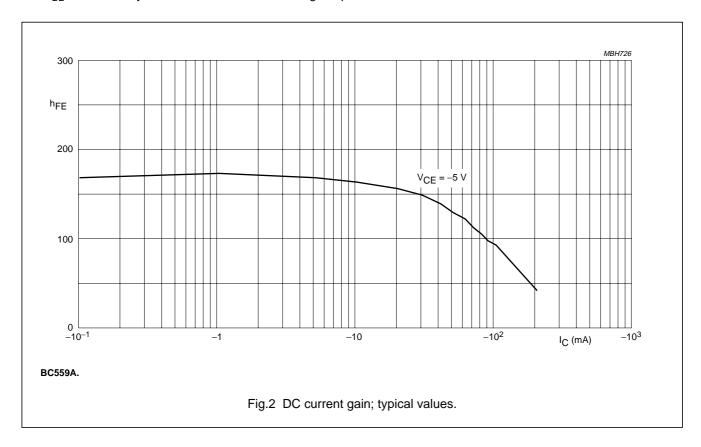
PNP general purpose transistors

BC559; BC560

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}; \text{ note } 2$	-600	-650	-750	mV
		I _C = -10 mA; V _{CE} = -5 V; note 2	_	_	-820	mV
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	4	_	pF
f _T	transition frequency	$I_E = -10 \text{ mA}; V_{CB} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure BC559A; BC560A BC559B; BC560B; BC559C; BC560C	I_C = -200 μA; V_{CE} = -5 V; R_S = 2 kΩ; f = 30 Hz to 15.7 kHz			10 4	dB dB
F	noise figure BC559A; BC560A BC559B; BC560B; BC559C; BC560C	$I_C = -200 \ \mu A; \ V_{CE} = -5 \ V; \ R_S = 2 \ k\Omega;$ $f = 1 \ kHz; \ B = 200 \ Hz$	-		10 4	dB dB

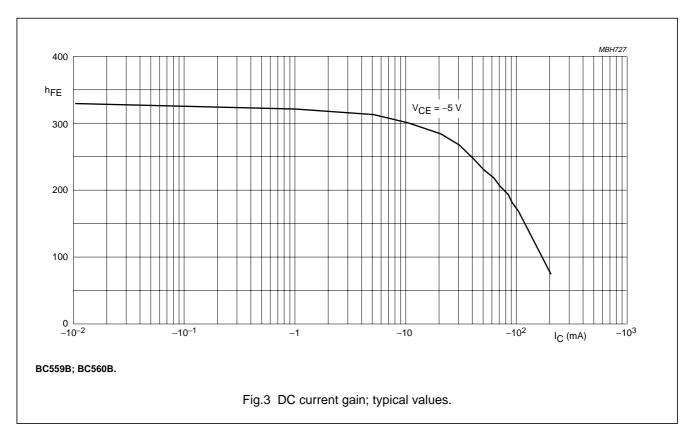
Notes

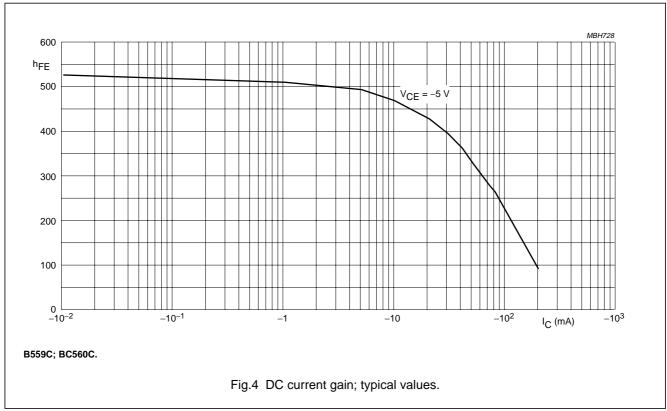
- 1. V_{BEsat} decreases by about –1.7 mV/K with increasing temperature.
- 2. V_{BE} decreases by about -2 mV/K with increasing temperature.



PNP general purpose transistors

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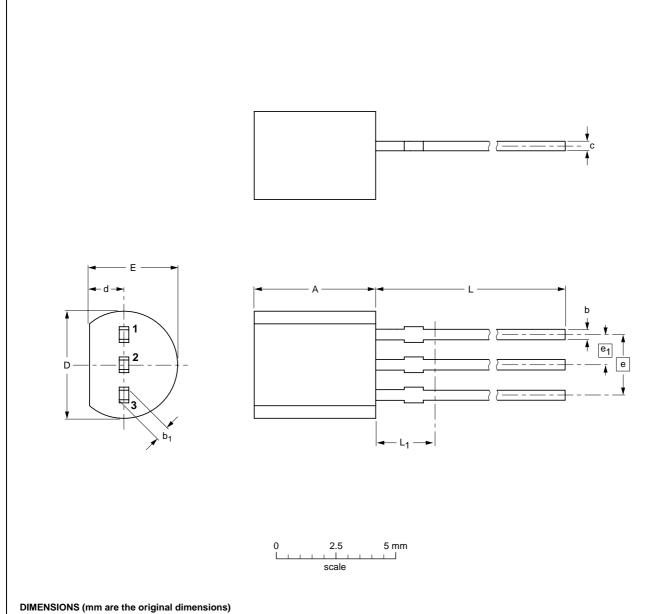
PNP general purpose transistors

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



UNIT	A	b	b ₁	С	D	d	E	е	e ₁	L	L ₁ ⁽¹⁾
mm	5.2 5.0	0.48 0.40	0.66 0.56	0.45 0.40	4.8 4.4	1.7 1.4	4.2 3.6	2.54	1.27	14.5 12.7	2.5

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE		REFER	ENCES	EUROPEAN ISSUE DATE		
VERSION	IEC	JEDEC	EIAJ	PROJECTION	ISSUE DATE	
SOT54		TO-92	SC-43		97-02-28	

1997 Jun 03 6

PNP general purpose transistors

BC559; BC560

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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