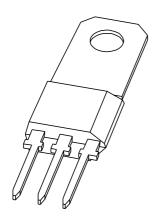
#### **DISCRETE SEMICONDUCTORS**

# DATA SHEET



## BD825; BD829 NPN power transistors

Product specification Supersedes data of September 1994 File under Discrete Semiconductors, SC04 1997 Jun 20





### **NPN** power transistors

BD825; BD829

#### **FEATURES**

- High current (max. 1 A)
- Low voltage (max. 80 V).

#### **APPLICATIONS**

- General purpose
- Driver stages in hi-fi amplifiers and television circuits.

#### **DESCRIPTION**

NPN power transistor in a TO-202; SOT128B plastic package. PNP complements: BD826 and BD830.

#### **PINNING**

PIN	DESCRIPTION
1	emitter
2	collector, connected to metal part of mounting surface
3	base

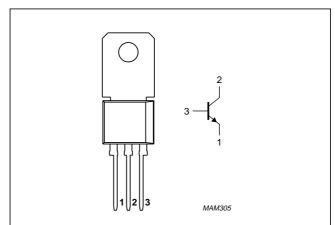


Fig.1 Simplified outline (TO-202; SOT128B) and symbol.

#### **QUICK REFERENCE DATA**

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter				
	BD825		_	_	45	V
	BD829		_	_	100	V
$V_{CEO}$	collector-emitter voltage	open base				
	BD825		_	_	45	V
	BD829		_	_	80	V
I <sub>CM</sub>	peak collector current		_	_	1.5	Α
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	_	2	W
		T <sub>mb</sub> ≤ 50 °C	_	_	8	W
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 150 mA; V <sub>CE</sub> = 2 V	40	_	250	
f <sub>T</sub>	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 5 \text{ V}; f = 100 \text{ MHz}$	_	250	_	MHz

### NPN power transistors

BD825; BD829

#### **LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter			
	BD825		_	45	V
	BD829		_	100	V
V <sub>CEO</sub>	collector-emitter voltage	open base			
	BD825		_	45	V
	BD829		_	80	V
V <sub>EBO</sub>	emitter-base voltage	open collector	_	5	V
I <sub>C</sub>	collector current (DC)		_	1	Α
I <sub>CM</sub>	peak collector current		_	1.5	Α
I <sub>BM</sub>	peak base current		_	500	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	_	2	W
		T <sub>mb</sub> ≤ 50 °C	_	8	W
T <sub>stg</sub>	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	in free air	62.5	K/W
R <sub>th j-mb</sub>	thermal resistance from junction to mounting base		12.5	K/W

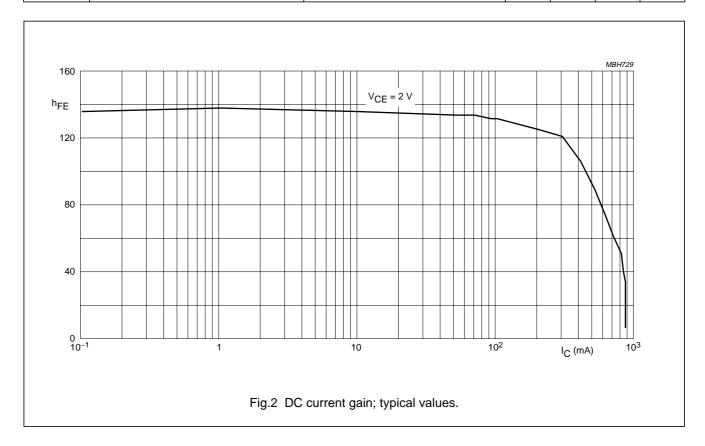
### NPN power transistors

BD825; BD829

#### **CHARACTERISTICS**

 $T_j = 25$  °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V	_	_	100	nA
		I <sub>E</sub> = 0; V <sub>CB</sub> = 30 V; T <sub>j</sub> = 125 °C	_	_	10	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	_	_	100	nA
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = 2 V; see Fig.2				
		$I_C = 5 \text{ mA}$	40	_	-	
		I <sub>C</sub> = 150 mA	40	_	250	
		I <sub>C</sub> = 500 mA	25	_	-	
h <sub>FE</sub>	DC current gain	$I_C = 150 \text{ mA}; V_{CE} = 2 \text{ V}; \text{ see Fig.2}$				
	BD825-10; BD829-10		63	_	160	
	BD825-16; BD829-16		100	_	250	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 500 mA	_	_	500	mV
V <sub>BE</sub>	base-emitter voltage	I <sub>C</sub> = 500 mA; V <sub>CE</sub> = 2 V	_	_	1	V
f <sub>T</sub>	transition frequency	$I_C = 50 \text{ mA}; V_{CE} = 5 \text{ V};$ f = 100 MHz	_	250	_	MHz



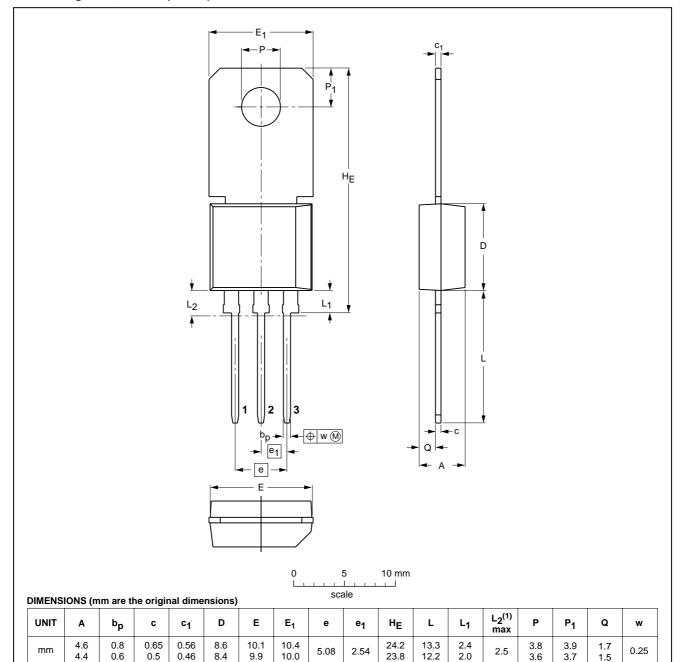
### NPN power transistors

BD825; BD829

#### **PACKAGE OUTLINE**

Plastic single-ended leaded (through hole) package; with cooling fin, mountable to heatsink, 1 mounting hole; 3 leads (in-line)

SOT128B



#### Note

1. Plastic flash allowed within this zone

0.6

0.5

0.46

8.4

9.9

10.0

OUTLINE		REFER	ENCES	EUROPEAN ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ	PROJECTION	1330E DATE
SOT128B		TO-202			97-02-28

23.8

12.2

1997 Jun 20 5

#### NPN power transistors

BD825; BD829

#### **DEFINITIONS**

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

#### Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.

### NPN power transistors

BD825; BD829

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