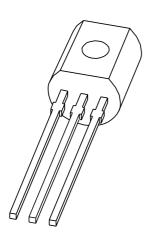
DISCRETE SEMICONDUCTORS

DATA SHEET



2N4401 NPN switching transistor

Product specification Supersedes data of September 1994 File under Discrete Semiconductors, SC04 1997 May 07





NPN switching transistor

2N4401

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 40 V).

APPLICATIONS

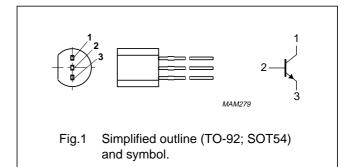
• Industrial and consumer switching applications.

DESCRIPTION

NPN switching transistor in a TO-92; SOT54 plastic package. PNP complement: 2N4403.

PINNING

PIN	DESCRIPTION
1	collector
2	base
3	emitter



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	60	V
V _{CEO}	collector-emitter voltage	open base	_	40	V
I _C	collector current (DC)		_	600	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	630	mW
h _{FE}	DC current gain	I _C = 150 mA; V _{CE} = 1 V	150	300	
f _T	transition frequency	I _C = 20 mA; V _{CE} = 10 V; f = 100 MHz	250	_	MHz
t _{off}	turn-off time	$I_{Con} = 150 \text{ mA}; I_{Bon} = 15 \text{ mA}; I_{Boff} = -15 \text{ mA}$	_	250	ns

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	60	V
V _{CEO}	collector-emitter voltage	open base	_	40	٧
V _{EBO}	emitter-base voltage	open collector	_	6	٧
I _C	collector current (DC)		_	600	mA
I _{CM}	peak collector current		_	800	mA
I _{BM}	peak base current		_	200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C; note 1	_	630	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

Note

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th j-a}	thermal resistance from junction to ambient	note 1	200	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

^{1.} Transistor mounted on an FR4 printed-circuit board.

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CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

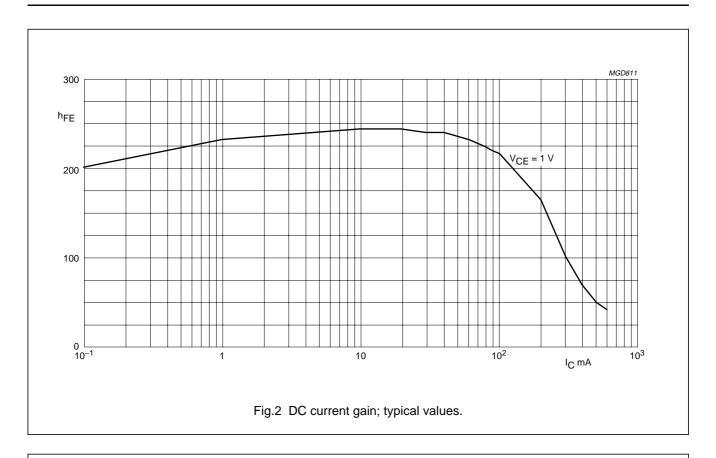
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 60 V	_	50	nA
I _{EBO}	emitter cut-off current	I _C = 0; V _{EB} = 6 V	_	50	nA
h _{FE}	DC current gain	V _{CE} = 1 V; see Fig.2			
		I _C = 0.1 mA	20	-	
		I _C = 1 mA	40	_	
		I _C = 10 mA	80	_	
		I _C = 150 mA; note 1	100	300	
h _{FE}	DC current gain	I _C = 500 mA; V _{CE} = 2 V; note 1	40	_	
V _{CEsat}	collector-emitter saturation voltage	I _C = 150 mA; I _B = 15 mA; note 1	_	400	mV
		I _C = 500 mA; I _B = 50 mA; note 1	_	750	mV
V _{BEsat}	base-emitter saturation voltage	I _C = 150 mA; I _B = 15 mA; note 1	_	950	mV
		$I_C = 500 \text{ mA}; I_B = 50 \text{ mA}; \text{ note 1}$	_	1.2	V
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 5$ V; $f = 1$ MHz	_	6.5	pF
C _e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 500 \text{ mV}$; $f = 1 \text{ MHz}$	_	30	pF
f _T	transition frequency	$I_C = 20 \text{ mA}; V_{CE} = 10 \text{ V}; f = 100 \text{ MHz}$	250	_	MHz
Switching	times (between 10% and 90% levels)	; see Fig.3			
t _{on}	turn-on time	I _{Con} = 150 mA; I _{Bon} = 15 mA;	_	35	ns
t _d	delay time	$I_{Boff} = -15 \text{ mA}$	_	15	ns
t _r	rise time		_	20	ns
t _{off}	turn-off time		_	250	ns
ts	storage time		_	200	ns
t _f	fall time		_	60	ns

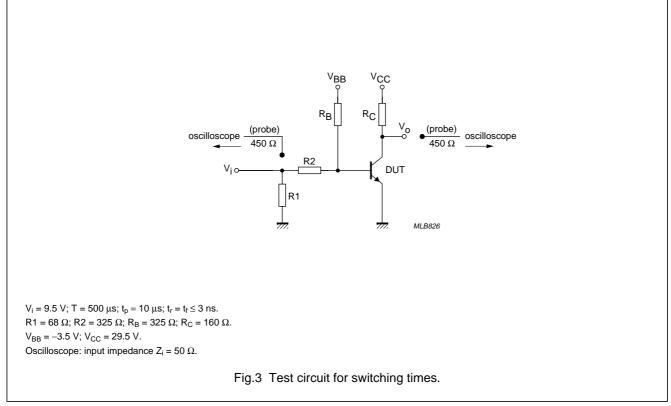
Note

1. Pulse test: $t_p \le 300~\mu s;~\delta \le 0.02.$

NPN switching transistor

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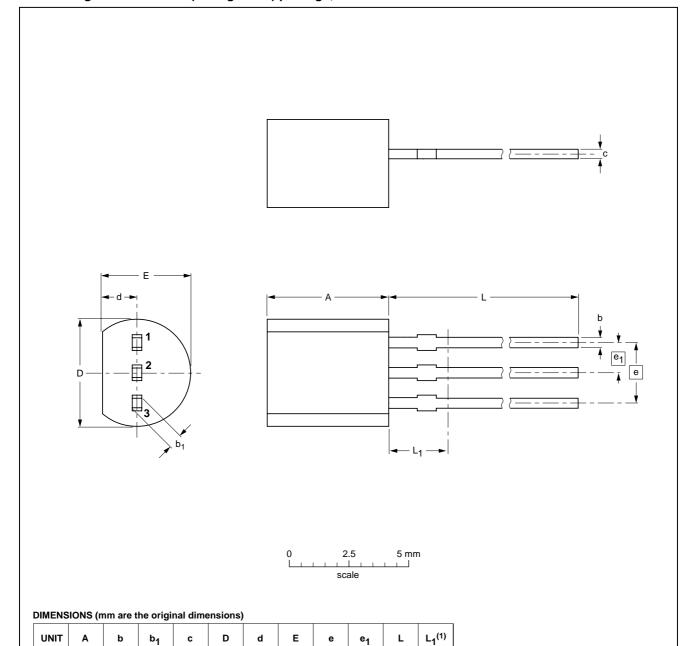
NPN switching transistor

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PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads

SOT54



mm

Note

0.48

1. Terminal dimensions within this zone are uncontrolled to allow for flow of plastic and terminal irregularities.

OUTLINE REFERENCES		EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	EIAJ		PROJECTION ISSUE DAT	
SOT54		TO-92	SC-43			97-02-28

1.27

2.5

2.54

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0.45

4.8

NPN switching transistor

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DEFINITIONS

Data sheet status				
Objective specification	This data sheet contains target or goal specifications for product development.			
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.			
Product specification	This data sheet contains final product specifications.			
Limiting values				
Limiting values given are in accordance with the Absolute Mavinsum Dating Custom (IEC 424). Stress above one or				

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

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