# T0610xH T0612xH

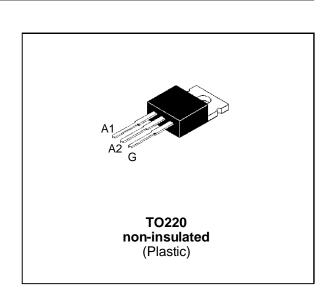
# STANDARD TRIACS

#### **FEATURES**

- $I_{T(RMS)} = 6A$
- $V_{DRM} = 400 \text{V to } 800 \text{V}$
- High surge current capability



The T06xxxH series of triacs uses a high performance MESA GLASS technology. These parts are intended for general purpose switching and phase control applications.



#### **ABSOLUTE RATINGS** (limiting values)

Symbol	Parameter	Value	Unit	
I <sub>T(RMS)</sub>	RMS on-state current (360° conduction angle)	Tc= 100 °C	6	А
I <sub>TSM</sub>	Non repetitive surge peak on-state current	tp = 8.3 ms	66	Α
	(T <sub>j</sub> initial = 25°C)	tp = 10 ms	60	
l <sup>2</sup> t	$I^2$ t Value for fusing tp = 10 ms		18	A <sup>2</sup> s
dI/dt	Critical rate of rise of on-state current $I_G = 500 \text{ mA}$ $di_G/dt = 1 \text{ A/}\mu\text{s}$ .		10	A/μs
	Non Repetitive		50	
T <sub>stg</sub> T <sub>j</sub>	Storage and operating junction temperature	- 40, + 150 - 40, + 125	°C	
TI	Maximum lead temperature for soldering dur 4.5mm from case	260	°C	

Symbol	Parameter		Unit			
		D	М	S	N	
VDRM VRRM	Repetitive peak off-state voltage $T_j = 125^{\circ}C$	400	600	700	800	V

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# T0610xH/T0612xH

#### THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
Rth(j-a)	Junction to ambient	60	°C/W
Rth(j-c)	Junction to case for D.C	4	°C/W
Rth(j-c)	Junction to case for A.C 360° conduction angle (F=50Hz)	3	°C/W

### **GATE CHARACTERISTICS** (maximum values)

 $P_{G (AV)} = 1 W$   $P_{GM} = 10 W (tp = 20 \mu s)$   $I_{GM} = 4 A (tp = 20 \mu s)$ 

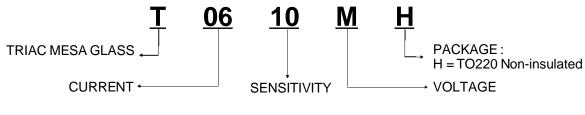
# **ELECTRICAL CHARACTERISTICS**

Symbol	Test Conditions		Quadrant		Sensitivity		Unit
Symbol	rest Conditions	•	Quadrant		10	12	
IGT	$V_D=12V$ (DC) $R_L=33\Omega$	Tj= 25°C	I-II-III-IV	MAX	25 50		mA
V <sub>GT</sub>	V <sub>D</sub> =12V (DC) R <sub>L</sub> =33Ω	Tj= 25°C	I-II-III-IV	MAX	1.5		V
$V_{GD}$	$V_D=V_{DRM}$ $R_L=3.3k\Omega$	Tj= 125°C	I-II-III-IV	MIN	0.2		V
tgt	$V_D=V_{DRM}$ $I_G=500$ mA $I_T=8.5$ A $dI_G/dt=3$ A/ $\mu$ s	Tj= 25°C	I-II-III-IV	TYP	2		μs
I <sub>H</sub> *	I <sub>T</sub> = 250 mA Gate open	Tj= 25°C		MAX	25	50	mA
IL	I <sub>G</sub> = 1.2 I <sub>G</sub> T	Tj= 25°C	I-III-IV	TYP	25 50		mA
			Ш	TYP	50	100	
V <sub>TM</sub> *	ITM= 8.5A tp= 380μs	Tj= 25°C		MAX	1.65		V
$I_{DRM}$ $V_{D} = V_{DRM}$		Tj= 25°C		MAX	5		μΑ
I <sub>RRM</sub>	$V_R = V_{RRM}$	Tj= 110°C		MAX	2		mA
dV/dt*	VD=67%V <sub>DRM</sub> Gate open	=		MIN	200	500	V/μs
(dV/dt)c*	(dl/dt)c = 2.7 A/ms	Tj= 110°C		MIN	MIN 2 5		V/μs

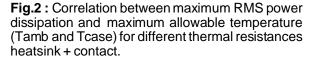
<sup>\*</sup> For either polarity of electrode A2 voltage with reference to electrode A1

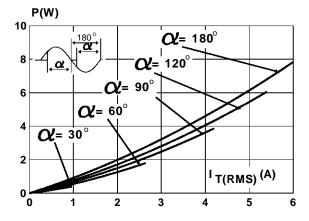
#### **ORDERING INFORMATION**

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**Fig.1**: Maximum RMS power dissipation versus RMS on-state current.

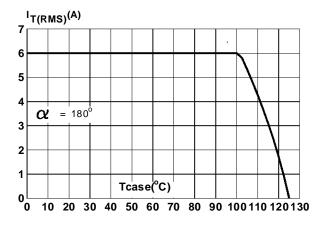


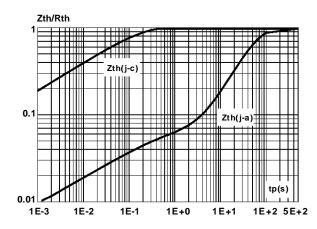


P (W) Tcase (°C) 10 Rth =  $0^{\circ}$  C/W 2.5° C/W 5° C/W 8 -100 10° C/W -105 6 -110 115 2 120 Tamb (°C) 125 140 20 60 100 120 0 40 80

**Fig.3**: RMS on-state current versus case temperature.

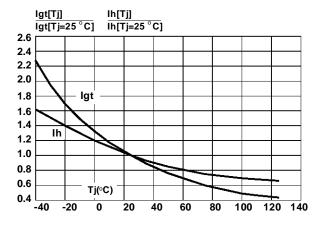
**Fig.4**: Relative variation of thermal impedance versus pulse duration.

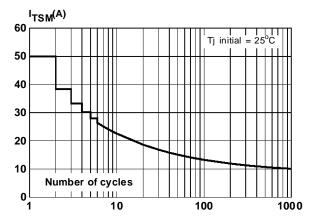




**Fig.5**: Relative variation of gate trigger current and holding current versus junction temperature.

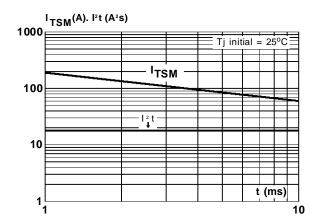
**Fig.6:** Non repetitive surge peak on-state current versus number of cycles.

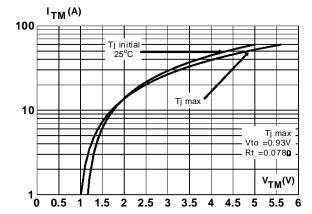




**Fig.7**: Non repetitive surge peak on-state current for a sinusoidal pulse with width :  $t \le 10$ ms, and corresponding value of  $l^2t$ .

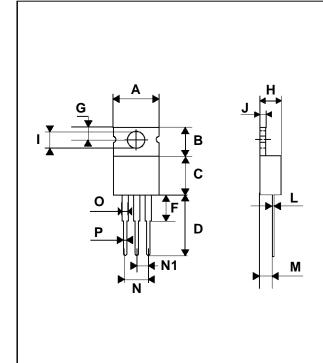
Fig.8: On-state characteristics (maximum values).





#### PACKAGE MECHANICAL DATA

TO220 Non-insulated (Plastic)



	DIMENSIONS						
REF.	Millimeters			Inches			
	Тур.	Min.	Max.	Тур.	Min.	Max.	
Α			10.3			0.406	
В		6.3	6.5	0.248	0.256		
С			9.1			0.358	
D		12.7			0.500		
F			4.2			0.165	
G			3.0			0.118	
Н		4.5	4.7		0.177	0.185	
ı		3.53	3.66		0.139	0.144	
J		1.2	1.3		0.047	0.051	
L			0.9			0.035	
М	2.7			0.106			
N			5.3			0.209	
N1	2.54			0.100			
0		1.2	1.4		0.047	0.055	
Р			1.15			0.045	

Marking: type number

Weight: 1.8 g

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