- Designed for Complementary Use with the BD246 Series
- 80 W at 25°C Case Temperature
- 10 A Continuous Collector Current
- 15 A Peak Collector Current
- Customer-Specified Selections Available

SOT-93 PACKAGE (TOP VIEW) B 1 C 2 3

Pin 2 is in electrical contact with the mounting base.

MDTRAA

absolute maximum ratings at 25°C case temperature (unless otherwise noted)

RATING			VALUE	UNIT
	BD245		55	
Collector-emitter voltage (R_{BE} = 100 Ω)	BD245A		70	V
	BD245B	V _{CER}	90	
	BD245C		115	
	BD245		45	
Collector-emitter voltage (I _C = 30 mA)	BD245A		60	V
	BD245B	V _{CEO}	80	
	BD245C		100	
Emitter-base voltage			5	V
Continuous collector current			10	Α
Peak collector current (see Note 1)			15	Α
Continuous base current			3	Α
Continuous device dissipation at (or below) 25°C case temperature (see Note 2)			80	W
Continuous device dissipation at (or below) 25°C free air temperature (see Note 3)			3	W
Unclamped inductive load energy (see Note 4)			62.5	mJ
Operating junction temperature range			-65 to +150	°C
Storage temperature range			-65 to +150	°C
Lead temperature 3.2 mm from case for 10 seconds	TL	250	°C	

NOTES: 1. This value applies for $t_p \le 0.3$ ms, duty cycle $\le 10\%$.

- 2. Derate linearly to 150°C case temperature at the rate of 0.64 W/°C.
- 3. Derate linearly to 150°C free air temperature at the rate of 24 mW/°C.
- 4. This rating is based on the capability of the transistor to operate safely in a circuit of: L = 20 mH, $I_{B(on)}$ = 0.4 A, R_{BE} = 100 Ω , $V_{BE(off)}$ = 0, R_{S} = 0.1 Ω , V_{CC} = 20 V.



BD245, BD245A, BD245B, BD245C NPN SILICON POWER TRANSISTORS

JUNE 1973 - REVISED MARCH 1997

electrical characteristics at 25°C case temperature

PARAMETER		TEST CONDITIONS			MIN	TYP	MAX	UNIT	
V _{(BR)CEO}	Collector-emitter	I _C = 30 mA	I _B = 0	BD245 BD245A BD245B	45 60 80			V	
	broakdown volkago	(see Note 5)		BD245C	100				
		V _{CE} = 55 V	$V_{BE} = 0$	BD245			0.4	mA	
1	Collector-emitter	$V_{CE} = 70 V$	$V_{BE} = 0$	BD245A			0.4		
ICES	cut-off current	V _{CE} = 90 V	$V_{BE} = 0$	BD245B			0.4	ША	
		V _{CE} = 115 V	$V_{BE} = 0$	BD245C			0.4		
1	Collector cut-off	V _{CE} = 30 V	I _B = 0	BD245/245A			0.7	mA	
I _{CEO}	current	$V_{CE} = 60 \text{ V}$	$I_B = 0$	BD245B/245C			0.7		
I _{EBO}	Emitter cut-off current	V _{EB} = 5 V	I _C = 0				1	mA	
	Forward current transfer ratio	V _{CE} = 4 V	C	40					
h _{FE}		transfer ratio $V_{CE} = 4 V$	-	$I_C = 3 A$	(see Notes 5 and 6)	20			
		$V_{CE} = 4 V$	$I_C = 10 A$		4				
V _{CE(sat)}	Collector-emitter	$I_B = 0.3 A$	$I_C = 3 A$	(see Notes 5 and 6)			1	V	
*CE(sat)	saturation voltage	$I_B = 2.5 A$	$I_C = 10 A$				4	•	
V _{BE}	Base-emitter	$V_{CE} = 4 V$	$I_C = 3 A$	(see Notes 5 and 6)			1.6	V	
*BE	voltage	V _{CE} = 4 V	I _C = 10 A				3	•	
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	I _C = 0.5 A	f = 1 kHz	20				
h _{fe}	Small signal forward current transfer ratio	V _{CE} = 10 V	$I_{\rm C} = 0.5 {\rm A}$	f = 1 MHz	3				

NOTES: 5. These parameters must be measured using pulse techniques, t_p = 300 μs , duty cycle \leq 2%.

thermal characteristics

	PARAMETER		TYP	MAX	UNIT
$R_{\theta JC}$	Junction to case thermal resistance			1.56	°C/W
$R_{\theta JA}$	Junction to free air thermal resistance			42	°C/W

resistive-load-switching characteristics at 25°C case temperature

	PARAMETER	TEST CONDITIONS †			MIN	TYP	MAX	UNIT
t _{on}	Turn-on time	I _C = 1 A	$I_{B(on)} = 0.1 A$	$I_{B(off)} = -0.1 A$		0.3		μs
t _{off}	Turn-off time	$V_{BE(off)} = -3.7 V$	$R_L = 20 \Omega$	$t_p = 20 \ \mu s, \ dc \le 2\%$		1		μs

[†] Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

^{6.} These parameters must be measured using voltage-sensing contacts, separate from the current carrying contacts.

TYPICAL CHARACTERISTICS

TYPICAL DC CURRENT GAIN VS COLLECTOR CURRENT TCS633AG $V_{CE} = 4 V$ $V_{CE} = 25^{\circ}C$ $V_{DE} = 300 \,\mu\text{s}$, duty cycle < 2% $V_{DE} = 100 \,\mu\text{s}$ $V_{$

Figure 1.

COLLECTOR-EMITTER SATURATION VOLTAGE

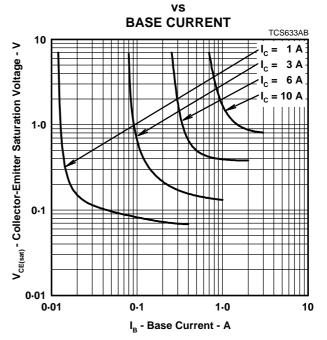
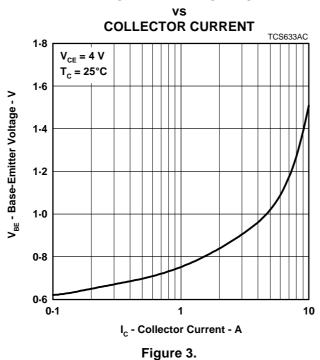


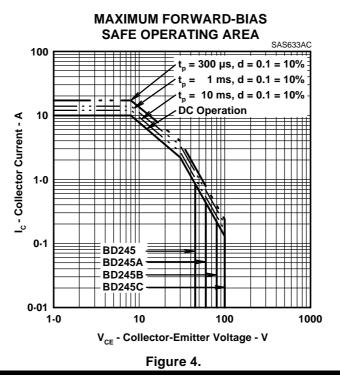
Figure 2.





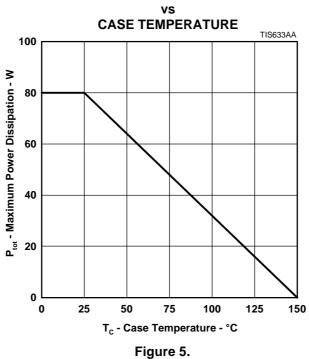
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MAXIMUM SAFE OPERATING REGIONS



THERMAL INFORMATION

MAXIMUM POWER DISSIPATION



INFORMATION

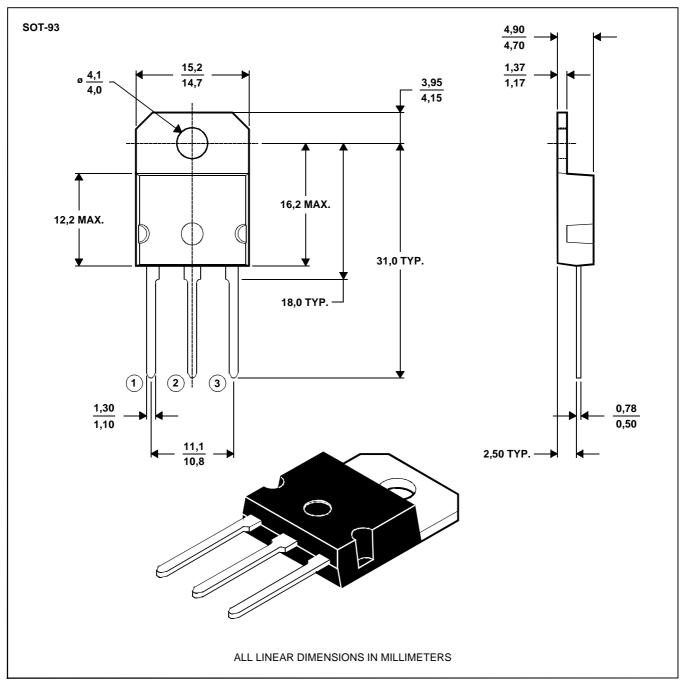
PRODUCT

MECHANICAL DATA

SOT-93

3-pin plastic flange-mount package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: The centre pin is in electrical contact with the mounting tab.

MDXXAW



BD245, BD245A, BD245B, BD245C NPN SILICON POWER TRANSISTORS

JUNE 1973 - REVISED MARCH 1997

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