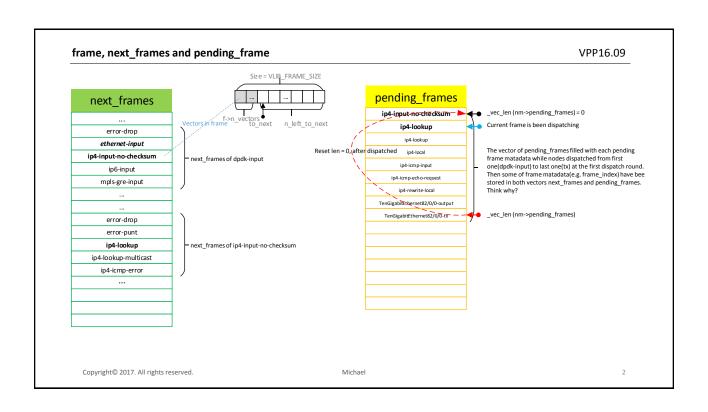
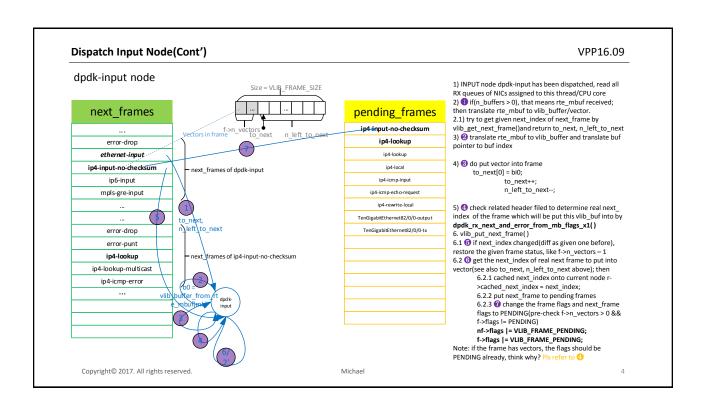
Worker Thread

Michael 2017

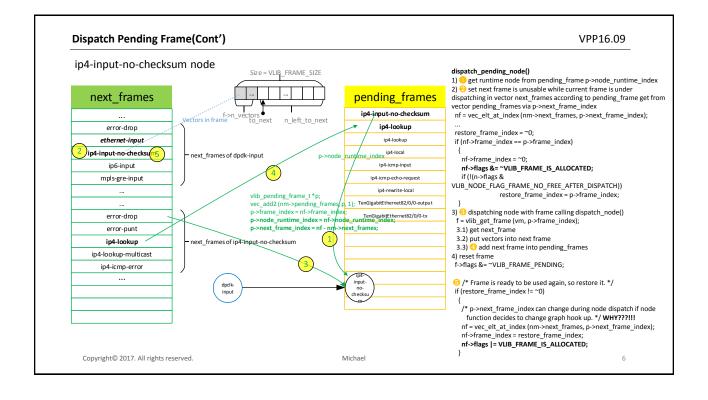


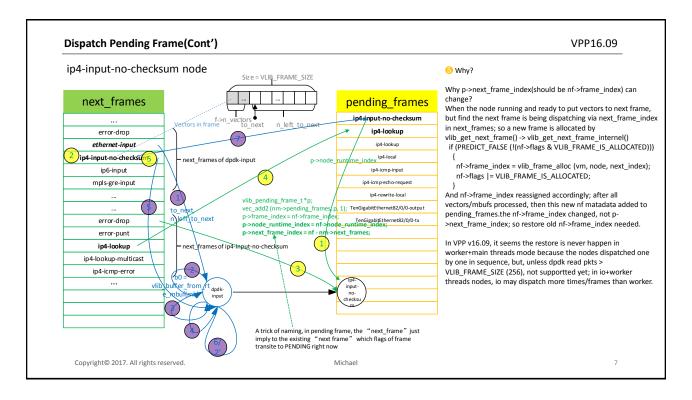
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Dispatch Pending Frame VPP16.09 vlib node runtime t *n; pending_frames vec_foreach (n, nm->nodes_by_type[VLIB_NODE_TYPE_INPUT]) ip4-input-no-checksum ip4-lookup cpu_time_now = dispatch_node (vm, n, VLIB_NODE_TYPE_INPUT, ip4-lookup VLIB_NODE_STATE_POLLING, /* frame */ 0, ip4-local cpu_time_now); ip4-icmp-input } ip4-icmp-echo-request ip4-rewrite-local TenGigabitEthernet82/0/0-output TenGigabitEthernet82/0/0-tx Copyright© 2017. All rights reserved. Michael









Output node: <interface name>_output

vnet_interface_output_node_no_flatten()

- vnet_interface_output_node_no_flatten_inline()
- -- vlib_get_new_next_frame(), call this if the frame of tx node has some vectors not dispatched, so allocate new frame and reassign nf->frame_index. If IO thread available, then above scenario could be, because IO thread may run slowly; otherwise if only worker threads, the scenario never happen. If vlib buffer
- if ((b->flags & BUFFER_OUTPUT_FEAT_DONE) == 0)

While dispatching vector, check

count trailing zeros (next1, vnet buffer (b1)->output features.bitmap);

To determine that whether output node need to dispatch the vector to the frame which "next1" pointed to. HOW ABOUT OUTPUT FEATURE WORKS?

Otherwise output node dispatch vectors to TX frame with next_index = VNET_INTERFACE_OUTPUT_NEXT_TX

TX node:

dpdk_interface_tx()

- get tx_vector/tx_ring
- translate vlib_buffer to rte_mbuf

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- Transit

Actually each TX queue has its own a pair of <ifname>_output and <ifname>_tx nodes in the same worker threads. So the previous node of <ifname>_output node should determine output interface in advance. E.g. in node ip4-rewrite-transit $next0 = (error0 == IP4_ERROR_NONE) ? adj0[0].rewrite_header.next_index : next0;$

 $Think \ why? See \ also \ dispatch_process() <- \ ... <- \ dpdk_process() <- \ dpdk_lib_init() <- \ ethernet_register_interface() <- \ vnet_register_interface() <- \ vnet_register_inte$

Michael

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