

AS5600_soft_IIC::start_iic

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graph LR; A[AS5600_soft_IIC::start_iic] --> B[AS5600_soft_IIC::write_iic]; B --> C[AS5600_soft_IIC::wait_ack_iic];
```

The diagram illustrates a three-step process for IIC communication. It begins with a grey box labeled 'AS5600_soft_IIC::start_iic'. A blue arrow points from this box to a white box labeled 'AS5600_soft_IIC::write_iic'. Another blue arrow points from the second box to a third white box labeled 'AS5600_soft_IIC::wait_ack_iic'.

AS5600_soft_IIC::write_iic

AS5600_soft_IIC::wait
_ack_iic