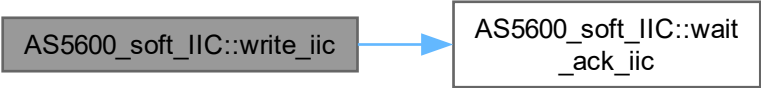


AS5600_soft_IIC::write_iic



```
graph LR; A[AS5600_soft_IIC::write_iic] --> B[AS5600_soft_IIC::wait_ack_iic]
```

AS5600_soft_IIC::wait
_ack_iic