





Exercise 2: Solutions

Lecture, Summer 2019

Prof. Dr. Matthias S. Müller Dr. Christian Terboven Simon Schwitanski Julian Miller







Hardware Accelerators

K-means Clustering







Task 1

Task 1.1 Preparations







- This exercise can be done on the RWTH cluster environment CLAIX
- Access via lecture project lect0034
 - → Register for an account "Hochleistungsrechnen RWTH Aachen" at the selfservice (http://www.rwth-aachen.de/selfservice)
 - → Sent your TIM ID to contact@hpc.rwth-aachen.de in case you are no member yet
- Any problems with the cluster environment?

Task 1.2 Implementation of K-means







Implement the proposed k-means algorithm as denoted in the code by TODO: task 1.2.

```
void k means(int niters, point t *points, point t *centroids,
             int *assignment, point t *result, int n, int k) {
  for (int iter = 0; iter < niters; ++iter) {</pre>
    // determine nearest centroids
    for (int i = 0; i < n; ++i) {
      double optimal dist = DBL MAX; // Calculate Euclidean
distance to each centroid and
         determine the closest mean
      for (int j = 0; j < k; ++j) {
        double dist = (points[i].x - centroids[j].x) *
                       (points[i].x - centroids[j].x) +
                       (points[i].y - centroids[i].y) *
                       (points[i].y - centroids[i].y);
        if (dist < optimal dist) {</pre>
          optimal dist = dist;
          assignment[i] = j;
```

Task 1.2 Implementation of K-means







```
// Calculate new positions of centroids
  int count[k];
  double sum x[k];
                                           $ make run-small
  double sum y[k];
                                           Executing k-means clustering with
  for (j = 0; j < k; ++j) {
                                           20 iterations, 1000 points, and 5
    count[j] = 0;
                                           centroids...
    sum x[j] = 0.0;
                                           Time Elapsed: 0.000235 s
    sum y[j] = 0.0;
  for (i = 0; i < n; ++i) {
    count[assignment[i]]++;
    sum x[assignment[i]] += points[i].x;
    sum y[assignment[i]] += points[i].y;
  for (j = 0; j < k; ++j) {
    if (count[j] != 0.0) {
       centroids[j].x = sum x[j] / count[j];
       centroids[j].y = sum y[j] / count[j];
} } }
```

Deploy: Check for Correctness







- Verify that the results obtained in task 1.2 are correct (template: TODO: task 1.3).
 - → Write initial position of centroids to result before execution the algorithm:

```
for (int i = 0; i < k; ++i) {
  result[i].x = centroids[i].x;
  result[i].y = centroids[i].y;
}</pre>
```

→ Store results after each iteration:

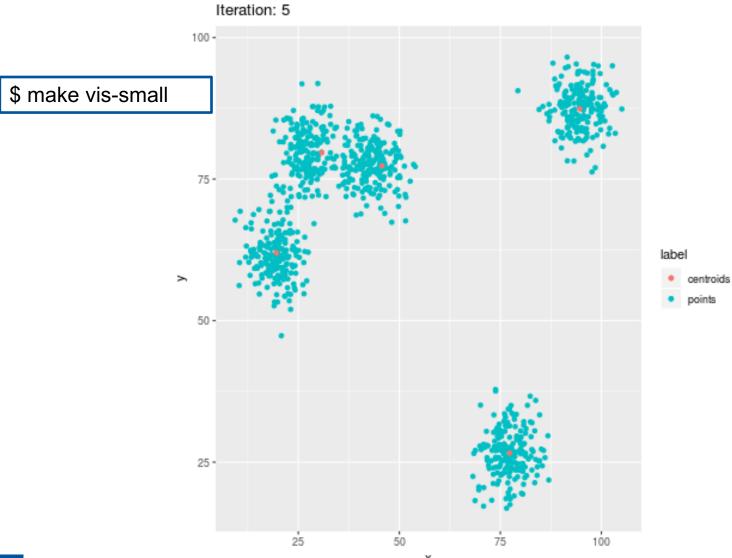
```
for (int j = 0; j < k; ++j) {
  if (count[j] != 0) {
    centroids[j].x = sum_x[j] / count[j];
    centroids[j].y = sum_y[j] / count[j];
  }
  result[(iter + 1) * k + j].x = centroids[j].x;
  result[(iter + 1) * k + j].y = centroids[j].y;
}</pre>
```

Task 1.3 Deploy: Check for Correctness









Task 1.4 Optimization of K-means







- Optimize the serial implementation.
- Remember AoS vs. SoA

```
struct point_t {
  double x;
  double y;
};
```

```
struct point_aos_t {
  double *x;
  double *y;
};
```

Address	0	8	16	24	32	40	
AoS	x[0]	y[0]	x[1]	y[1]	x[2]	y[2]	
SoA	x[0]	x[1]	x[2]	y[0]	y[1]	y[2]	

Task 1.4 Optimization of K-means





\$ make run-small

RWTHAACHEN UNIVERSITY

Executing k-means clustering with

20 iterations, 1000 points, and 5

Change all accesses from *[i].x to *->x[i]

```
centroids...
                                               Time Elapsed (AoS): 0.000235 s
int main(int argc, const char* argv[]) {
                                               Time Elapsed (SoA): 0.000235 s
  // Initialize points and centroids in SoA format
 point soa t points soa;
  points soa.x = (double*) malloc(n*sizeof(double));
  points soa.y = (double*) malloc(n*sizeof(double));
  for (int i = 0; i < (niters + 1) * k; ++i) {
    result[i].x = -1.0;
    result[i].y = -1.0;
  for (int i = 0; i < k; ++i) {
    result->y[i] = centroids->x[i];
    result->y[i] = centroids->y[i];
```

Task 1.4 Optimization of K-means







- Why is there no performance difference?
 - → Optimal coalescing: all data read is requested
- Would the following data structure change things?
 - → Yes, to compute distance only x and y are needed

→ Degree of coalescing =
$$\frac{\text{#bytes requested}}{\text{#bytes read}} = \frac{16 \text{ bytes}}{24 \text{ bytes}} = \frac{2}{3}$$

```
struct point_t {
  double x;
  double y;
  int assignment;
};
```

Address	0	4	8	12	16	20	24	28	32	36	40	44	48	52	56	60	64	68
AoS	x[0]	у[[0]	a[0]		x[1]		y[1]		a[1]		x[2]		y[2]		a[2]	
SoA	x[0]	x[[1]	x[2]	у[0]	y[1]		y[2]		a[0]	a[1]	a[2]			







Task 2

Preparations







Frontend for development and short test

- → \$ ssh login18-g-1.hpc.itc.rwth-aachen.de
- → Load NVIDIA compiler
 - \rightarrow \$ module load cuda/100
- → List loaded modules
 - →\$ module list

Backend for performance measurements

→ \$ sbatch kmeans gpu.sh

Assess: Investigation of Parallelism₂





Identify the hotspot of the algorithm of the optimized and checked serial version (task 1.4) and evaluate which parts are suitable for the GPU. Investigate which steps of the algorithm can be parallelized and how it can be achieved. Reason about dependencies.

- → Profiler shows 100% time spent in kmeans ()
- → 3 nested loops:

```
\rightarrow for (iter = 0; iter < NO ITER; ++iter) {
```

→Outer loop over iterations not parallelizable

```
\rightarrow for (i = 0; i < n; ++i) {
```

→ 2nd loop parallelizable (no data dependencies)

```
\rightarrow for (j = 0; j < k; ++j) {
```

→ 3rd loop parallelizable with a reduction on optimal_dist

Assess: Performance Modeling of K-Means





- Model the execution time t_{GPU} of the hotspot on the V100 GPU based on the performance model introduced in the lecture. What limits the execution time of the hotspot?
 - → No data dependencies in 2nd loop
 - → Many computations to offload

Assess: Performance Modeling of K-Means





- n*k operations per iteration with
 - → Simplification: leave out update to optimal_dist
 - → 7 DP operations
 - → 4 READS from main memory (down to 0 reads if points and centroids can be cached)

Assess: Performance Modeling of K-Means





→ Count, sum_x and sum_y
can be (for reasonable large k)
cached

- → 2n READ of points and n READ of assignment
- → 4k WRITE to centroids
 and result
- → 2n DP operations (additions on sum_x and sum_y) and 2k DP operations (divisions for calculation of centroids)

```
int count[k];
double sum x[k];
double sum y[k];
for (j = 0; j < k; ++j) {
  count[j] = 0;
  sum x[j] = 0.0; sum y[j] = 0.0;
for (i = 0; i < n; ++i) {
  count[assignment[i]]++;
  sum x[assignment[i]] += points[i].x;
  sum y[assignment[i]] += points[i].y;
for (int j = 0; j < k; ++j) {
  if (count[j] != 0) {
    centroids[j].x = sum x[j] / count[j];
    centroids[j].y = sum y[j] / count[j];
  result[(iter + 1) * k + j].x =
     centroids[j].x;
  result[(iter + 1) * k + j].y =
     centroids[j].y;
```

Assess: Performance Modeling of K-Means





Total kernel:

- \rightarrow n*k*7 + 2k +2n DP operations
- → n*k*4 + 2n + 4k DP READs/ WRITEs and n Integer READs
- → Dominated by main loop over n and k
- → Operational intensity of main loop: 7 Flops/32 byte

Assess: Performance Modeling of K-Means





Performance Model

$$\rightarrow t_{kernel} = \max(t_{compute}, t_{memory})$$

$$ightarrow t_{compute} = \frac{arithmetic operations [Flop]}{P_{max}}$$

- →No. DP operations: no_iter * n * k * 7 Flop
- \rightarrow P_{max} = 5120 cores * 1.3 GHz = 6656 GFlop/s

$$\rightarrow t_{memory} = \frac{data\ transfers(LOAD,STORE)\ [words]}{b_S}$$

- →Data transfers: no_iter * n * k * 4 Words
- →b_s measured with BabelStream benchmark: 865 GB/s

Assess: Performance Modeling of K-Means





Performance Model

$$\rightarrow t_{GPU} = t_{H2D} + t_{kernel} + t_{D2H}$$

$$\rightarrow t_{data} = t_{H2D} + t_{D2H}$$

- → H2D: points (2n doubles), centroids (2k doubles), assigments (n integers), result (2k (no iter+1) doubles)
- →D2H: result (2k (no iter+1) doubles)
- →b_{PCI} measured: 12 GB/s







Offload the identified hotspot in task 2.1 to a GPU while taking care of the required data.

```
int main(int argc, const char* argv[]) {
  // Allocate memory for GPU
 point t *d points = 0; point t *d centroids = 0;
  int *d assignments = 0; point t *d result = 0;
  cudaMalloc((void**)&d points, N * sizeof(point t));
  cudaMalloc((void**)&d centroids, K * sizeof(point t));
  cudaMalloc((void**)&d assignments, N * sizeof(int));
  cudaMalloc((void**)&d result, (niters + 1) * k * sizeof(point t));
  // Copy data to GPU
  double runtime all = get time();
  cudaMemcpy(d points, h points, N * sizeof(point t),
             cudaMemcpyHostToDevice);
  cudaMemcpy(d centroids, h centroids, K * sizeof(point t),
             cudaMemcpyHostToDevice);
  cudaMemcpy(d assignments, h assignments, N * sizeof(int),
             cudaMemcpyHostToDevice);
  cudaMemcpy(d_result, result, (niters + 1) * k * sizeof(point t),
             cudaMemcpyHostToDevice);
```







```
// Apply k-means algorithm
double runtime kernel = get time();
k means<<<(n + THREADSPERBLOCK - 1) / THREADSPERBLOCK,</pre>
            THREADSPERBLOCK>>>(d points, d centroids, d assignment
            d result, n, k);
cudaDeviceSynchronize();
runtime kernel = get time() - runtime kernel;
// Copy results back to host
cudaMemcpy(result, d result, (niters + 1) * k * sizeof(point t),
           cudaMemcpyDeviceToHost);
runtime all = get time() - runtime all;
// Free memory
cudaFree(d result);
cudaFree(d assignment);
cudaFree(d centroids);
cudaFree(d points);
```







Intuitive solution

```
global void k means(point t *points,
                       point t *centroids,
                       int *assignment) {
int iter, j;
int tid = blockDim.x * blockIdx.x + threadIdx.x;
for (iter = 0; iter < NO ITER; ++iter) {</pre>
  if (tid < n) {
    double optimal dist = DBL MAX;
    // Calculate Euclidean distance to each centroid and
       determine the closest mean
    for (j = 0; j < K; ++j) {
      double dist = (points[tid].x - centroids[j].x) *
                     (points[tid].x - centroids[j].x) +
                     (points[tid].y - centroids[j].y) *
                     (points[tid].y - centroids[j].y);
      if (dist < optimal dist) {</pre>
        optimal dist = dist;
        assignment[tid] = j;
  } } !
```







```
// Calculate new positions of centroids
    if (tid < k) {
      int count = 0;
      double sum x = 0.0;
                                                $ make run-small
      double sum y = 0.0;
                                                Executing k-means clustering with
      for (int j = 0; j < n; ++j) {
                                                20 iterations, 1000 points, and 5
        if (assignment[j] == tid) {
                                                centroids...
          sum x += points[j].x;
                                                Time Elapsed (kernel): 0.000492 s
          sum y += points[j].y;
                                                Time Elapsed (total): 0.000554 s
          count++;
      if (count != 0.0) {
        centroids[tid].x = sum x / count;
        centroids[tid].y = sum y / count;
      result[(iter + 1) * k + tid].x = centroids[tid].x;
      result[(iter + 1) * k + tid].y = centroids[tid].y;
} } }
```

Wrong results! Why?







Two separate kernels are required for synchronization

```
global void calc distances (point t *points, point t *centroids,
                              int *assignment, int n, int k) {
int tid = blockDim.x * blockIdx.x + threadIdx.x;
if (tid < n) {
  double optimal dist = DBL MAX;
  for (int j = 0; j < k; ++j) {
    double dist = (points[tid].x - centroids[j].x) *
                   (points[tid].x - centroids[j].x) +
                   (points[tid].y - centroids[j].y) *
                   (points[tid].y - centroids[j].y);
    if (dist < optimal dist) {</pre>
      optimal dist = dist;
      assignment[tid] = j;
} } }
```







```
global void update centroids (int iter, point t *points,
                                 point t *centroids, int *assignment,
                                 point t *result, int n, int k) {
int tid = blockDim.x * blockIdx.x + threadIdx.x;
if (tid < k) {
  int count = 0;
  double sum x = 0.0;
  double sum y = 0.0;
                                            $ make run-small
  for (int j = 0; j < n; ++j) {
                                            Executing k-means clustering with
    if (assignment[j] == tid) {
                                            20 iterations, 1000 points, and 5
                                            centroids
      sum x += points[j].x;
                                            Time Elapsed (kernel): 0.001543 s
      sum y += points[j].y;
                                            Time Elapsed (total): 0.001608 s
      count++;
  } }
  if (count != 0.0) {
    centroids[tid].x = sum x / count;
    centroids[tid].y = sum y / count;
  result[(iter + 1) * k + tid].x = centroids[tid].x;
  result[(iter + 1) * k + tid].y = centroids[tid].y;
```

Optimize and Parallelize K-means on the GPU





- Optimize the algorithm and parallelize it to utilize the GPU as much as possible. Keep the data layout optimizations from task 1.4 in mind.
 - → Similar findings here: optimal coalescing
 - → Distribute updating of centroids over multiple thread blocks to prevent serialization
 - → Updating of centroids could be done in shared memory

Deploy: Verify Results







Check you results for correctness and evaluate the actual performance with the performance estimated with your model in task 3.1. How close is you implementation to the theoretical peak performance for that hotspot based on the model? Evaluate reasons for potential differences between the modeled and the achieved performance.

→ Exact same results

Task 2.5 Deploy: Verify Results







Performance Comparison (large data set)

$$\rightarrow t_{data} = t_{total} - t_{kernel} = 0.607147 \text{ s} - 0.600960 \text{ s} \approx 6.2 \text{ ms}$$

$$ightarrow$$
 Modelled: $t_{data} = 2\alpha + \frac{2(n+k+k(no_iter+1))*8\ bytes + n*4\ bytes}{12\ GB/s} + \frac{2k(no_iter+1)*8\ bytes}{12\ GB/s} pprox$

$$\frac{2(1000000+5000+10000(50+1))*8\ bytes +1000000*4\ bytes}{12\frac{GB}{s}}\approx 2.35\ ms$$

- $\rightarrow t_{kernel} = 0.600960$
 - \rightarrow Modelled: $t_{kernel} = \max(t_{compute}, t_{memory})$

$$\rightarrow t_{compute} = \frac{arithmetic operations}{P_{max}} = \frac{50*1000000*5000*7 \text{ Flop}}{6656 \text{ GFlop/s}} = 262.9 \text{ ms}$$

$$ightharpoonup t_{memory} = rac{data\ transfers(LOAD,STORE)}{b_s} = rac{50*1000000*5000*4*8\ bytes}{865\ GB/s} = 9.25\ s$$

Lots of caching

Task 2.6 Performance Comparison







Compare your obtained performance results of the k-means algorithm on the GPU to the ones on the CPU (task 1.4). Is this problem suitable for the GPU (meaning: is the algorithm accelerated by the usage of the GPU)? Justify your answer.

$$\rightarrow t_{CPU} = 124.785246 \text{ s}$$

$$\rightarrow t_{GPU} = 0.607147 \text{ s}$$

- → Speedup of ~231
- Is this comparison fair? Justify your answer.
 - → No, use all 48 cores on CPU node

$$\rightarrow t_{CPU} = 4.007033 \text{ s} -> 436.7 \text{ Gflop/s} (P_{max,CPU} = 2150 \text{ Gflop/s})$$

$$\rightarrow t_{GPU} = 0.607147 \text{ s} -> 2882.3 \text{ Gflop/s} \ (P_{max,GPU} = 6656 \text{ Gflop/s})$$

→ Speedup of ~9