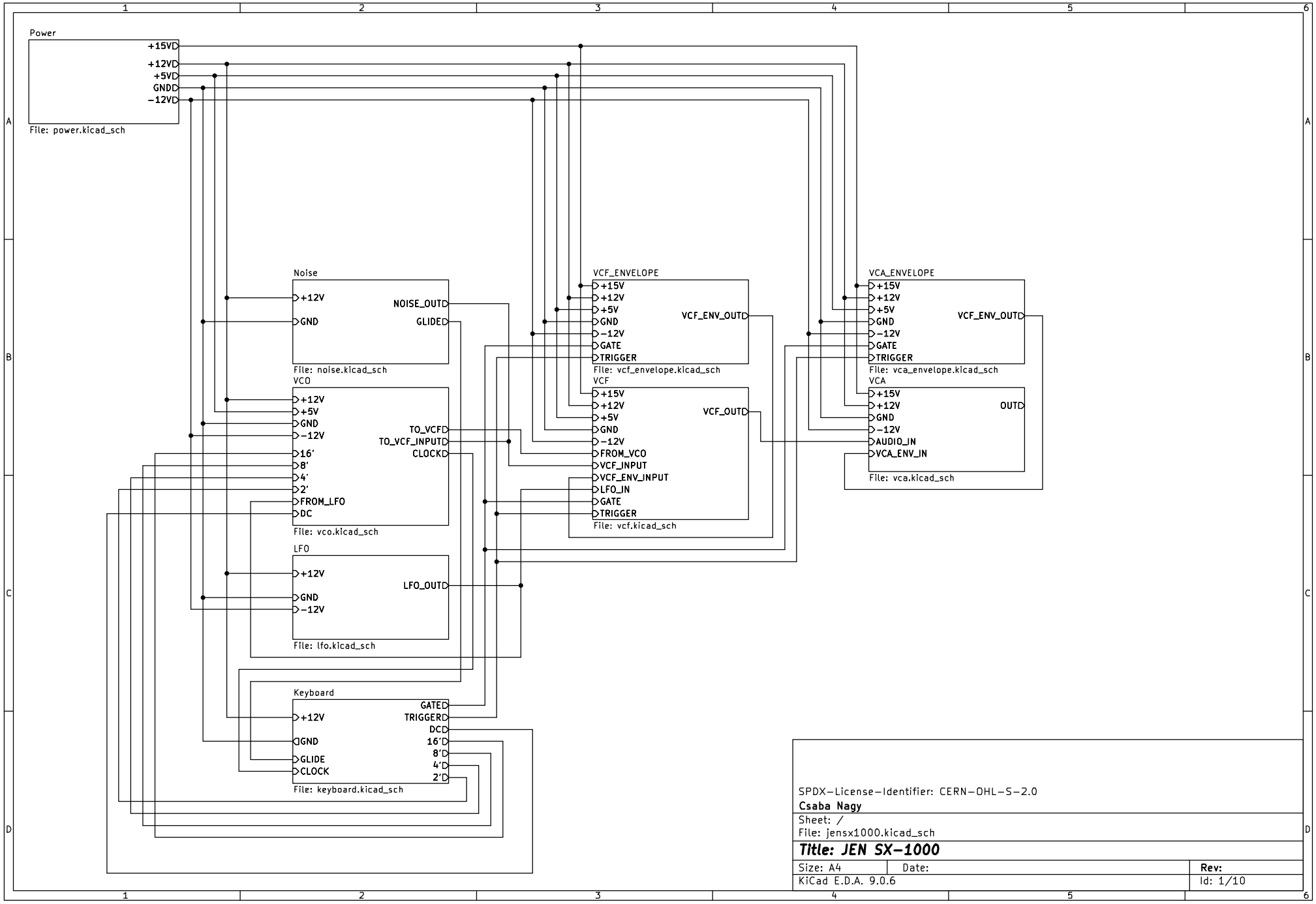
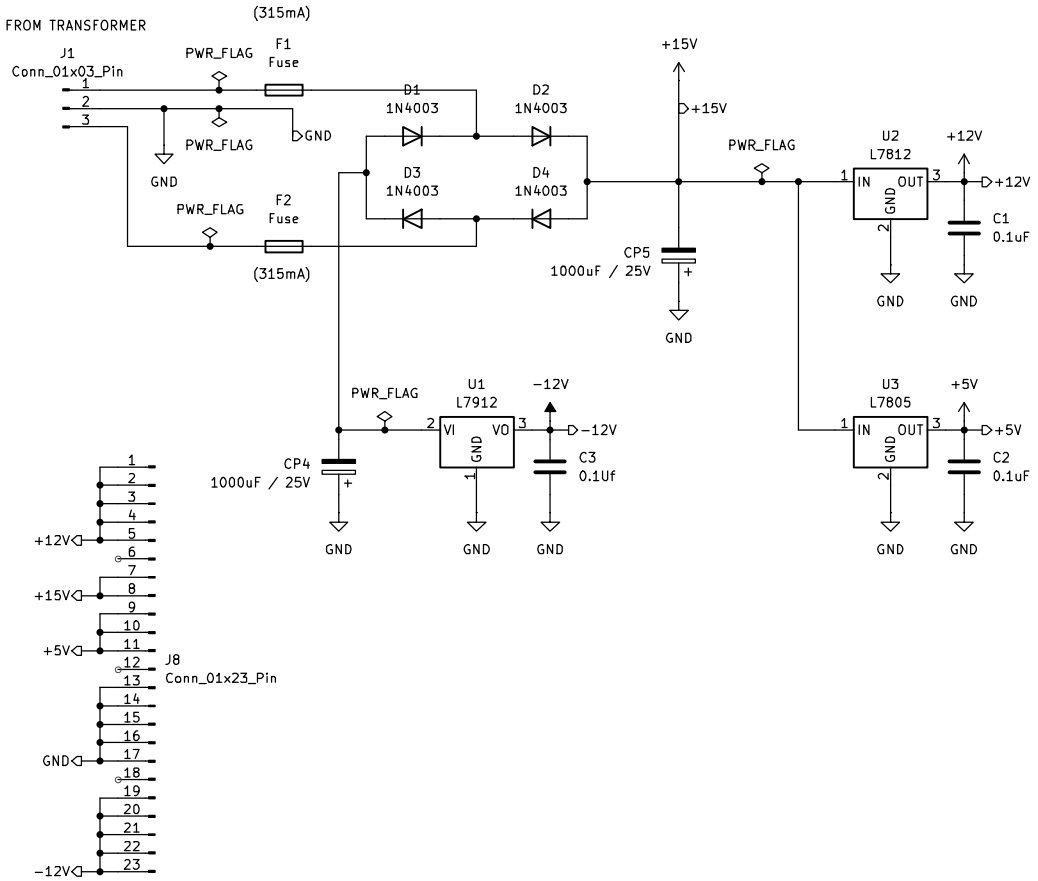

Power
+15V
+12V
+5V
GND
−12V
File: power.kicad_sch
Noise
+12V
GND
NOISE_OUT
GLIDE
File: noise.kicad_sch
VCO
+12V
+5V
GND
−12V
16'
8'
4'
2'
FROM_LFO
DC
TO_VCF
TO_VCF_INPUT
CLOCK
File: vco.kicad_sch
LFO
+12V
GND
−12V
LFO_OUT
File: lfo.kicad_sch
Keyboard
+12V
GND
GLIDE
CLOCK
GATE
TRIGGER
DC
16'
8'
4'
2'
File: keyboard.kicad_sch
VCF_ENVELOPE
+15V
+12V
+5V
GND
−12V
GATE
TRIGGER
VCF_ENV_OUT
File: vcf_envelope.kicad_sch
VCF
+15V
+12V
+5V
GND
−12V
FROM_VCO
VCF_INPUT
VCF_ENV_INPUT
LFO_IN
GATE
TRIGGER
VCF_OUT
File: vcf.kicad_sch
VCA_ENVELOPE
+15V
+12V
+5V
GND
−12V
GATE
TRIGGER
VCF_ENV_OUT
File: vca_envelope.kicad_sch
VCA
+15V
+12V
GND
−12V
AUDIO_IN
VCA_ENV_IN
OUT
File: vca.kicad_sch
SPDX-License-Identifier: CERN-OHL-S-2.0
Csaba Nagy
Sheet: /
File: jensx1000.kicad_sch
Title: JEN SX-1000
Size: A4
Date:
Rev:
KiCad E.D.A. 9.0.6
Id: 1/10



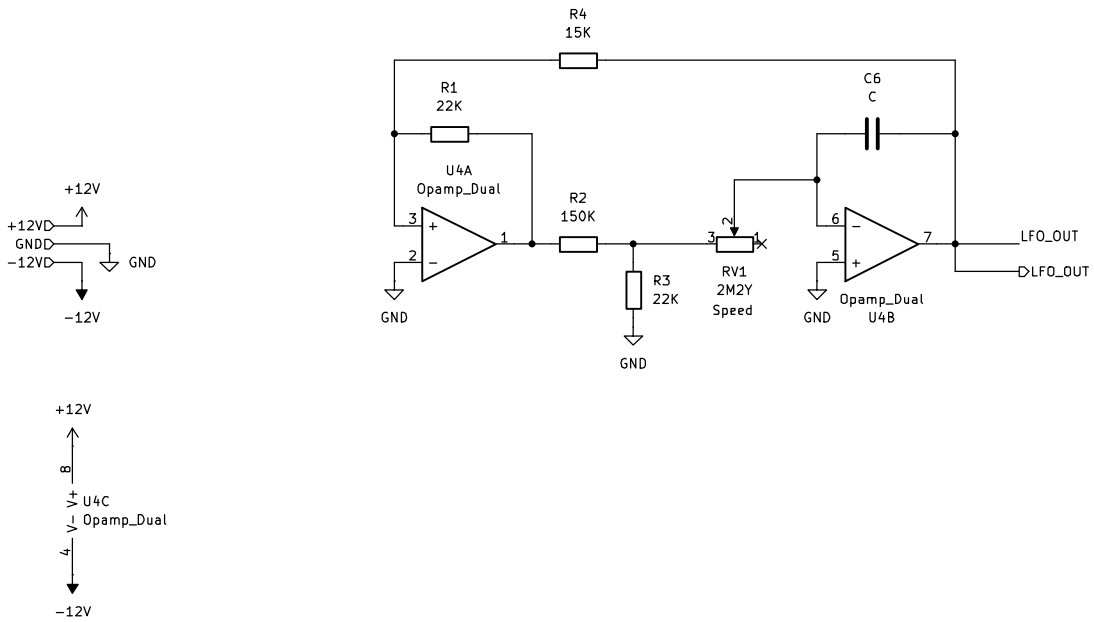
SPDX-License-Identifier: CERN-OHL-S-2.0

Csaba Nagy

Sheet: /Power/
File: power.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 2/10



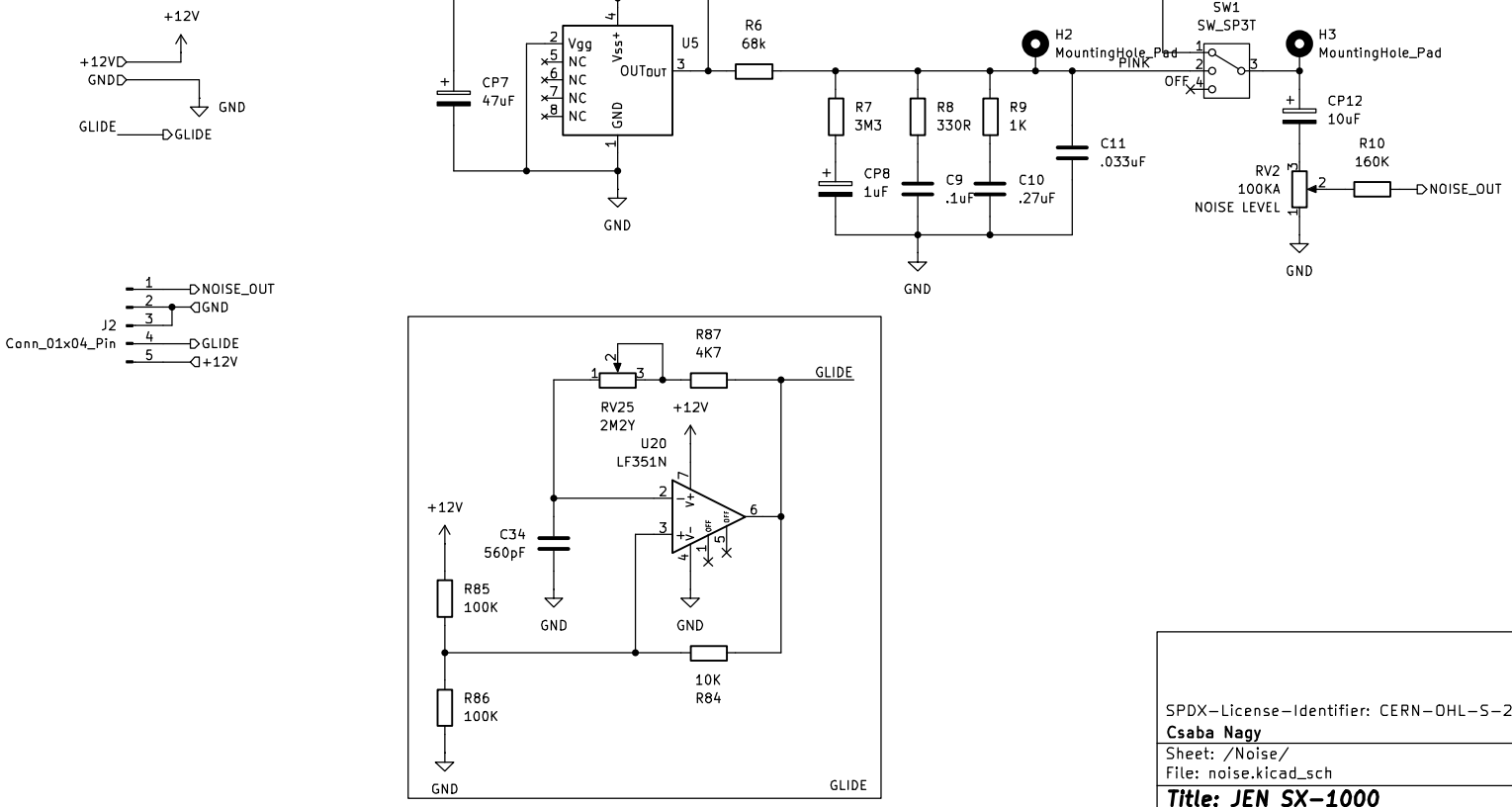
SPDX-License-Identifier: CERN-OHL-S-2.0

Csaba Nagy

Sheet: /LFO/
File: lfo.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 3/10



SPDX-License-Identifier: CERN-OHL-S-2.0

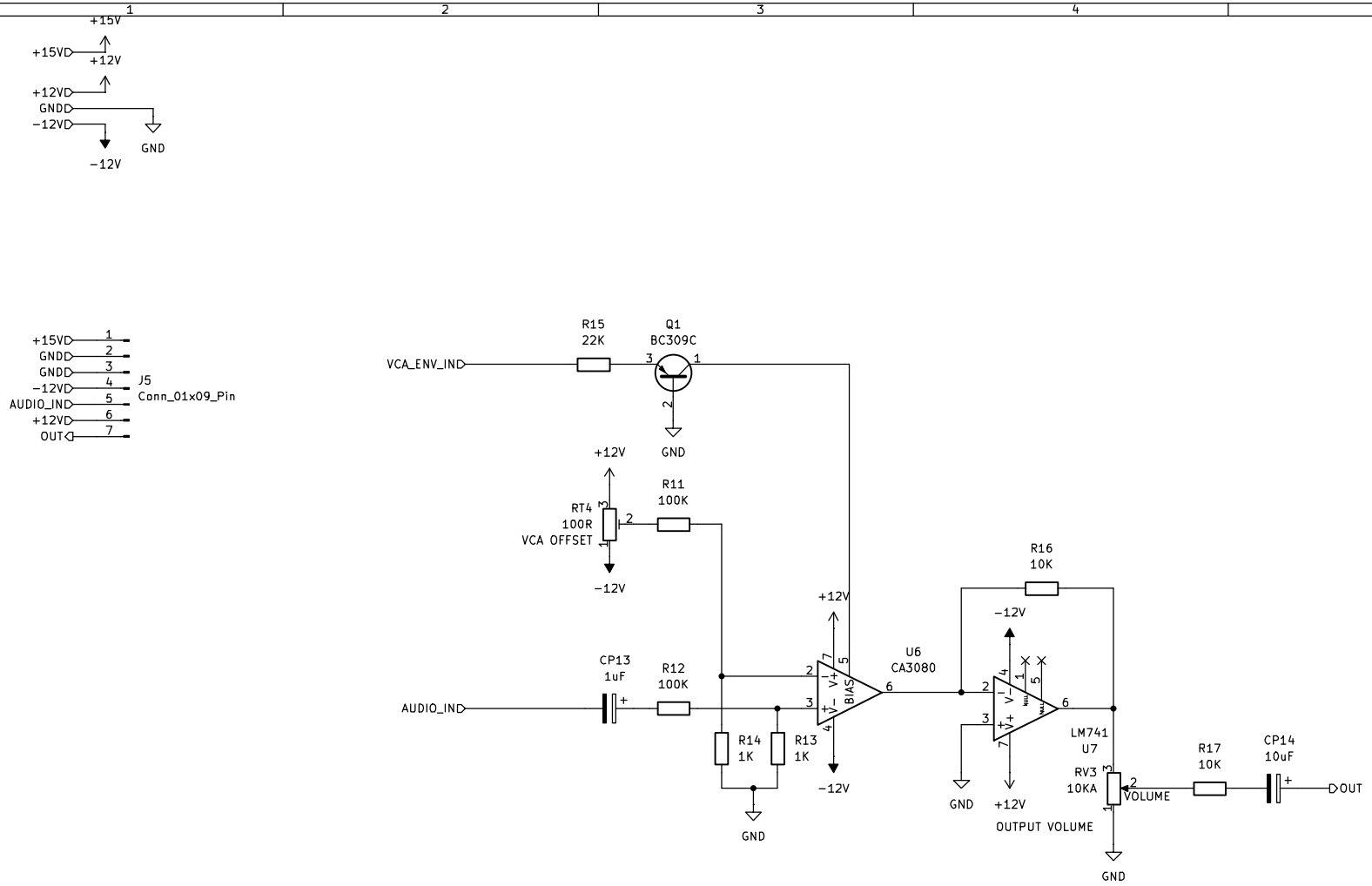
Csaba Nagy

Sheet: /Noise/

File: noise.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 4/10



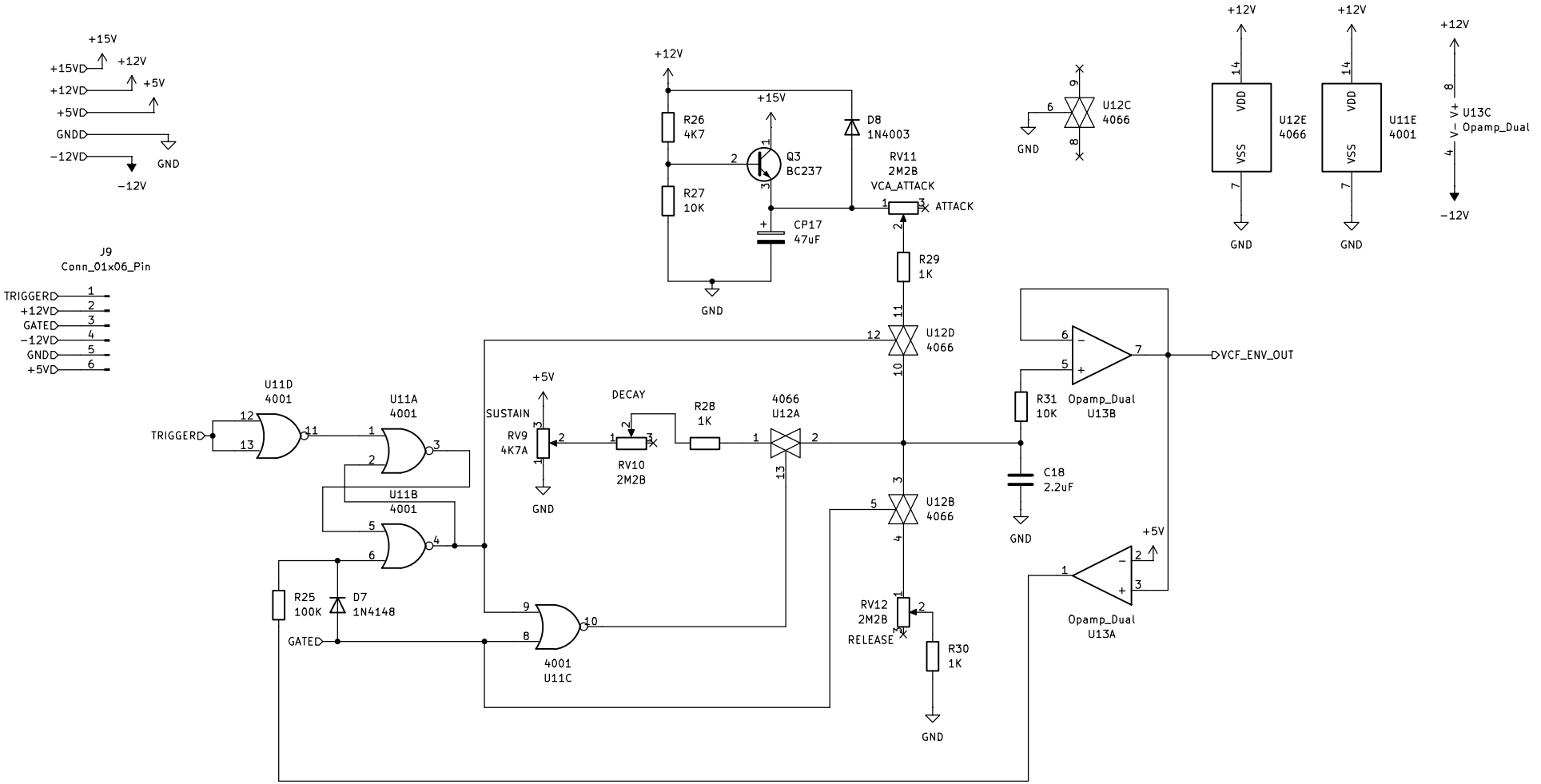
SPDX-License-Identifier: CERN-OHL-S-2.0

Csaba Nagy

Sheet: /VCA/
File: vca.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 5/10



SPDX-License-Identifier: CERN-OHL-S-2.0

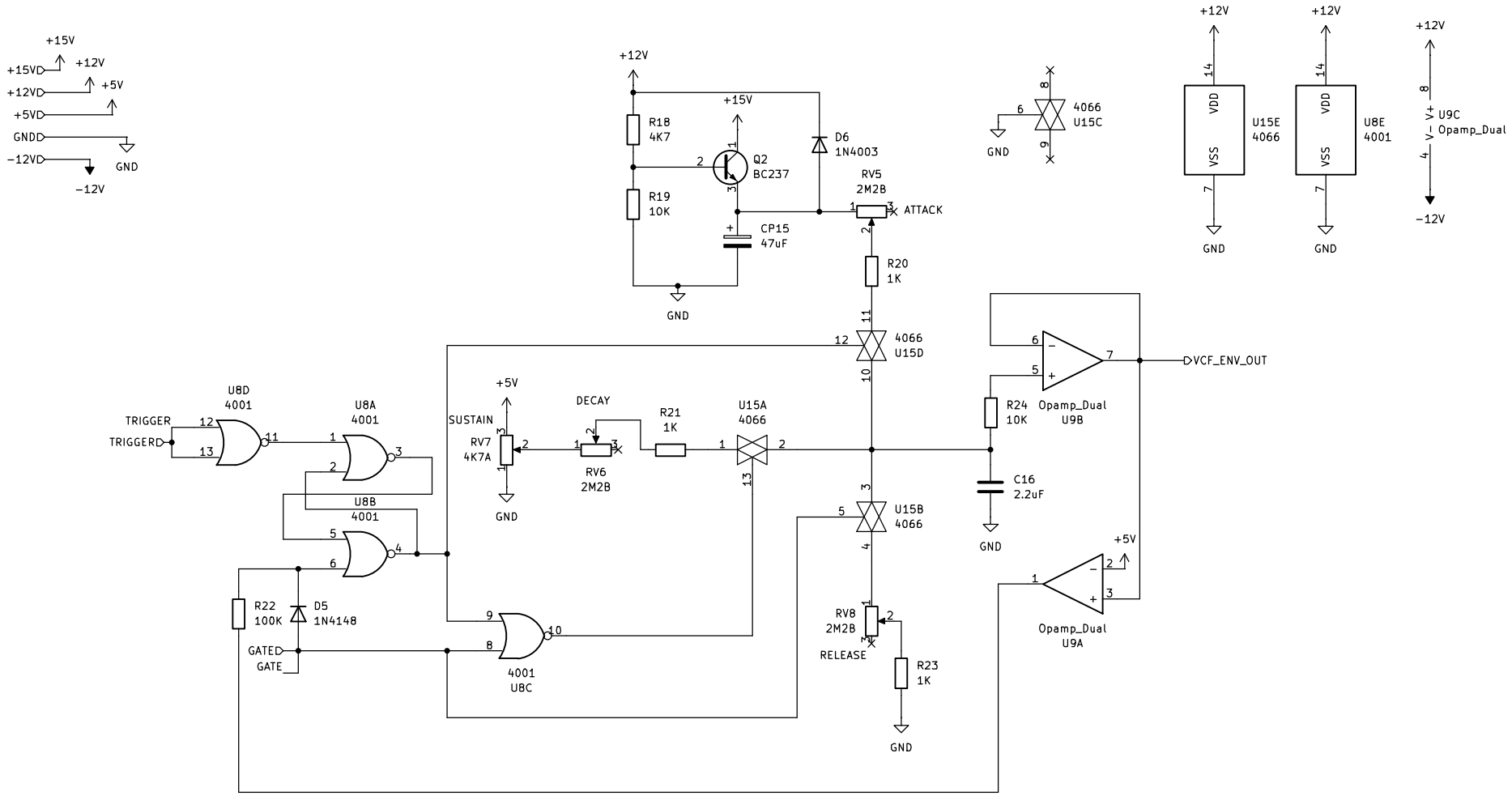
Csaba Nagy

Sheet: /VCA_ENVELOPE/

File: vca_envelope.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 7/10



SPDX-License-Identifier: CERN-OHL-S-2.0

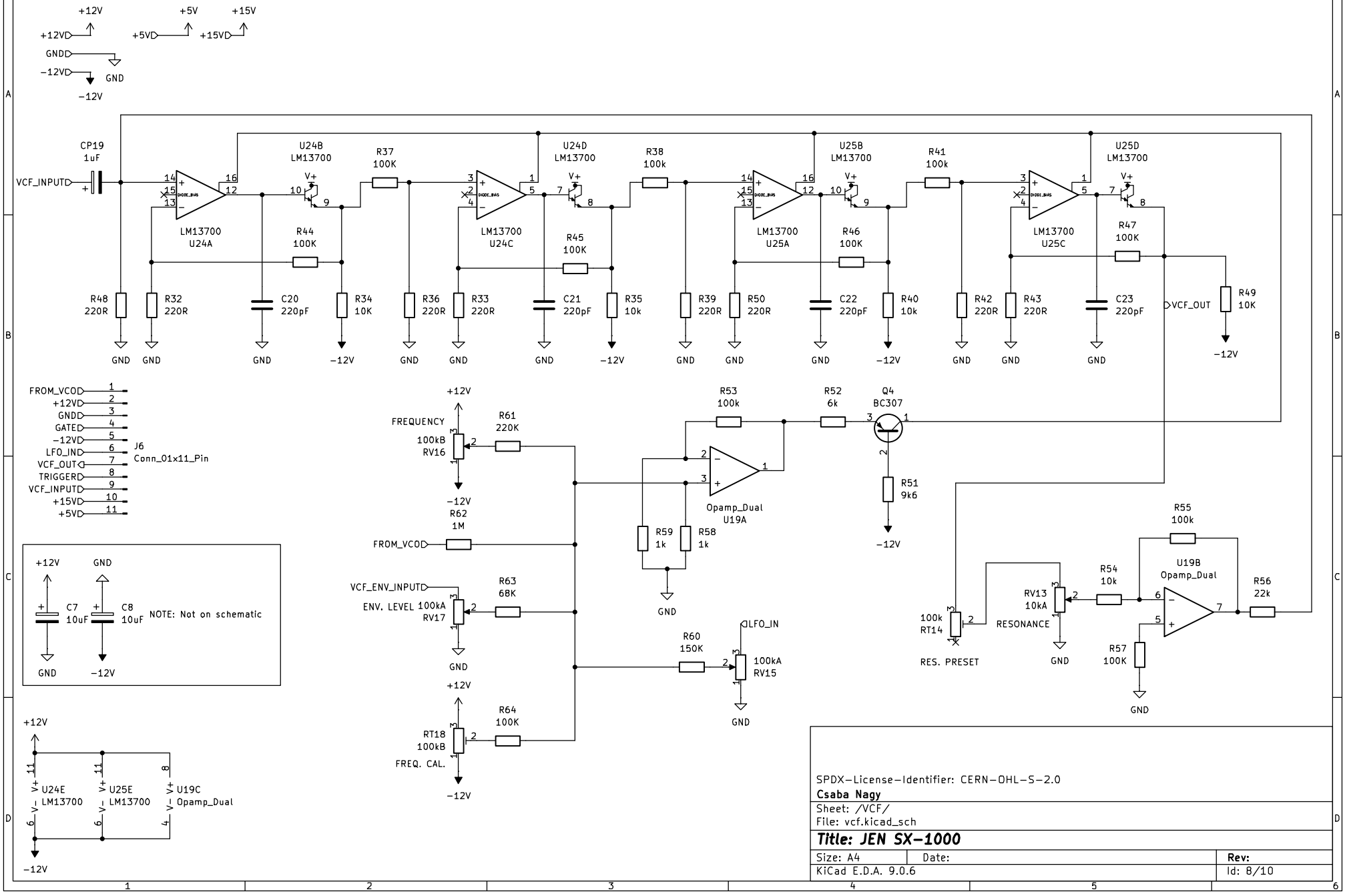
Csaba Nagy

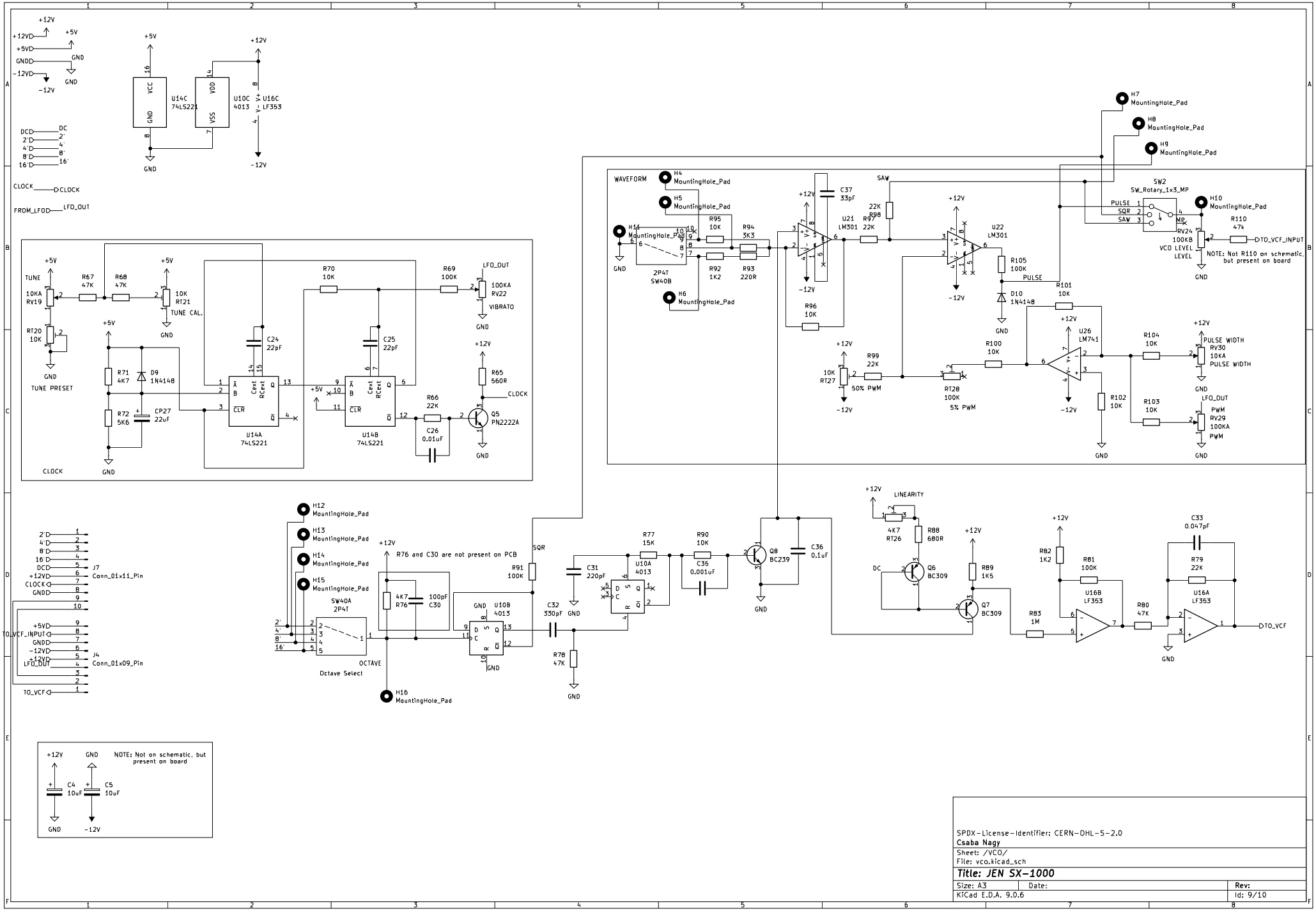
Sheet: /VCF_ENVELOPE/

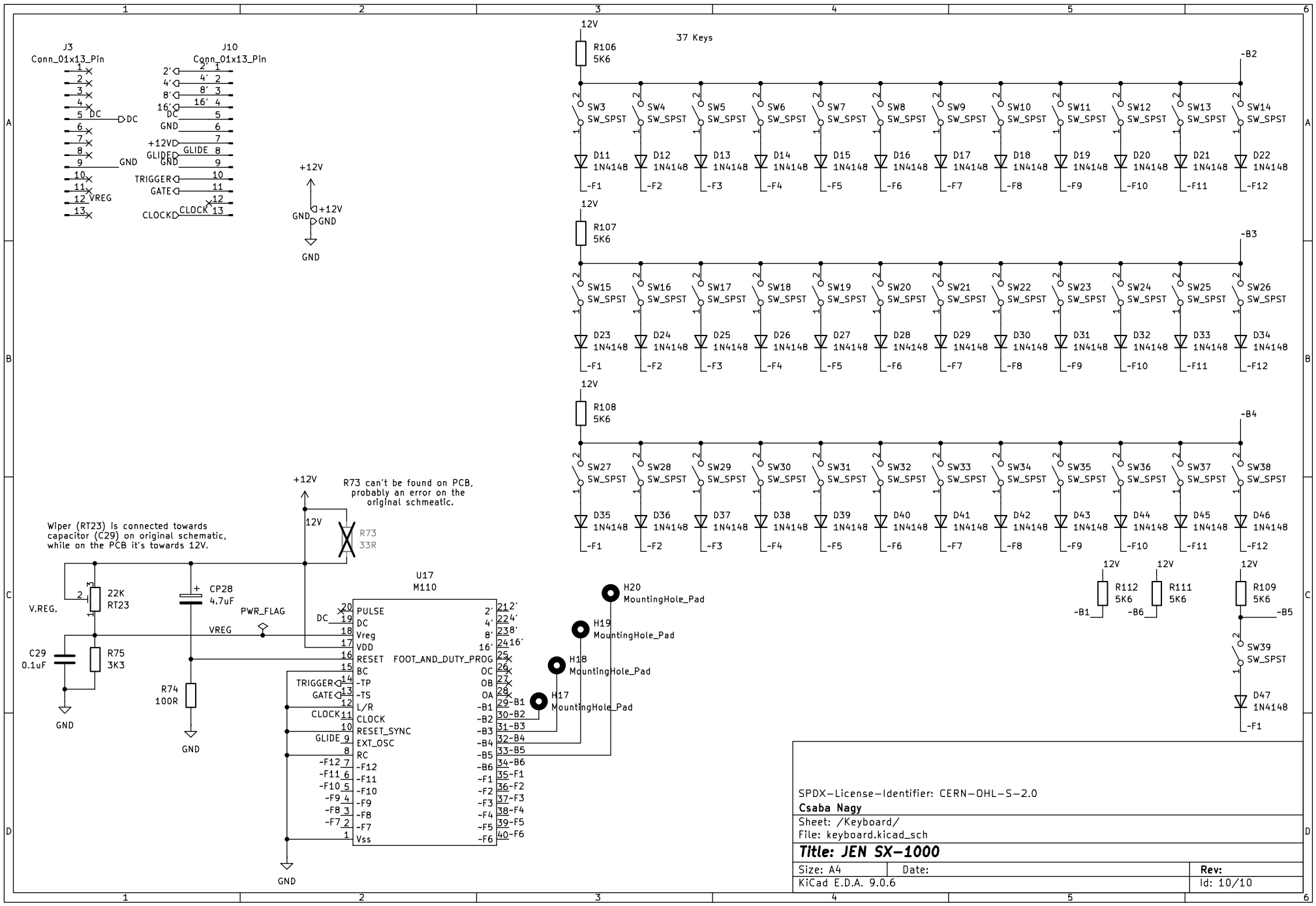
File: vcf_envelope.kicad_sch

Title: JEN SX-1000

Size: A4	Date:	**Rev:**
KiCad E.D.A. 9.0.6		Id: 8/10


+12V
+5V
+15V
GND
−12V
CP19
1uF
VCF_INPUT
U24B
LM13700
U24A
U24C
U24D
U25A
U25B
U25C
U25D
R37
100K
R38
100k
R41
100k
R44
100K
R45
100K
R46
100K
R47
100K
R48
220R
R32
220R
C20
220pF
R34
10K
R36
220R
R33
220R
C21
220pF
R35
10k
R39
220R
R50
220R
C22
220pF
R40
10k
R42
220R
R43
220R
C23
220pF
VCF_OUT
R49
10K
FROM_VCO
GATE
LFO_IND
TRIGGER
VCF_INPUT
J6
Conn_01x11_Pin
C7
10uF
C8
10uF
NOTE: Not on schematic
FREQUENCY
100kB
RV16
R61
220K
R62
1M
VCF_ENV_INPUT
ENV. LEVEL 100kA
RV17
R63
68K
R64
100K
RT18
100kB
FREQ. CAL.
R53
100k
R52
6k
Q4
BC307
R51
9k6
Opamp_Dual
U19A
R59
1k
R58
1k
R60
150K
LFO_IN
100kA
RV15
R55
100k
R54
10k
U19B
Opamp_Dual
R56
22k
RV13
10kA
RESONANCE
100k
RT14
RES. PRESET
R57
100K
U24E
LM13700
U25E
LM13700
U19C
Opamp_Dual
SPDX-License-Identifier: CERN-OHL-S-2.0
Csaba Nagy
Sheet: /VCF/
File: vcf.kicad_sch
Title: JEN SX-1000
Size: A4
Date:
Rev:
KiCad E.D.A. 9.0.6
Id: 8/10


+12V
+5V
GND
-12V
U14C
74LS221
U10C
4013
U16C
LF353
DC
CLOCK
FROM_LFOD
LFO_OUT
TUNE
TUNE PRESET
TUNE CAL.
VIBRATO
CLOCK
U14A
74LS221
U14B
74LS221
WAVEFORM
SAW
PULSE
SW2
SW_Rotary_1x3_MP
VCO LEVEL
NOTE: Not R110 on schematic, but present on board
LINEARITY
50% PWM
5% PWM
PULSE WIDTH
PWM
R76 and C30 are not present on PCB
SW40A
2P4T
OCTAVE
Octave Select
SW40B
TO_VCF
J7
Conn_01x11_Pin
J4
Conn_01x09_Pin
NOTE: Not on schematic, but present on board
SPDX-License-Identifier: CERN-OHL-S-2.0
Csaba Nagy
Sheet: /VCO/
File: vco.kicad_sch
Title: JEN SX-1000
Size: A3
Date:
Rev:
KiCad E.D.A. 9.0.6
Id: 9/10


J3
Conn_01x13_Pin
J10
Conn_01x13_Pin
DC
GND
VREG
GLIDE
TRIGGER
GATE
CLOCK
+12V
37 Keys
R106
5K6
R107
5K6
R108
5K6
R109
5K6
R111
5K6
R112
5K6
SW3 SW_SPST
SW4 SW_SPST
SW5 SW_SPST
SW6 SW_SPST
SW7 SW_SPST
SW8 SW_SPST
SW9 SW_SPST
SW10 SW_SPST
SW11 SW_SPST
SW12 SW_SPST
SW13 SW_SPST
SW14 SW_SPST
SW15 SW_SPST
SW16 SW_SPST
SW17 SW_SPST
SW18 SW_SPST
SW19 SW_SPST
SW20 SW_SPST
SW21 SW_SPST
SW22 SW_SPST
SW23 SW_SPST
SW24 SW_SPST
SW25 SW_SPST
SW26 SW_SPST
SW27 SW_SPST
SW28 SW_SPST
SW29 SW_SPST
SW30 SW_SPST
SW31 SW_SPST
SW32 SW_SPST
SW33 SW_SPST
SW34 SW_SPST
SW35 SW_SPST
SW36 SW_SPST
SW37 SW_SPST
SW38 SW_SPST
SW39 SW_SPST
D11 1N4148
D12 1N4148
D13 1N4148
D14 1N4148
D15 1N4148
D16 1N4148
D17 1N4148
D18 1N4148
D19 1N4148
D20 1N4148
D21 1N4148
D22 1N4148
D23 1N4148
D24 1N4148
D25 1N4148
D26 1N4148
D27 1N4148
D28 1N4148
D29 1N4148
D30 1N4148
D31 1N4148
D32 1N4148
D33 1N4148
D34 1N4148
D35 1N4148
D36 1N4148
D37 1N4148
D38 1N4148
D39 1N4148
D40 1N4148
D41 1N4148
D42 1N4148
D43 1N4148
D44 1N4148
D45 1N4148
D46 1N4148
D47 1N4148
~B1
~B2
~B3
~B4
~B5
~B6
~F1
~F2
~F3
~F4
~F5
~F6
~F7
~F8
~F9
~F10
~F11
~F12
R73 can't be found on PCB, probably an error on the original schmeatic.
R73
33R
Wiper (RT23) is connected towards capacitor (C29) on original schematic, while on the PCB it's towards 12V.
V.REG.
22K
RT23
CP28
4.7uF
PWR_FLAG
VREG
C29
0.1uF
R75
3K3
R74
100R
U17
M110
PULSE
DC
Vreg
VDD
RESET
BC
~TP
~TS
L/R
CLOCK
RESET_SYNC
EXT_OSC
RC
~F12
~F11
~F10
~F9
~F8
~F7
Vss
FOOT_AND_DUTY_PROG
OC
OB
OA
H17 MountingHole_Pad
H18 MountingHole_Pad
H19 MountingHole_Pad
H20 MountingHole_Pad
SPDX-License-Identifier: CERN-OHL-S-2.0
Csaba Nagy
Sheet: /Keyboard/
File: keyboard.kicad_sch
Title: JEN SX-1000
Size: A4
Date:
Rev:
KiCad E.D.A. 9.0.6
Id: 10/10