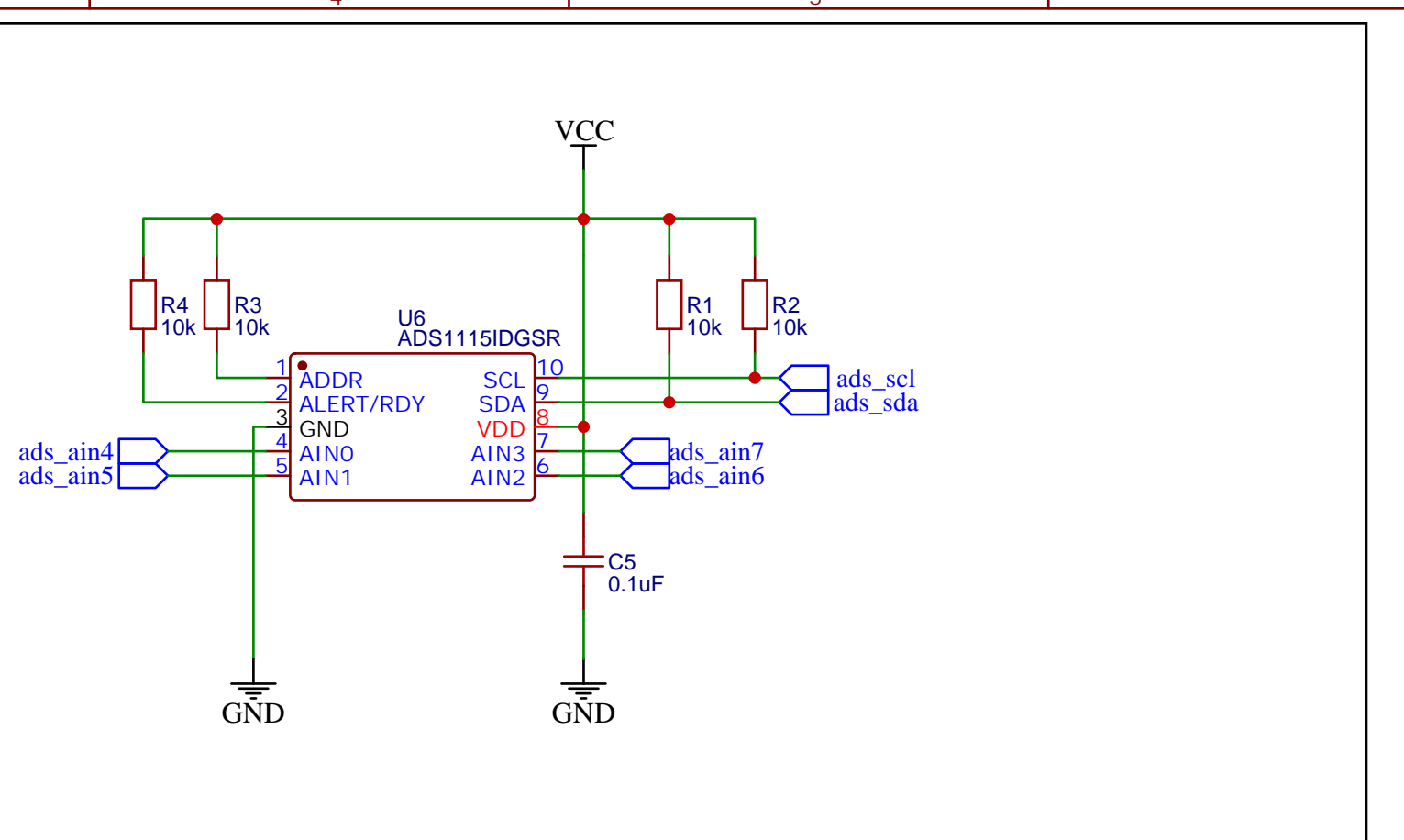
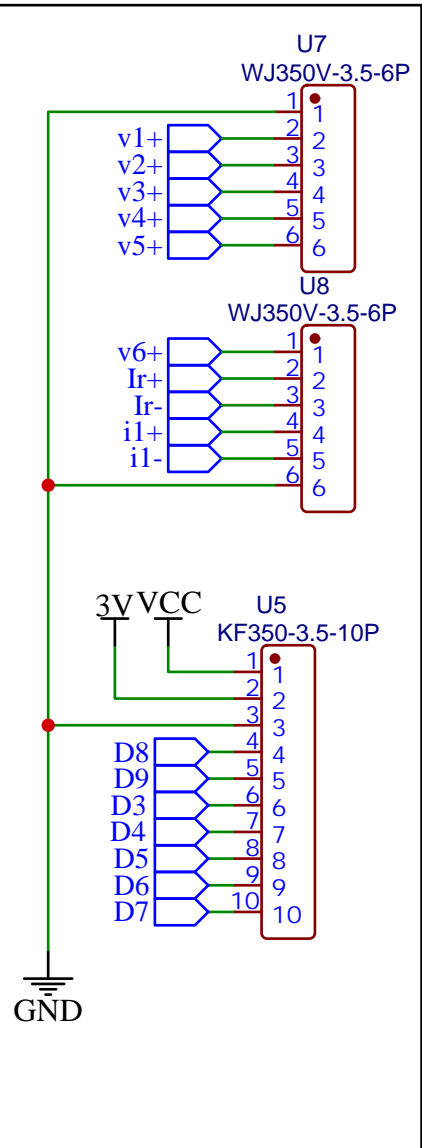


# External ADCs

The image displays two circuit diagrams for external ADCs (ADS1115) connected to a system. Both diagrams show the ADC chip (U4 and U6) connected to VCC and GND, with various pins (ADDR, ALERT/RDY, SCL, SDA, AIN0, AIN1, AIN2, AIN3) connected to the system. The left diagram shows U4 (ADS1115IDGSR) connected to VCC, GND, and various pins (ADDR, ALERT/RDY, SCL, SDA, AIN0, AIN1, AIN2, AIN3). The right diagram shows U6 (ADS1115IDGSR) connected to VCC, GND, and various pins (ADDR, ALERT/RDY, SCL, SDA, AIN0, AIN1, AIN2, AIN3). Both diagrams include resistors (R1, R2, R3, R4, R6, R12, R13, R14) and a capacitor (C2, C5) connected to GND.

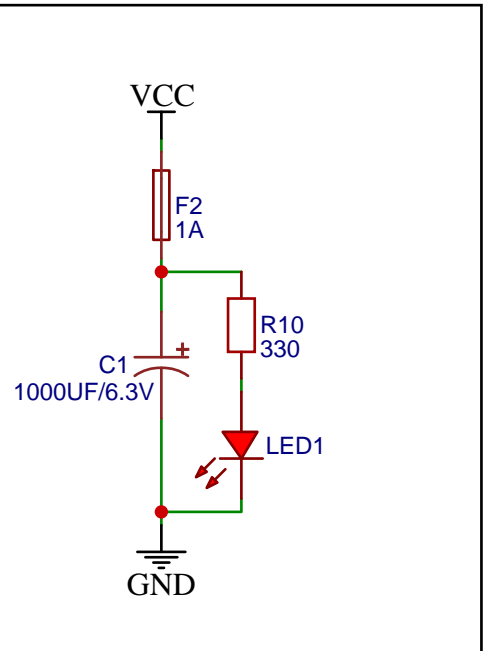


# Shield pinout

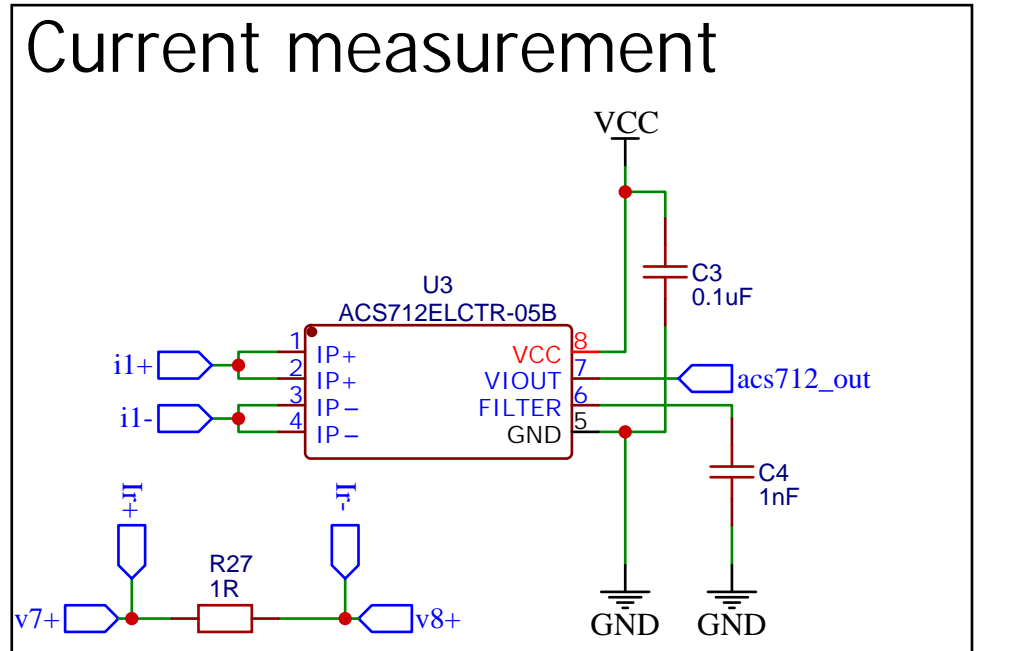


# Power

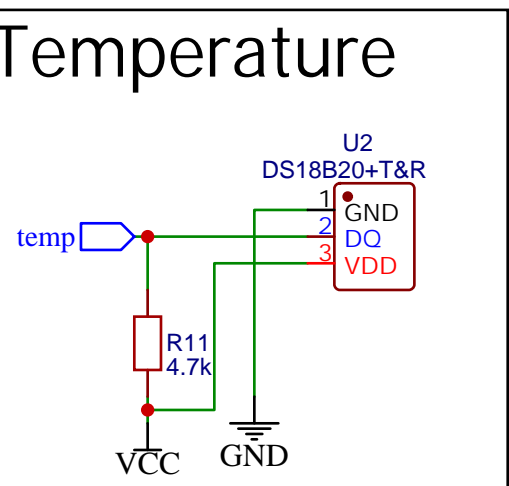
The diagram shows a power supply circuit for an LED. It starts with a VCC input at the top, connected to a fuse labeled F2 (1A). Following the fuse, the circuit splits into two parallel branches. The first branch contains a capacitor labeled C1 (1000UF/6.3V) connected to ground (GND). The second branch contains a resistor labeled R10 (330) in series with an LED labeled LED1, which is also connected to GND. The output of the parallel combination is the power supply for the LED.



# Current measurement

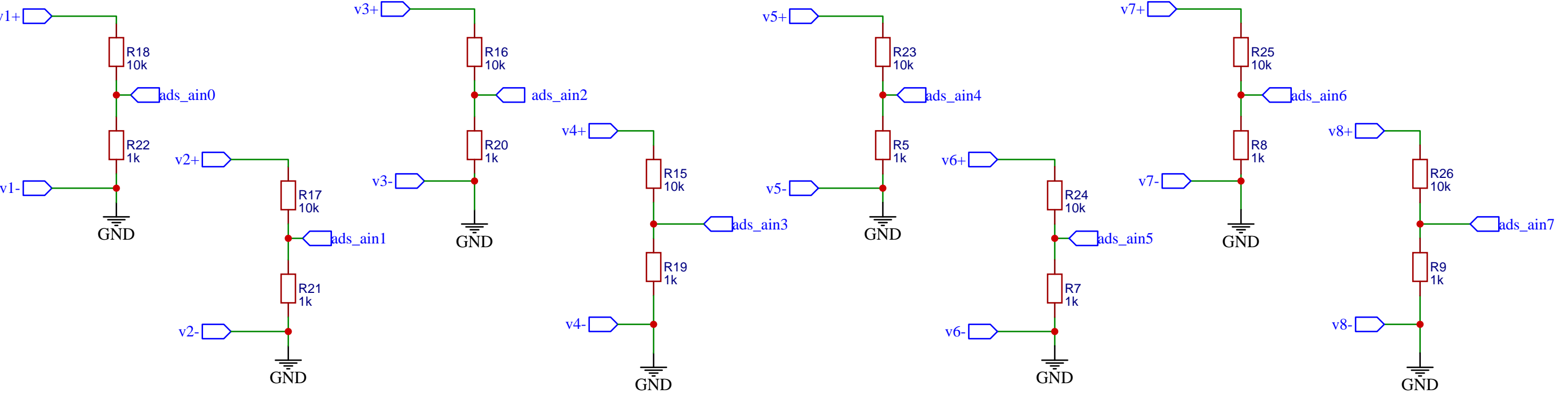


# Temperature



TITLE: <b>ArduDAQ</b>		REV: <b>v2</b>
	Company: <a href="https://github.com/icrnjavic/ArduDAQ">https://github.com/icrnjavic/ArduDAQ</a>	Sheet: <b>1/2</b>
	Date: <b>2025-08-27</b> Drawn By: <b>icrnjavic</b>	

# ADC dividers



TITLE: ArduDAQ		REV: v2
	Company: <a href="https://github.com/icrnjavic/ArduDAQ">https://github.com/icrnjavic/ArduDAQ</a>	Sheet: 2/2
	Date: 2025-08-27 Drawn By: icrnjavic	