

Lab 5: Bandpass filter using OP-AMP

Circuit Theory and Electronics Fundamentals
Master's programme in Engineering Physics, Técnico, University of Lisbon

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1 Introduction

The main objective of this laboratory assignment is to design a bandpass filter, using an OP-AMP. This filter should be centered at 1 kHz and produce a gain of 40 dB at this frequency. There are also some limitations in terms of resources: maximum 3 $1\text{ k}\Omega$ resistors, 3 $10\text{ k}\Omega$ resistors, 3 $100\text{ k}\Omega$ resistors, 3 220 nF capacitors and 3 $1\text{ }\mu\text{F}$ capacitors.

The secondary goal is to achieve the best performance at the lowest cost. This is quantified by maximizing the following function:

$$M = \frac{1}{\text{cost}(\cdot \text{gainDeviation} \cdot \text{centralFrequencyDeviation} + 10^{-6})} \quad (1)$$

$$\text{cost} = \text{cost of resistors} + \text{cost of capacitors} + \text{cost of transistors}$$

$$\text{cost of resistors} = 1\text{MU}/\text{k}\Omega$$

$$\text{cost of capacitors} = 1\text{MU}/\mu\text{F}$$

$$\text{cost of transistors} = 0.1\text{MU}/\text{transistor}$$

In Section 2 a basic filter circuit is introduced and the study performed in the laboratory, using this circuit, is presented. This is followed by Section 3, where the same circuit is simulated. Furthermore, in an effort to improve our results, an optimized circuit is obtained and presented. A theoretical analysis is then performed, considering either circuit, in Section 4. The results obtained for the optimized circuit via simulation and theoretical analysis are compared in Section 5. Here, M is presented, allowing for some considerations to be made about the studies performed. The simulation was run on **Ngspice** and the theoretical calculations were performed with **Octave**.

2 Real circuit

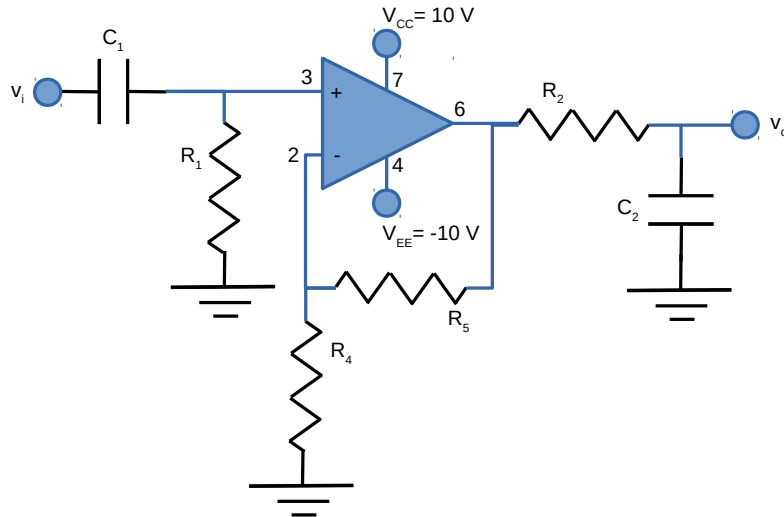


Figure 1: Bandpass filter

For the purpose of trying out this circuit, we chose the following parameters:

Parameter	Value
$C_1(nF)$	220
$R_1(k\Omega)$	1
$C_2(nF)$	220
$R_2(k\Omega)$	0.5
$R_4(k\Omega)$	1
$R_5(k\Omega)$	100

Table 1: Parameters for the simple circuit

By varying the frequency using a signal generator and measuring the peak to peak amplitudes of v_i and v_o , the gain as a function of the frequency could be determined. Below is a table which includes the data we retrieved (f , $2|v_1|$ and $2|v_2|$) and the calculated gain, in dB .

f (Hz)	$2 v_1 $ (V)	$2 v_2 $ (V)	$(\frac{v_2}{v_1})_{dB}$
500	0.105	5.5	34.28
600	0.105	5.9	34.99
700	0.105	6.2	35.42
800	0.105	6.2	35.42
900	0.105	6.2	35.42
1000	0.105	6.2	35.42
1100	0.105	6	35.14
1200	0.104	5.9	35.08
1300	0.104	5.7	34.78
1400	0.104	5.55	34.55
1500	0.104	5.4	34.31
2000	0.104	4.6	32.91
2500	0.104	3.9	31.48
3000	0.104	3.35	30.16
3500	0.104	2.9	28.91
4000	0.104	2.6	27.96
4500	0.104	2.3	26.89
5000	0.104	2	25.68
5500	0.104	1.8	24.76
6000	0.104	1.6	23.74
10000	0.103	1	19.74

Table 2: Data collected

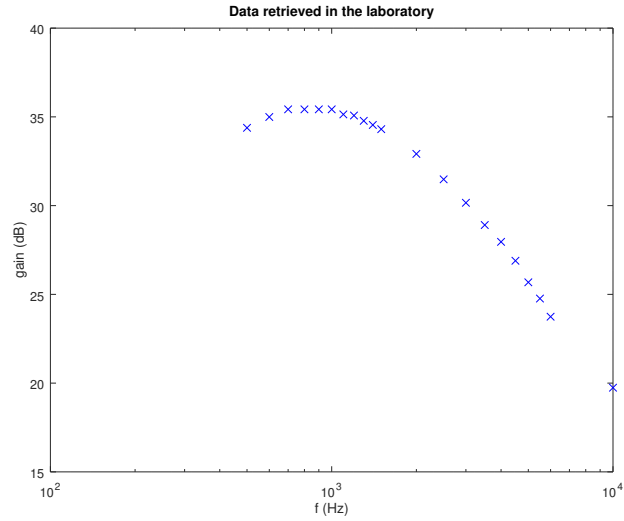


Figure 2: Data collected

3 Simulation Analysis

3.1 Simple circuit

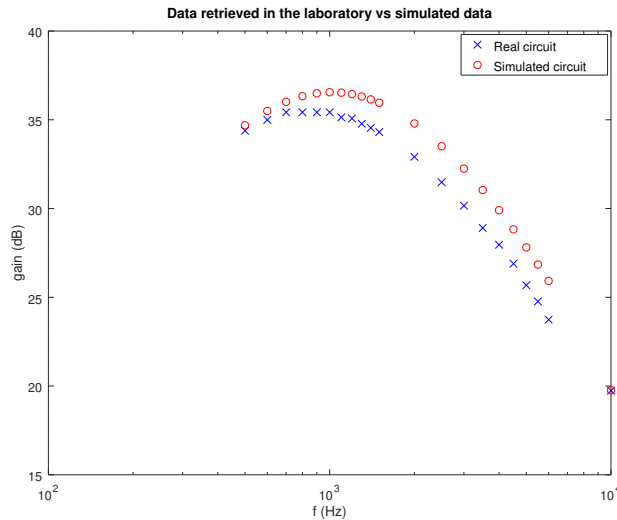


Figure 3: Data collected versus simulated data

It is obvious, from the above graph, that there are slight deviations between the real data and the simulated data, as expected. This can be due to any number of experimental causes:

- Errors in the peak to peak amplitude determined by the oscilloscope
- Errors in the resistors used in the lab (had gold tolerance)
- Errors in the capacitors used in the lab
- For the $500\ \Omega$ resistor, two $1\ k\Omega$ resistors were employed, in parallel
- All signals went through the breadboard
- V_{CC} was not exactly $10\ V$ and V_{EE} was not exactly $-10\ V$
- Discrepancies in the **Ngspice** models

Using these parameters, the following was obtained:

Name	Value
$f_c\ (Hz)$	1004.3987056244149
$gain(1000\ Hz)\ (dB)$	36.55104
z_{in}	$999.9928 + i\ (-723.536)$
z_{out}	$341.692 + i\ (-233.043)$

Table 3: Results

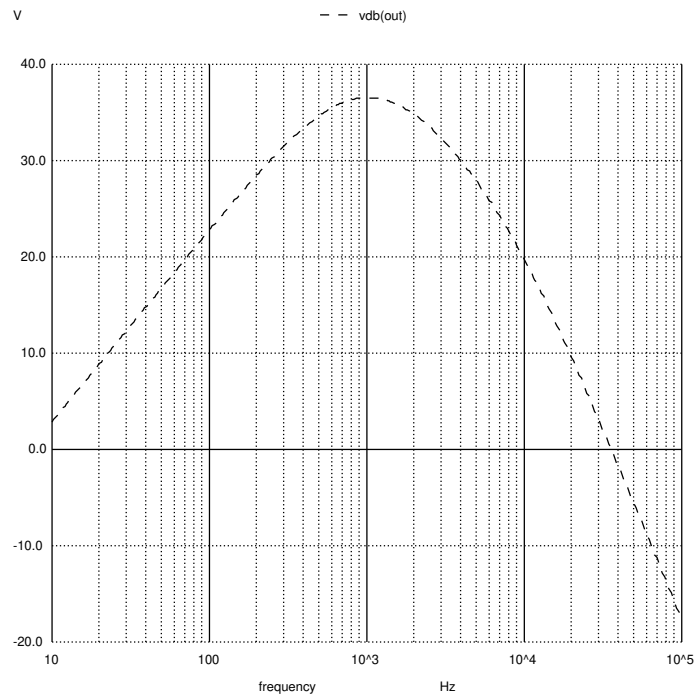


Figure 4: Frequency response - gain

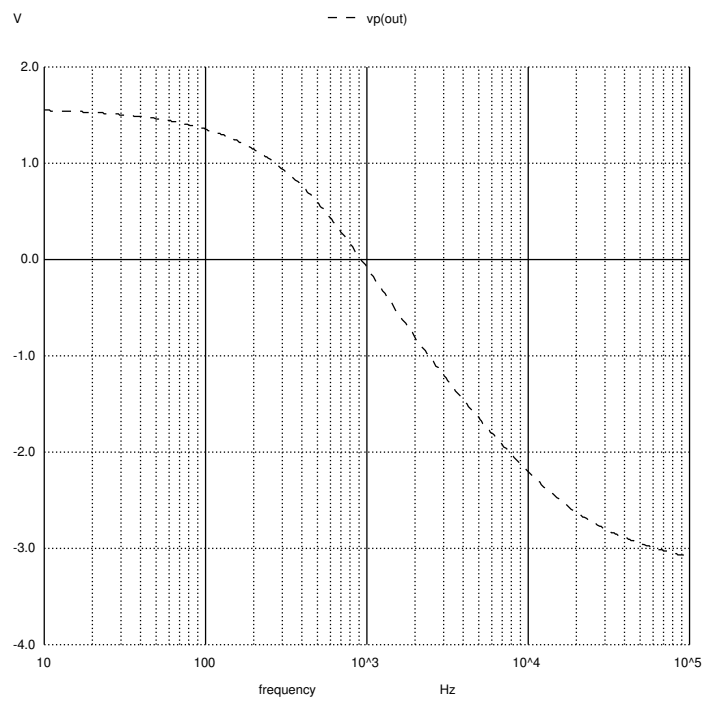


Figure 5: Frequency response - phase

3.2 Optimized circuit

By a lengthy process of trial and error, attempting to maximize the merit figure, while using only the components available, the following parameters were obtained:

Parameter	Value
$C_1(nF)$	220
$R_1(k\Omega)$	1
$C_2(nF)$	110 (two 220 in series)
$R_2(k\Omega)$	1
$R_4(k\Omega)$	1
$R_5(k\Omega)$	105 (one 100 in series with two 10 in parallel)

Table 4: Parameters for the optimized circuit

Name	Value
$f_c (Hz)$	1000.8163441704977
$gain(1000 Hz) (dB)$	36.94817
z_{in}	999.9899 + i (-723.539)
z_{out}	680.2165 + i (-466.878)

Table 5: Results

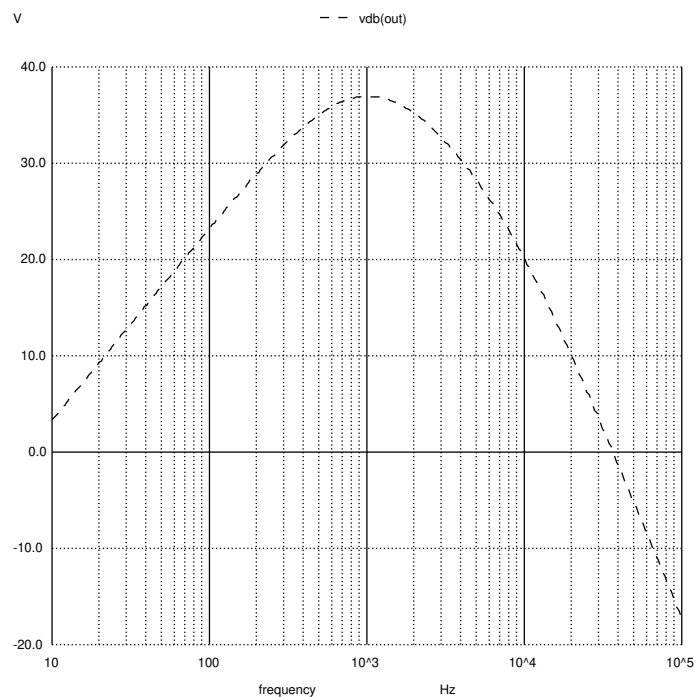


Figure 6: Frequency response - gain

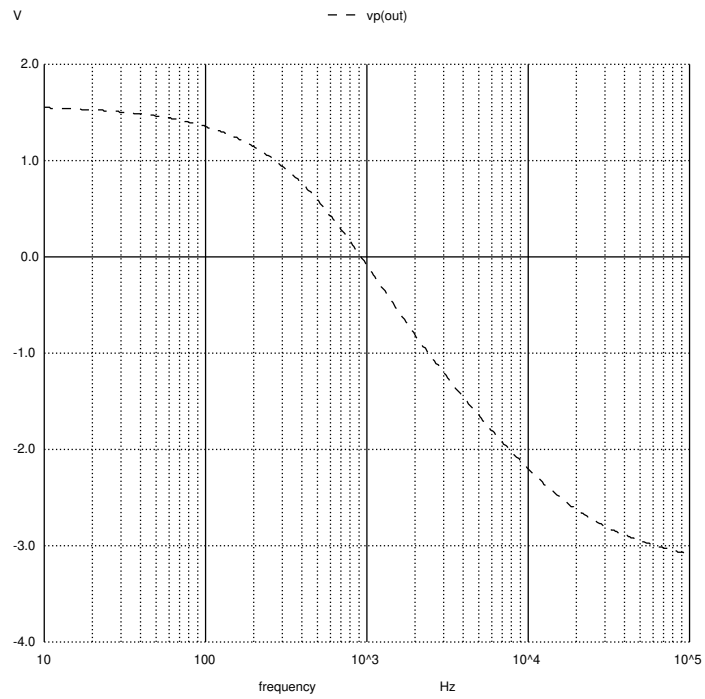


Figure 7: Frequency response - phase

4 Theoretical Analysis

4.1 Simple circuit

First, the real circuit was analysed. As already mentioned, to perform the theoretical analysis **Octave** was used. This analysis was conducted considering a sine wave of amplitude 1 and phase 0 as the input. In order to compute the gain, the input impedance and the output impedance, the circuit was analysed using phasors.

It is possible to observe that the OP-AMP is inserted in a non-inverting amplifier with resistive feedback loop. Considering the ideal model for the OP-AMP, the voltage in node 3 must be equal to voltage in node 2. Also, voltage in node 2 must be:

$$\widetilde{V}_2 = \frac{R_4}{R_4 + R_5} \widetilde{V}_6 \iff \widetilde{V}_6 = (1 + \frac{R_5}{R_4}) \widetilde{V}_2 \quad (2)$$

An ideal OP-AMP has infinite input impedance, so no current goes in the entries of the OP-AMP. Considering this, we can perform nodal analysis and, for node 3, we obtain:

$$(\widetilde{V}_3 - \widetilde{V}_i)j\omega C_1 + \frac{\widetilde{V}_3}{R_1} = 0 \iff \widetilde{V}_3 = \frac{j\omega C_1 R_1 \widetilde{V}_i}{j\omega C_1 R_1 + 1} \quad (3)$$

For the node of the output voltage, we get:

$$\frac{\widetilde{V}_o - \widetilde{V}_6}{R_2} + \widetilde{V}_o j\omega C_2 = 0 \iff \widetilde{V}_o = \frac{\widetilde{V}_6}{1 + j\omega C_2 R_2} \quad (4)$$

The transfer function is then:

$$\frac{\widetilde{V}_o}{\widetilde{V}_i} = (1 + \frac{R_5}{R_4}) \frac{j\omega C_1 R_1}{j\omega C_1 R_1 + 1} \frac{1}{1 + j\omega C_2 R_2} \quad (5)$$

The central frequency will then be calculated as:

$$w_c = \sqrt{w_L w_H} \quad (6)$$

where $w_L = \frac{1}{R_1 C_1}$ and $w_H = \frac{1}{R_2 C_2}$.

The input impedance will be:

$$z_{in} = \frac{\widetilde{V}_i}{\widetilde{I}_i} = \frac{\widetilde{V}_i}{(\widetilde{V}_i - \widetilde{V}_3)j\omega C_1} \quad (7)$$

The output impedance is calculated considering that the input source is off, is like a short-circuit. That results in no current in C_1 , R_1 and R_4 . R_5 is in parallel with the OP-AMP. Ideally, the OP-AMP has 0 output impedance, so the parallel with R_5 results in 0 impedance. So, in the end, what is left is the parallel of R_2 and C_2 . Consequently, the output impedance is:

$$z_{out} = \frac{1}{\frac{1}{R_2} + j\omega C_2} \quad (8)$$

With this analysis, the following results were obtained (gain and impedances were calculated for 1000 Hz):

Name	Value
f_c (Hz)	1023.086723
$gain(1000 \text{ Hz})$ (dB)	36.562591
z_{in}	1000.000000 + i (-723.431560)
z_{out}	338.366226 + i (-233.861947)

Table 6: Results

The frequency response was also calculated and the following was obtained:

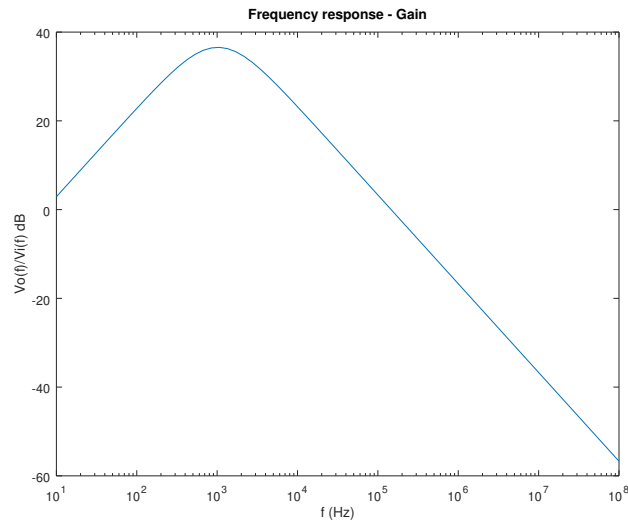


Figure 8: Frequency response - Gain

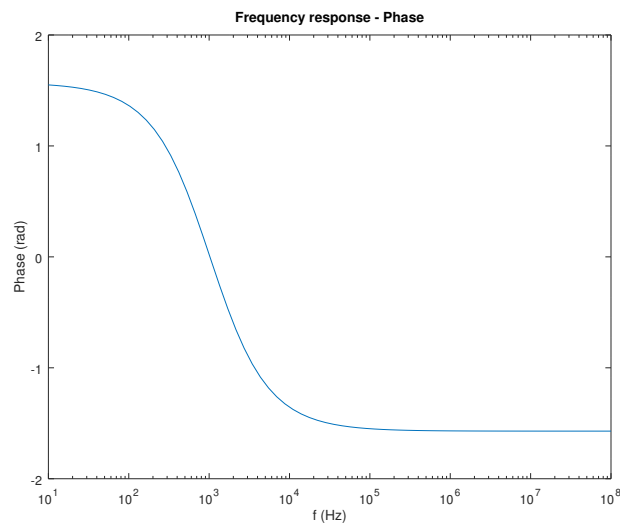


Figure 9: Frequency response - Phase

4.2 Optimized circuit

For the optimized circuit, the following results were calculated:

Name	Value
f_c (Hz)	1023.086723
$gain(1000\text{ Hz})$ (dB)	36.982281
z_{in}	1000.000000 + i (-723.431560)
z_{out}	676.732451 + i (-467.723894)

Table 7: Results

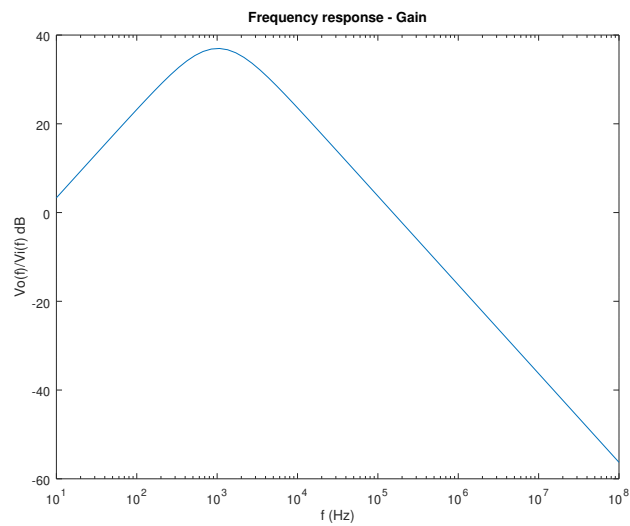


Figure 10: Frequency response - Gain

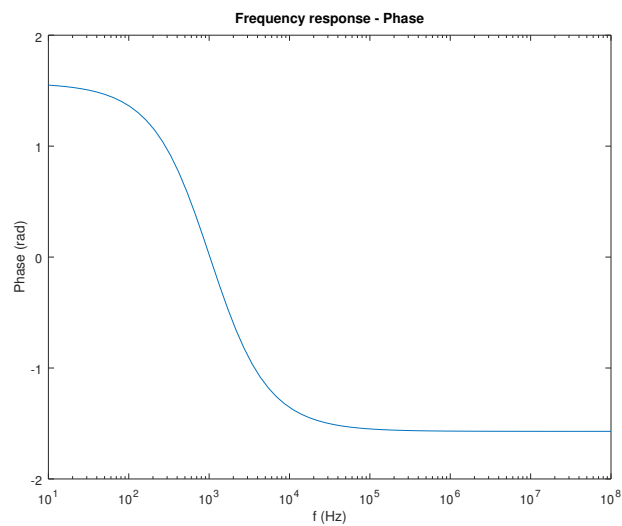


Figure 11: Frequency response - Phase

5 Conclusion

This laboratory assignment had as the main objective the construction and analysis of a band-pass filter, using an OP-AMP. In the laboratory, the circuit presented in figure 1 was analysed. After, this circuit was simulated using **Ngspice** and analysed theoretically using **Octave**. In the following graph, the results obtained in the lab and the ones obtained with **Ngspice** and **Octave** are presented.

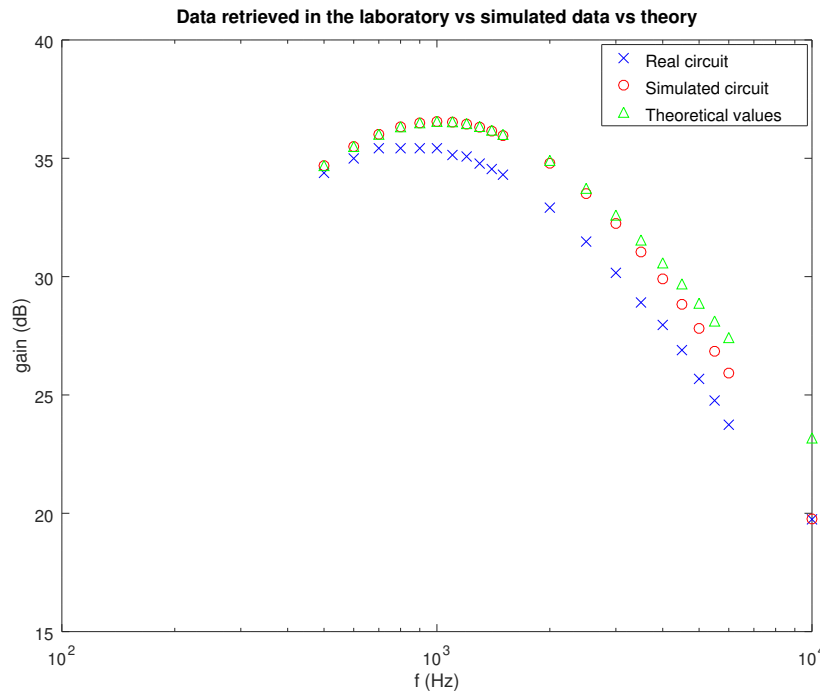


Figure 12: Data comparison

As it was expected, the different results show some deviations. Some possible reasons for the deviations of the experimental points were already given in Section 3. The deviations between the simulation and the theory are probably the consequence of the different model used for the OP-AMP. **Ngspice** uses a quite complex model. On the contrary, for the theoretical analysis a very simple model was considered, in which the OP-AMP is ideal. As expected, the model used by **Ngspice** is more realistic, so the simulated points come closer to the experimental points than the theoretical ones.

In the next table, the results for the simulation and the theory regarding the central frequency, the gain and the impedances are compared:

V	V_t	V_s	$ V_t - V_s $	$Error(\%)$
$f_c \text{ (Hz)}$	1023.086723	1004.39870562	20	2
$gain(1000 \text{ Hz}) \text{ (V)}$	36.562591	36.55104	0.02	0.04
$Re(z_{in})$	1000.0	999.9928	0.008	0.0008
$Im(z_{in})$	-723.43156	-723.536	0.2	0.02
$Re(z_{out})$	338.366226	341.692	4	1
$Im(z_{out})$	-233.861947	-233.043	0.9	0.4

Table 8: Theoretical values (V_t) and Simulation values (V_s) (table produced with **Python**) - The absolute deviation and error presented here are rounded up to one significant digit, for ease of interpretation.

For the optimized circuit, the following results were obtained:

V	V_t	V_s	$ V_t - V_s $	$Error(\%)$
$f_c (Hz)$	1023.086723	1000.81634417	30	3
$gain(1000 Hz) (V)$	36.982281	36.94817	0.04	0.1
$Re(z_{in})$	1000.0	999.9899	0.02	0.002
$Im(z_{in})$	-723.43156	-723.539	0.2	0.02
$Re(z_{out})$	676.732451	680.2165	4	0.6
$Im(z_{out})$	-467.723894	-466.878	0.9	0.2

Table 9: Theoretical values (V_t) and Simulation values (V_s) (table produced with **Python**) - The absolute deviation and error presented here are rounded up to one significant digit, for ease of interpretation.

In both instances, there were slight deviations, as was expected. The second circuit, compared with the first one, allowed for a slightly higher gain and a central frequency closer to the goal. Furthermore, it complied with the component limitations imposed, and the first one did not.

The merit figure (for the optimized, second circuit) is: 0.000328984599428822 .

Regarding the impedances, the input impedance is higher than the output impedance as desired. However, specially in the second circuit, the output impedance is quite large. That would damage the signal if the circuit was connected to a load with lower impedance.

All in all, the objectives were met. With more time and a lengthier study, better results could be obtained, and other solutions implemented.