MIPS instructions codes

a. **Codes of the Opcode fields - IR(31 downto 26)**

    sw   =[101011]=43
    lw   =[100011]=35
    lui   =[001111]=15
    ori   =[001101]=13
    addi   =[001000]=8
    beq   =[000100]=4
    bne   =[000101]=5
    j   =[000010]=2
    jal   =[000011]=3
    R-type =[000000]=0

b. **Function field codes for RType instructions – IR(5 downto 0)**

    add   =[100000]=32
    sub   =[100010]=34
    and   =[100100]=36
    or   =[100101]=37
    xor   =[100110]=38
    slt   =[101010]=42
    jr   =[001000]=8

Rs, Rt and Rd fields have a 5 bit binary number of the register (0-31)