Here is the MIPS binary code you need to dis-assembly:

x"00400000" => x"20010001"

x"00400004" => x"20020002"

x"00400008" => x"20030003"

x"0040000C" => x"20040004"

x"00400010" => x"20050005"

x"00400014" => x"20060006"

x"00400018" => x"20070007"

x"0040001C" => x"20080008"

x"00400020" => x"20090009"

x"00400024" => x"200A000A"

x"00400028" => x"200B000B"

x"0040002C" => x"200C000C"

x"00400030" => x"200D000D"

x"00400034" => x"200E000E"

x"00400038" => x"200F000F"

x"0040003C" => x"20100010"

x"00400040" => x"20110001"

x"00400044" => x"20120002"

x"00400048" => x"20130004"

x"0040004C" => x"20140008"

x"00400050" => x"20150010"

x"00400054" => x"20160020"

x"00400058" => x"20170040"

x"0040005C" => x"20180080"

x"00400060" => x"20190100"

x"00400064" => x"201B0200"

x"00400068" => x"201C0400"

x"0040006C" => x"201D0800"

x"00400070" => x"201E1000"

x"00400074" => x"201F2000"

x"00400078" => x"02018020"

x"0040007C" => x"02028020"

x"00400080" => x"02038020"

x"00400084" => x"02048020"

x"00400088" => x"00000000"

x"0040008C" => x"00000000"

x"00400090" => x"00000000"

x"00400094" => x"00220820"

x"00400098" => x"00000000"

x"0040009C" => x"00000000"

x"004000A0" => x"00230820"

x"004000A4" => x"00000000"

x"004000A8" => x"00000000"

x"004000AC" => x"00810820"

x"004000B0" => x"00000000"

x"004000B4" => x"00000000"

x"004000B8" => x"00A10820"

x"004000BC" => x"00000000"

x"004000C0" => x"00000000"

x"004000C4" => x"00260820"

x"004000C8" => x"00000000"

x"004000CC" => x"00000000"

x"004000D0" => x"00E83820"

x"004000D4" => x"00000000"

x"004000D8" => x"00000000"

x"004000DC" => x"00E90820"

x"004000E0" => x"00000000"

x"004000E4" => x"00000000"

x"004000E8" => x"002A0820"

x"004000EC" => x"00000000"

x"004000F0" => x"00000000"

x"004000F4" => x"002B0820"

x"004000F8" => x"00000000"

x"004000FC" => x"00000000"

x"00400100" => x"002C0820"

x"00400104" => x"00000000"

x"00400108" => x"00000000"

x"0040010C" => x"002D0820"

x"00400110" => x"00000000"

x"00400114" => x"00000000"

x"00400118" => x"002E0820"

x"0040011C" => x"00000000"

x"00400120" => x"00000000"

x"00400124" => x"002F0820"

x"00400128" => x"00000000"

x"0040012C" => x"00000000"

x"00400130" => x"00008020"

x"00400134" => x"200F0003"

x"00400138" => x"00000000"

x"0040013C" => x"00000000"

x"00400140" => x"00000000"

x"00400144" => x"22100001"

x"00400148" = > x"00000000"

x"0040014C" => x"21EFFFFF"

x"00400150” = > x”11E00004"

x"00400154” = > x”00000000"

x"00400158” = > x”00000000"

x"0040015C” = > x”08100051"

x"00400160” = > x”00000000"

x"00400164” = > x”08100059"

x"00400168” = > x”00000000"

x"0040016C” = > x”00000000"

See questions below:

1. What does this code do?
2. Explain how this code tests the GPR\_file and ALU parts of a MIPS CPU. What is not covered?