

2IN70 - Track 2: Real-Time Architectures

Practical Training - Exercises 4 -

Today we will continue experimenting with the μ C/OS-II real-time operating system running on the Freescale EVB9S12XF512E board.

4.1 Limiting the time that interrupts are disabled

In this exercise you will gain insight into the consequences of non-interruptable task execution introduced by critical sections, and learn how to mitigate them. The Code-Warrior project for this exercise is in the directory `exercise4_1`.

1. In this exercise we continue with the program from the last exercises. The `main.c` and `ATD_driver.c` files are similar to the solution for `exercise3_2`¹. Compare these files with your own results from the last exercises. You are welcome to use your own `main.c` and `ATD_driver.c` implementations in this exercise, if you are convinced that they are working correctly.
2. (Optional) To avoid interference between the two tasks sharing the ATD converter, we have made the ATD conversion atomic by disabling interrupts inside the function `ATDReadChannel()`. **Using the simulator, measure the length of the critical section (expressed in CPU cycles and True Time), and describe how you measured it.**

Note: the interrupts disabled flag can be inspected in the Registers view in the debugger.

3. μ C/OS-II keeps track of time by means of a tick counter which is incremented every time the timer interrupt is handled. Disabling interrupts for a long time may lead to missed interrupts, and consequently wrong tick counter value (i.e. the tick counter not representing the actual time). Therefore, we would like to check whether interrupts are missed or not.

We can do it by comparing the tick count (returned by `OSTimeGet()`) before and after the conversion to see how many ticks were recorded during the conversion. Since both tasks use the ATD converter, we have a choice where to measure the ticks. **In which task would you count the ticks? Motivate your answer.**

4. **By how many ticks do you expect the before and after counts to differ if no interrupts are missed? Motivate your answer.**

Hint: You may assume that the length of the critical section is 2.151ms, or use the information you have obtained in Step 2. Consult Chapter 16 in [Labrosse, 2002] (available on the course website) for the specification of `OSTimeGet()`. Draw a timeline to motivate your answer.

5. The task that you have selected in step 3 should now be extended as follows: if ticks were missed then led D24 should be turned on, otherwise it should be turned off. Write on a piece of paper the new definition of the task.

¹The bit resolution of the ATD conversion has changed from 8 bits to 12 bits.

6. *Before you implement and run the program*, write down on a piece of paper how you expect the leds to behave and draw a timeline of the task execution.
7. **Implement the tasks specified in Step 5** and verify your prediction by running the program on the board.
8. **Describe the problem, propose one solution.**
Hint: in the current implementation of `ATDReadChannel()` atomicity was achieved by disabling the interrupts. Atomicity with respect to other tasks can be achieved differently.
9. **Implement your proposed solution from the previous step.**
10. Verify your implementation by running the program on the board.
11. (Optional) In step 8 you have identified a problem with disabling interrupts during sensor reading and proposed a solution. **What drawbacks does your solution have? Describe a situation illustrating the problem.**