1. Description

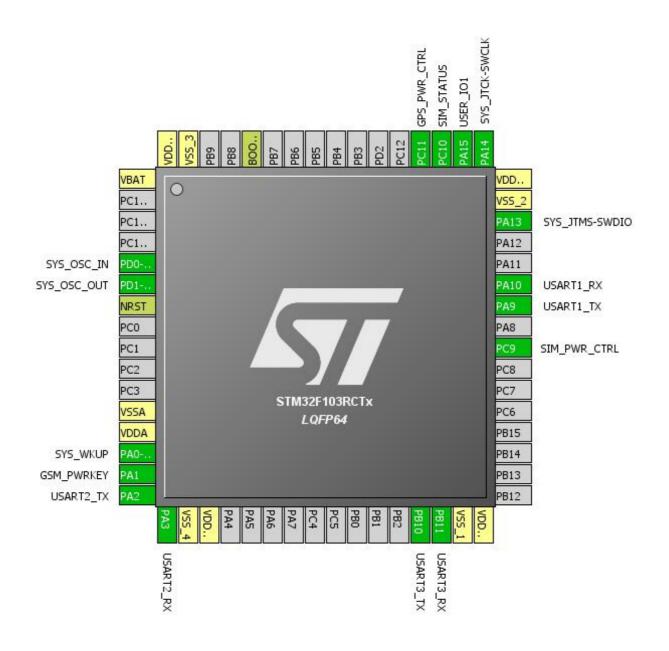
1.1. Project

Project Name	stm32gps_VB	
Generated with:	STM32CubeMX 4.2.0	
Date	11/16/2014	

1.2. MCU

MCU Serie	STM32F1
MCU Line	STM32F103
MCU name	STM32F103RCTx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration



3. IPs and Middlewares Configuration

IP	Mode Fonction		Pin
	Debug:	SYS_JTCK-SWCLK	PA14
	Serial-Wire	SYS_JTMS-SWDIO	PA13
SYS	OSC:	SYS_OSC_IN	PD0-OSC_IN
	HSE-External-Oscillator	SYS_OSC_OUT	PD1-OSC_OUT
	SYS-WKUP	SYS_WKUP	PA0-WKUP
USART1	Mode:	USART1_RX	PA10
	Asynchronous	USART1_TX	PA9
USART2	Mode:	USART2_RX	PA3
	Asynchronous	USART2_TX	PA2
USART3	Mode:	USART3_RX	PB11
	Asynchronous	USART3_TX	PB10

4. Pins Configuration

Pin	Pos	Function(s)	Label
PD0-OSC_IN	5	SYS_OSC_IN	
PD1-OSC_OUT	6	SYS_OSC_OUT	
PA0-WKUP	14	SYS_WKUP	
PA1 *	15	GPIO_Output	GSM_PWRKEY
PA2	16	USART2_TX	
PA3	17	USART2_RX	
PB10	29	USART3_TX	
PB11	30	USART3_RX	
PC9 *	40	GPIO_Output	SIM_PWR_CTRL
PA9	42	USART1_TX	
PA10	43	USART1_RX	
PA13	46	SYS_JTMS-SWDIO	
PA14	49	SYS_JTCK-SWCLK	
PA15 *	50	GPIO_Input	USER_IO1
PC10 *	51	GPIO_Input	SIM_STATUS
PC11 *	52	GPIO_Output	GPS_PWR_CTRL

^{*} The pin is affected with an I/O function