Introduction

Yunho Kim Hanyang Univ.

Who Am I?

- Assistant professor at the Dept. of computer science, Hanyang Univ.
 - I am leading the SE lab
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- Research goal
 - Build better software with automation
- Research interests
 - Automated test generation
 - Automated debugging
 - Program analysis (static/dynamic)



Yunho Kim (김윤호)

Timetable

♦ I will teach OOP/OOAD/UML and design patterns in this week

Time	1/3	1/4	1/5	1/6	1/7
09~10	ОТ	UP	<u>UML</u>	Traditional	New
10~11	OOP Review	Elaboration	<u>Practice</u>	Patterns	Patterns
11~12					
12~13	Happy Lunch Time!				
13~14	00	UP	GoF Pattern	Traditional	<u>Pattern</u>
14~15	Modeling	Elaboration	Review	Patterns	<u>Practice</u>
15~16	UP Inception	UML			
16~17			Traditional	New	
17~18			Patterns	Patterns	

Practices

- ♦ We have two practices in this week
 - Each practice

1. UML Practices

Write UML diagrams for a given description of a system

2. Design pattern practices

 Write Java code using design patterns to implement a program for a given description of a system

Assumption

♦ You are familiar with the Java programming language

♦ You have basic knowledge of computer science