

Project Name : HUB 8735
Project PCB :
PCB VERSION : V02
Project Phase : DVT

Page Description

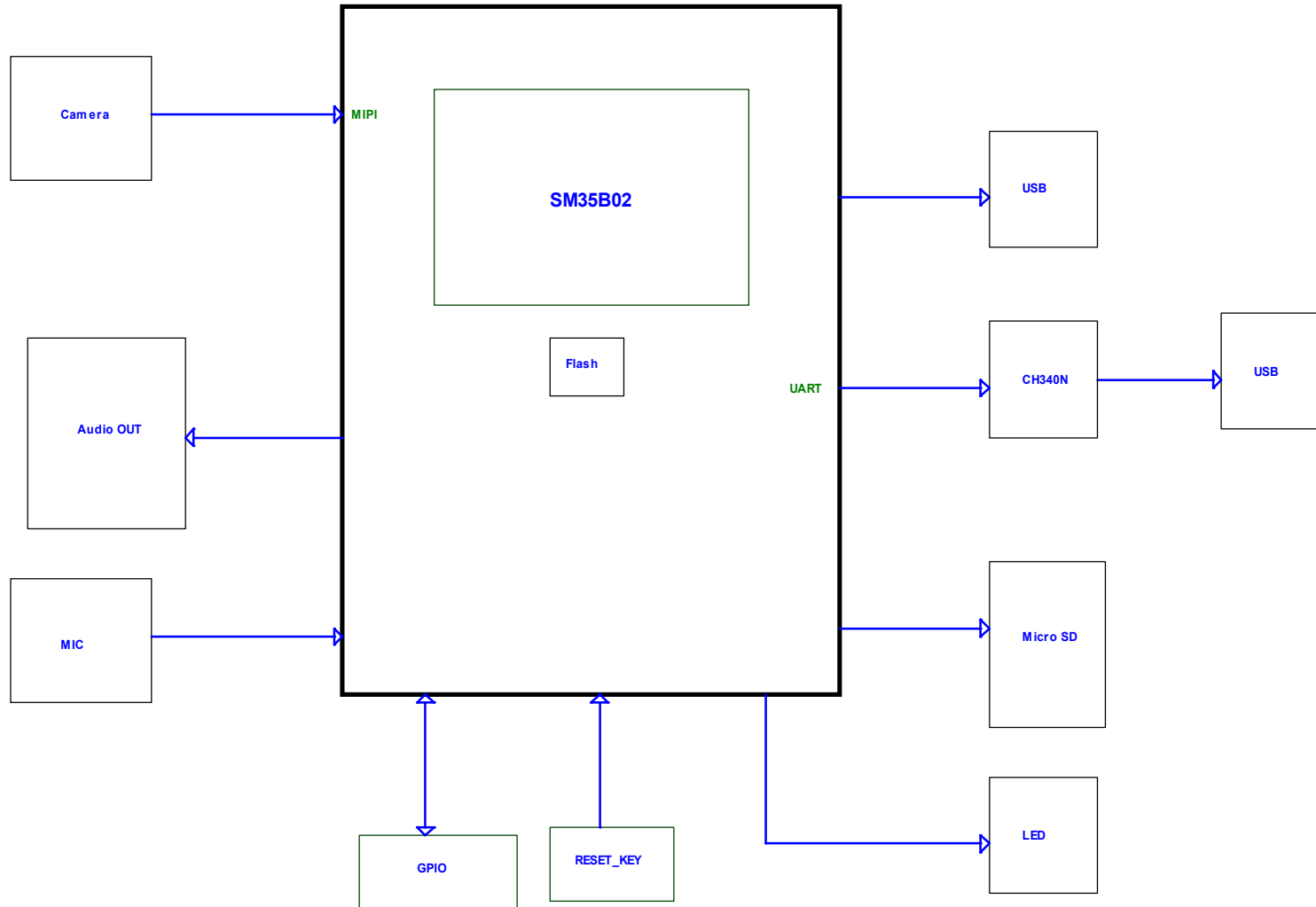
Revision History

REV. No.	DATE	PAGE	CONTENTS	FILE NAME	DEP.	RELEASED BY
01	2022-9-20	1-9	new design	Ameba_PCBV01_20220920.DSN		WINSTEC-RD-HW

- 01 COVER PAGE
- 02 SYSTEM BLOCK
- 03 POWER BLOCK
- 04 MODULE
- 05 CAMERA
- 06 Audio
- 07 MicroSD and IO
- 08 POWER
- 09 USB
- 10 EE-PIR-V01

HUB 8735 MAIN BOARD

- SYSTEM BLOCK DIAGRAM -

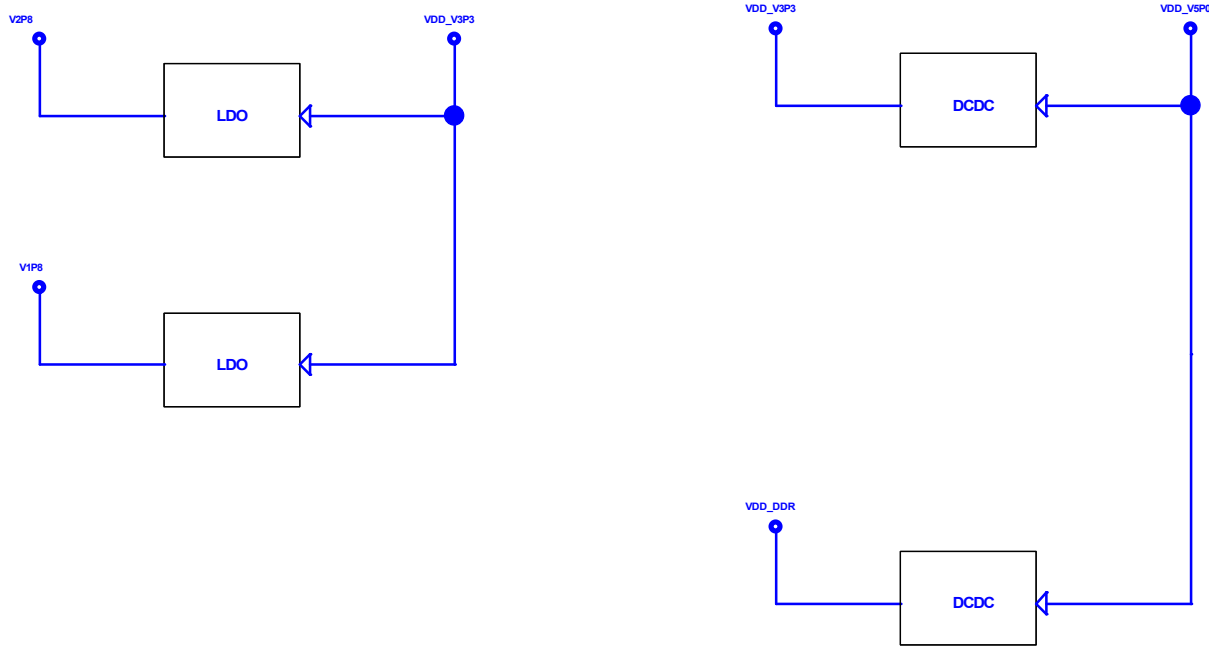


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HUB 8735 MAIN BOARD			
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A2	SYSTEM BLOCK	02	
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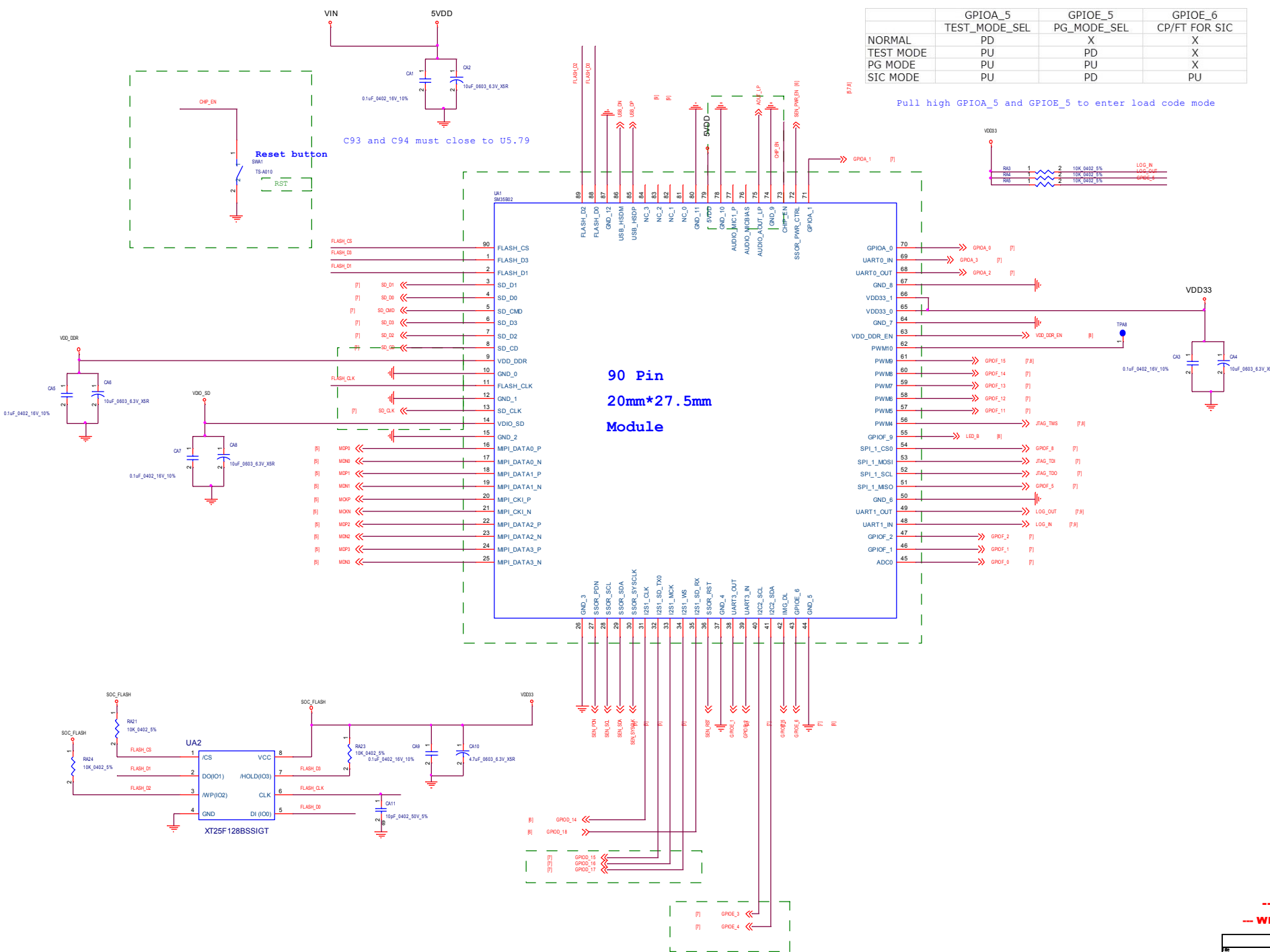
HUB 8735 MAIN BOARD

- POWER BLOCK -



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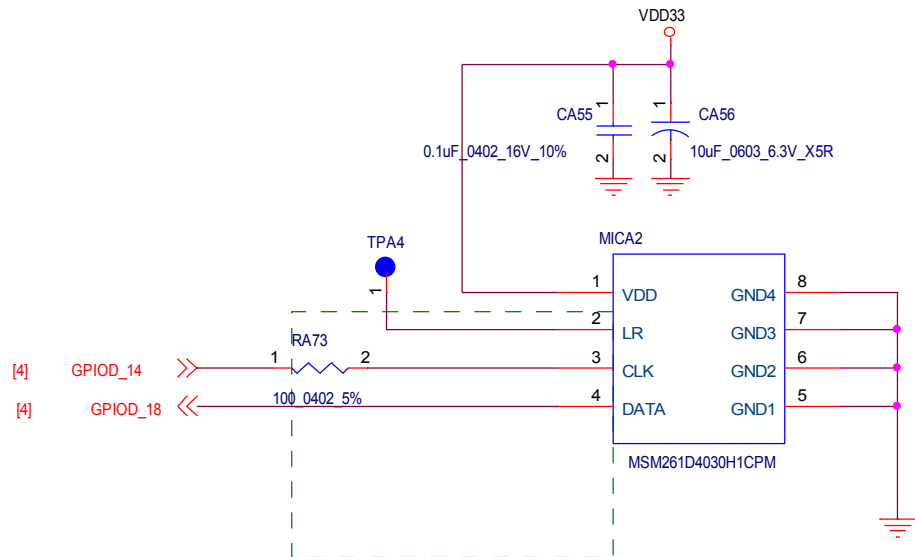
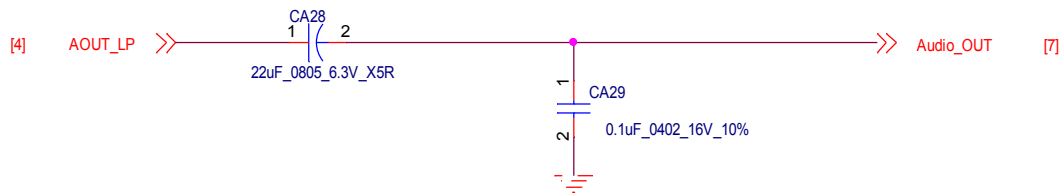
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	GPIOA_5	GPIOE_5	GPIOE_6
	TEST_MODE_SEL	PG_MODE_SEL	CP/FT FOR SIC
NORMAL	PD	X	X
TEST MODE	PU	PD	X
PG MODE	PU	PU	X
SIC MODE	PU	PD	PU

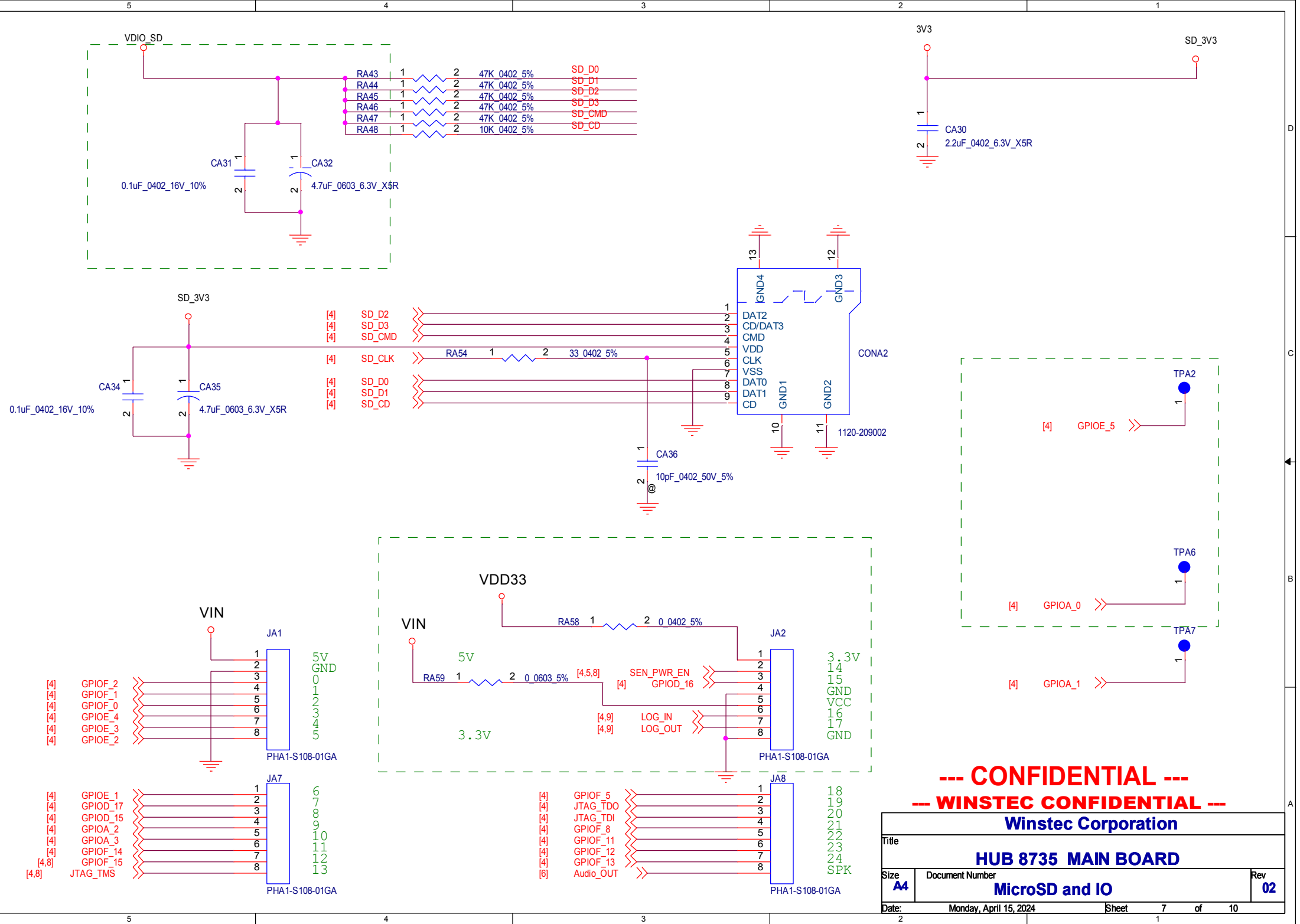
Pull high GPIOA_5 and GPIOE_5 to enter load code mode

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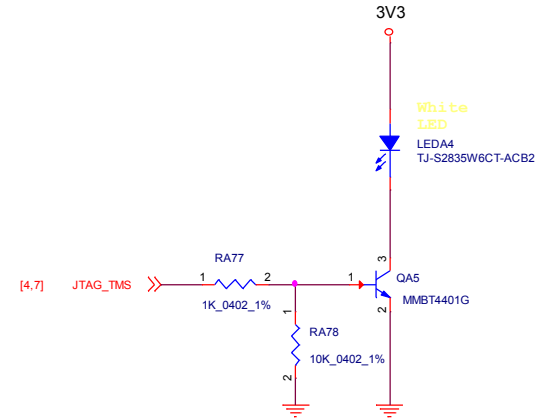
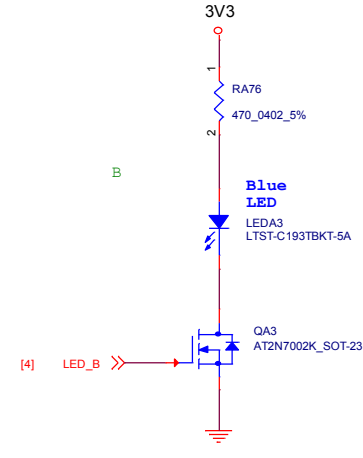
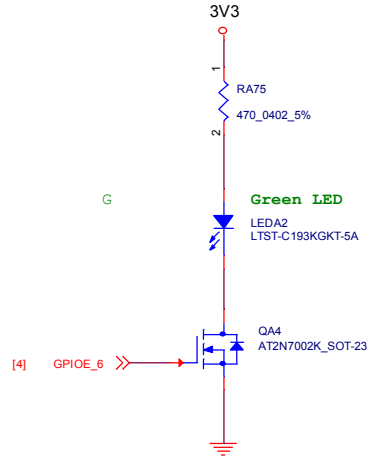
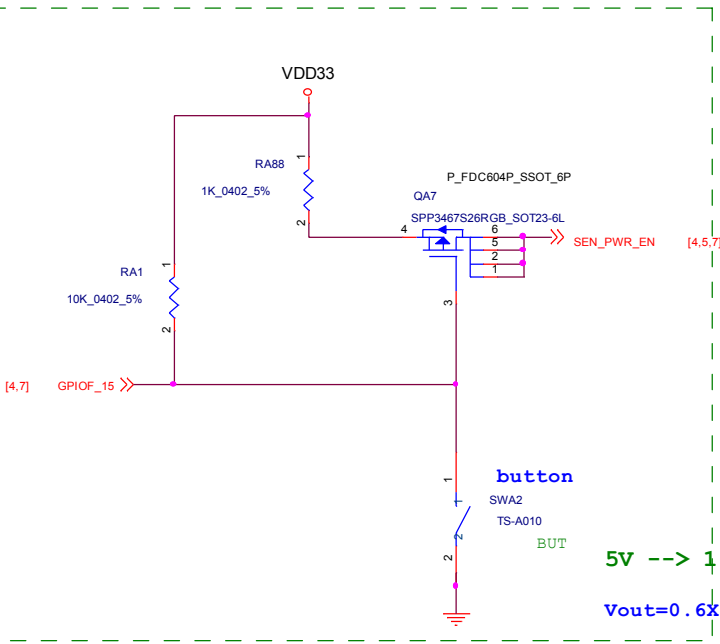
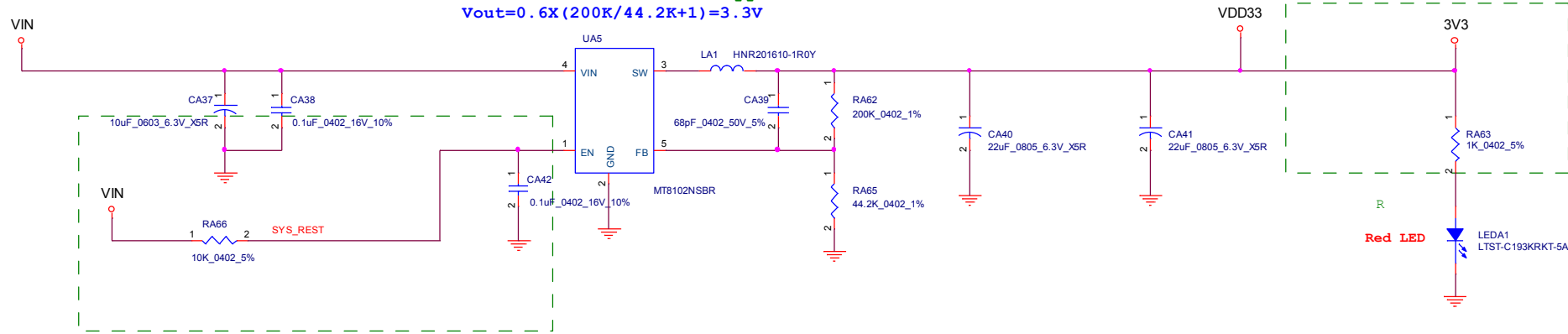
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Title			
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A4	Audio		02
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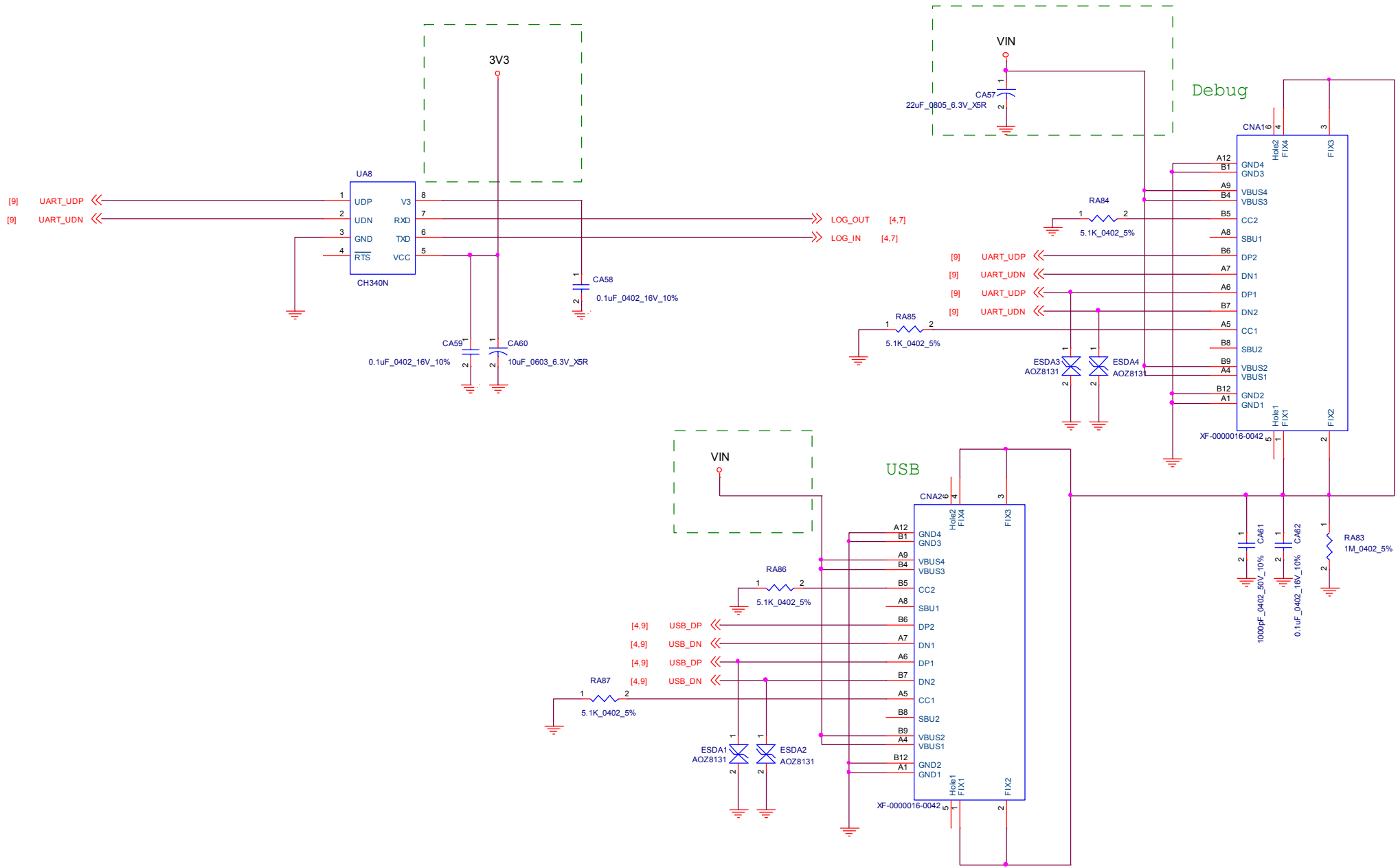
5V --> 3.3V for low cost application

$$V_{out} = 0.6 \times (200K / 44.2K + 1) = 3.3V$$



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HUB 8735 PIR of EE

Schematic-Rev	PCB-Rev	Date	Sheet	Contents	Reason	Remark
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