

Microprogrammed Control Unit

Control Memory → The one of functional unit of CPU in Control unit, whose function is to initiate sequences of microoperations. There are two methods of implementing Control unit :-

(1) Hardwired Control.

(2) Microprogrammed Control

1. Hardwired Control → When the Control signals are determined by hardware using conventional logic design techniques, then the Control unit is said to be hardwired.

Features :-

(a) Fixed instructions

(b) Fixed logic blocks of and/or arrays, encoder, decoder etc.

(c) Expensive relative complex high speed operation.

(d) No flexibility of adding new instructions.

e.g Intel 8085, Motorola 6802, Zilog 80, RISC CPUs.

(2) Microprogrammed Control → It is a Control unit whose binary Control variables are stored in the memory is called a microprogrammed Control.

e.g:- Intel 8080, Motorola 68000, CISC CPUs

* The Control function specifies a microoperation.

* The Control variables used in a Control function are binary and at any given time can be represented by a string of 0's and 1's called Control word.

* Each Control word in the Control memory contains within it a microinstruction.

* A microinstruction specifies one or more microoperations.

* A sequence of microinstructions is called a micro-program.

* The content of the word in ROM at any given point specifies the microinstruction.

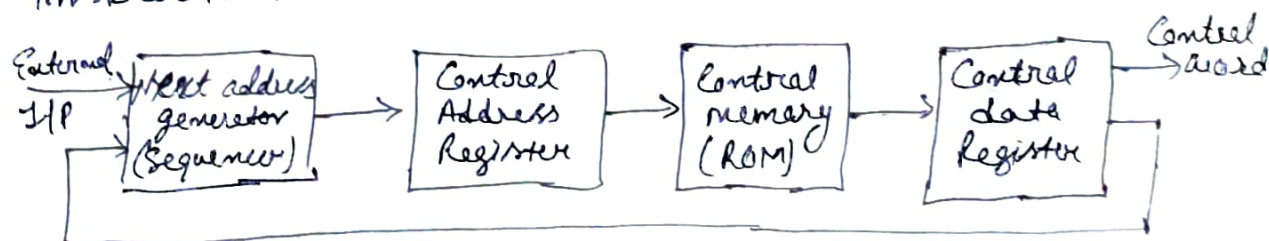
* The Control memory is read only & cannot be altered by programming because the ROM words are made permanent during the hardwired reduction of the unit.

Control Memory:- A memory i.e a part of the Control unit is referred to as control memory.

- * The Computer that employs micro-Programmed Control unit will have two separate units i.e a main memory & Control memory.
- * The main memory is used for storing the Programs for user.
- * The Content of main memory can be manipulated everytime the Program is changed i.e it consists of machine instructions & data.

But in contrast, the control memory hold a fixed micro-Programs which cannot be altered by the users. This micro-program consists of micro-instructions that specify various internal control signal for execution of register micro-operation.

Note:- Each machine instruction initiates the series of micro-instructions in the control memory. These micro-instructions generate the microoperations to fetch the instruction from the main memory. Evaluate the effective address to execute the operation specified by the instructions & to return control to fetch phase in order to repeat the cycle for the next instruction.



- * The Control memory address register specifies the address of microinstructions.
- * The Control data register holds the micro-instructions read from the memory.
- * The micro-Program Sequencer or Next address generator determines the address sequence that is read from control memory.
- * The micro-instruction contains a control word that specifies one or more micro-operations for the data processor. Once these operations are executed, the control must determine the next address.
- * The micro-instructions contains bits for initialising micro-operations in the data processor part & bit to determine the address sequence for control memory.
- * The Control data register holds the present micro-instructions while the next address is computed & read from the memory. So, data register is called Pipeline register.